

VOLUME A04 MACHINE 3705- -0015984 MODEL E08 SYSTEM 0004XBW MODE BOX SHIP 82/12/30

LOGIC TYPE -0- SYSTEMS DIAGRAMS

PAGE NUM	SH	TITLE	PART NUM	EC NUM	FEATURE B/M OR B/MS
PA000		CA TYPE 4	0001750516	321749	.W. 0001750043
PA001		CA TYPE 4	0001755103	318544	.W. 0001750043
PA002		CA TYPE 4	0001756448	321749	.W. 0001750043
PA011		CA TYPE 4	0001755000	314402	.W. 0001750043
PA012		CA TYPE 4	0001755001	316677	.W. 0001750043
PA013		CA TYPE 4	0001755002	318552	.W. 0001750043
PA014		CA TYPE 4	0001755003	314402	.W. 0001750043
PA015		CA TYPE 4	0001755004	314424	.W. 0001750043
PA016		CA TYPE 4	0001755005	314402	.W. 0001750043
PA017		CA TYPE 4	0001755006	314424	.W. 0001750043
PA018		CA TYPE 4	0001755007	318589	.W. 0001750043
PA020		CA TYPE 4	0001755008	314402	.W. 0001750043
PA048		CA TYPE 4	0001755104	316677	.W. 0001750043
PA049		CA TYPE 4	0001755105	317524	.W. 0001750043
PA050		CA TYPE 4	0001755106	318589	.W. 0001750043
PA051		CA TYPE 4	0001755107	316677	.W. 0001750043
PA052		CA TYPE 4	0001755108	316677	.W. 0001750043
PA053		CA TYPE 4	0001755109	318589	.W. 0001750043
PA054		CA TYPE 4	0001755110	316677	.W. 0001750043
PA055		CA TYPE 4	0001755116	316677	.W. 0001750043
PA056		CA TYPE 4	0001750419	316677	.W. 0001750043
PA060		CA TYPE 4	0001755111	316677	.W. 0001750043
PA061		CA TYPE 4	0001749376	316677	.W. 0001750043
PA101		CA TYPE 4	0001755009	316677	.W. 0001750043
PA102		CA TYPE 4	0001755010	315620	.W. 0001750043
PA103		CA TYPE 4	0001755011	321749	.W. 0001750043
PA104		CA TYPE 4	0001755012	316677	.W. 0001750043
PA105		CA TYPE 4	0001755013	314402	.W. 0001750043
PA106		CA TYPE 4	0001755014	316677	.W. 0001750043
PA107		CA TYPE 4	0001755015	316677	.W. 0001750043
PA108		CA TYPE 4	0001755016	316677	.W. 0001750043
PB101		CA TYPE 4	0001755017	314424	.W. 0001750043
PB102		CA TYPE 4	0001755018	314402	.W. 0001750043
PB103		CA TYPE 4	0001755019	316677	.W. 0001750043
PB104		CA TYPE 4	0001755020	314402	.W. 0001750043
PB105		CA TYPE 4	0001755021	314402	.W. 0001750043
PB106		CA TYPE 4	0001755022	314402	.W. 0001750043
PB107		CA TYPE 4	0001755023	314402	.W. 0001750043
PC101		CA TYPE 4	0001755024	316677	.W. 0001750043
PC102		CA TYPE 4	0001755025	321749	.W. 0001750043
PC103		CA TYPE 4	0001755026	315620	.W. 0001750043
PC104		CA TYPE 4	0001755027	316677	.W. 0001750043

VOLUME A04 MACHINE 3705- -0015984 MODEL E08 SYSTEM 0004XBW MODE BOX SHIP 82/12/30

LOGIC TYPE -0- SYSTEMS DIAGRAMS

PAGE NUM	SH	TITLE	PART NUM	EC NUM	FEATURE B/M OR B/MS
PC105		CA TYPE 4	0001755028	316677	.W. 0001750043
PC106		CA TYPE 4	0001755029	321749	.W. 0001750043
PD101		CA TYPE 4	0001755030	314402	.W. 0001750043
PD102		CA TYPE 4	0001755031	314402	.W. 0001750043
PD103		CA TYPE 4	0001755032	314402	.W. 0001750043
PD104		CA TYPE 4	0001755033	314402	.W. 0001750043
PD105		CA TYPE 4	0001755034	314402	.W. 0001750043
PD106		CA TYPE 4	0001755035	314402	.W. 0001750043
PD107		CA TYPE 4	0001755036	314402	.W. 0001750043
PD108		CA TYPE 4	0001755037	315620	.W. 0001750043
PD109		CA TYPE 4	0001755038	314402	.W. 0001750043
PE101		CA TYPE 4	0001755039	314402	.W. 0001750043
PE102		CA TYPE 4	0001755040	314402	.W. 0001750043
PE103		CA TYPE 4	0001755041	314424	.W. 0001750043
PE104		CA TYPE 4	0001755042	314402	.W. 0001750043
PE105		CA TYPE 4	0001755043	314402	.W. 0001750043
PE106		CA TYPE 4	0001755044	314402	.W. 0001750043
PE107		CA TYPE 4	0001755045	318552	.W. 0001750043
PF101		CA TYPE 4	0001755046	314424	.W. 0001750043
PF102		CA TYPE 4	0001755047	314402	.W. 0001750043
PF103		CA TYPE 4	0001755048	314424	.W. 0001750043
PF104		CA TYPE 4	0001755049	314424	.W. 0001750043
PF105		CA TYPE 4	0001755050	314402	.W. 0001750043
PG101		CA TYPE 4	0001755051	314424	.W. 0001750043
PG102		CA TYPE 4	0001755052	314424	.W. 0001750043
PH101		CA TYPE 4	0001755053	316677	.W. 0001750043
PH102		CA TYPE 4	0001755054	316677	.W. 0001750043
PH103		CA TYPE 4	0001755055	316677	.W. 0001750043
PH104		CA TYPE 4	0001755056	316677	.W. 0001750043
PH105		CA TYPE 4	0001755057	316677	.W. 0001750043
PH106		CA TYPE 4	0001755058	316677	.W. 0001750043
PH107		CA TYPE 4	0001755059	316677	.W. 0001750043
PJ101		CA TYPE 4	0001755060	314402	.W. 0001750043
PJ102		CA TYPE 4	0001755061	314402	.W. 0001750043
PK101		CA TYPE 4	0001755062	316677	.W. 0001750043
PK102		CA TYPE 4	0001755063	316677	.W. 0001750043
PK103		CA TYPE 4	0001755064	316677	.W. 0001750043
PK104		CA TYPE 4	0001755065	316677	.W. 0001750043
PK105		CA TYPE 4	0001755066	316677	.W. 0001750043
PK106		CA TYPE 4	0001755067	316677	.W. 0001750043
PK107		CA TYPE 4	0001755068	316677	.W. 0001750043
PL101		CA TYPE 4	0001755069	316677	.W. 0001750043

VOLUME A04 MACHINE 3705- -0015984 MODEL E08 SYSTEM 0004XBW MODE

BOX SHIP 82/12/30

LOGIC TYPE -0- SYSTEMS DIAGRAMS

PAGE NUM	SH	TITLE	PART NUM	EC NUM	FEATURE B/M OR B/MS
PL102		CA TYPE 4	0001755070	318552	.W. 0001750043
PL103		CA TYPE 4	0001755071	316677	.W. 0001750043
PL104		CA TYPE 4	0001755072	318552	.W. 0001750043
PL105		CA TYPE 4	0001755073	315620	.W. 0001750043
PM101		CA TYPE 4	0001755074	318552	.W. 0001750043
PM102		CA TYPE 4	0001755075	315620	.W. 0001750043
PM103		CA TYPE 4	0001755076	316677	.W. 0001750043
PM104		CA TYPE 4	0001755077	316677	.W. 0001750043
PP101		CA TYPE 4	0001755078	316677	.W. 0001750043
PP102		CA TYPE 4	0001755079	315620	.W. 0001750043
PP103		CA TYPE 4	0001755080	318589	.W. 0001750043
PP104		CA TYPE 4	0001755081	318589	.W. 0001750043
PP105		CA TYPE 4	0001755082	316677	.W. 0001750043
PP106		CA TYPE 4	0001755083	318552	.W. 0001750043
PQ101		CA TYPE 4	0001755084	318552	.W. 0001750043
PQ102		CA TYPE 4	0001755085	318552	.W. 0001750043
PQ103		CA TYPE 4	0001755086	316677	.W. 0001750043
PQ104		CA TYPE 4	0001755087	318552	.W. 0001750043
PQ105		CA TYPE 4	0001755088	316677	.W. 0001750043
PQ106		CA TYPE 4	0001755089	318552	.W. 0001750043
PR101		CA TYPE 4	0001755090	314402	.W. 0001750043
PR102		CA TYPE 4	0001755091	314402	.W. 0001750043
PR103		CA TYPE 4	0001755092	314402	.W. 0001750043
PR104		CA TYPE 4	0001755093	314402	.W. 0001750043
PR105		CA TYPE 4	0001755094	316677	.W. 0001750043
PR106		CA TYPE 4	0001755095	314402	.W. 0001750043
PS101		CA TYPE 4	0001755096	316677	.W. 0001750043
PS102		CA TYPE 4	0001755097	316677	.W. 0001750043
PS103		CA TYPE 4	0001755098	316677	.W. 0001750043
PS104		CA TYPE 4	0001755099	316677	.W. 0001750043
PS105		CA TYPE 4	0001755100	316677	.W. 0001750043
PS106		CA TYPE 4	0001755101	316677	.W. 0001750043
PS107		CA TYPE 4	0001755102	316677	.W. 0001750043

TOTAL PART NUMBERS THIS VOLUME

117







PLUG LIST

PART NO	ACC	TYPE	SOCKETS	TOTAL
5863413		N869	T4	
8231672	TPS	6837	R2	
8231720		2325	R2	
8235431		7601	P2	
8238687		7602	L2	
8250116		AC05	J2	
8250140		CE24	D2	
8250143		CE27	S2	
8250143	TPS	CE27	S2	
8251993		2326	T2	
8251996		7603	K2	
8251997		AC09	F2	
8252011		CE29	T2	
8252016		CE28	T2	
8254562		CE25	E2	
8254593		CE26	N2	
		CONN		
		B2		
		B3		
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		B100		

SOCKET LISTING  
 DATE 08-21-79 MACH. 27RNB  
 LOG 569 BOARD 010-E4  
 PREV. ENGR. 01-09-78 31852  
 PRES. ENGR. 08-09-79 321749  
 P.N. 1750516  
 IBM CORP. SDD BLK.

00000000

1755103

B

TYPE 4 CHANNEL ADAPTER TERMINATOR CARD SOCKET LISTING

"THIS DOCUMENT IS THE PROPERTY OF IBM. ITS USE IS AUTHORIZED ONLY FOR RESPONDING TO A REQUEST FOR QUOTATION OR FOR THE PERFORMANCE OF WORK FOR IBM. ALL QUESTIONS MUST BE REFERRED TO THE IBM PURCHASING DEPARTMENT."

**IBM**

NAME	DATE	CHANGE NO	DATE	CHANGE NO
	FEB 76	314402		
	SEP79	318544		
DESIGN DJR	FEB76	SHT	OF	
DETAIL AWL	FEB76			
CHECK		CLASSIFICATION		
APPRO		MUST CONFORM TO ENG SPEC	DEVELOPMENT NO	LOGIC PG NO
				PA001
				<b>B</b>

A2	B4	D2	K2	R2
	SINGLE CARD 5862885 N885			
A3	B5	E2	L2	S2
	SINGLE CARD 5862884 N884			
A4	C2	F2	M2	T2
SINGLE CARD 5864672 NOTE 2				
A5	C3	G2	N2	U2
B2	C4	H2	P2	V2
	SINGLE CARD 5862884 N884			
B3	C5	J2	Q2	

PART NO.	ACC	TYPE	SOCKET	TOTAL	
5862885		N885	B4	01	
5862884		N884	B5	01	
5862884		N884	C4	01	
5864672		CF94	A4	01	NOTE 2

NOTES

- 1 THIS PLUG CHART IS TO BE USED ONLY TO INDICATE WHERE THE A4 BOARD TERMINATOR CARDS ARE LOCATED (THERE ARE NO TERMINATOR CARD LOGIC PAGES). THE TERMINATOR CARDS ARE LOCATED IN THE LAST OXA-A4 BOARD INSTALLED.
- 2 REQUIRED FOR MODELS B,C,D,J,K,L AND MODELS F,G,H WITH 900 NS RPQ.(BOARD REWORK IS REQUIRED TO PROVIDE -4 VOLTS. SEE YZ000 FOR REWORK)

SOCKET LISTING FOR TYPE 4 C.A. TERMINATOR CARDS

DATE FEB 76 MACH. 27RNB  
 BOARD OXA-A4  
 PREV. ENG. FEB76 314402  
 PRES. ENG. SEP79 318544  
 P/N 1755103  
 IBM CORP. SCD

PART NO  
1755103

LOGIC PG NO  
PA001

1755103

**B**

PA002

A2	B4	D2	K2	R2
A3	B5	E2 QUAD CARD 8251998 CE25 (PRESENT IF EC 318552 NOT INSTAL- LED)	L2	S2
A4	C2	F2	M2	T2
A5	C3	G2	N2 QUAD CARD 8252008 CE26 (PRESENT IF EC 321749 IS NOT INSTALLED)	U2
B2	C4	H2	P2	V2
B3	C5	J2	Q2	

PART NO.	ACC	TYPE	SOCKET	SUBSTITUTE P/N
8251998		CE25	E2	8254562
8252008		CE26	N2	8254593

CARD SUBSTITUTION CHART-SOCKET LIST ADDENDUM I

DATE FEB78 MACH 27RNB  
BOARD OIA-E4

PREV. ENGR. FEB 78 318552

PRES. ENGR. AUG 79 321749

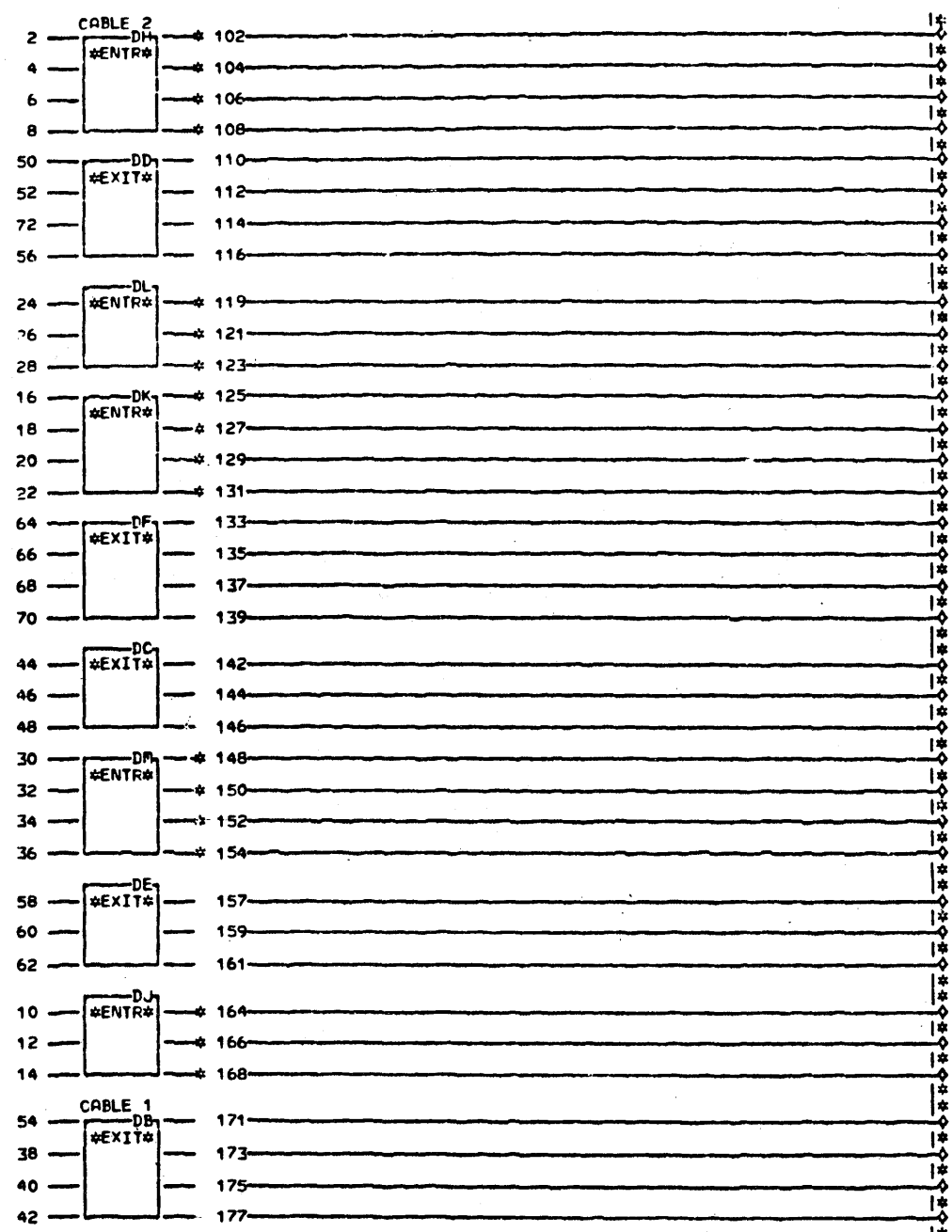
IBM CORP SCD

P/N 1756448

PA002

PA002

+ OUTBUS BIT 0.0 — AB002DH1- 2-  
 + OUTBUS BIT 0.0 — AB002DH3- 4-  
 + OUTBUS BIT 0.1 — AB002DH5- 6-  
 + OUTBUS BIT 0.2 — AB002DH7- 8-  
 + OUTBUS BIT 0.3 — AB002DJ2- 10-  
 + OUTBUS BIT 0.4 — AB002DJ4- 12-  
 + OUTBUS BIT 0.5 — AB002DJ6- 14-  
 + OUTBUS BIT 0.6 — AB002DK1- 16-  
 + OUTBUS BIT 0.7 — AB002DK3- 18-  
 + OUTBUS BIT 1.0 — AB002DK5- 20-  
 + OUTBUS BIT 1.0 — AB002DK7- 22-  
 + OUTBUS BIT 1.1 — AB002DL2- 24-  
 + OUTBUS BIT 1.2 — AB002DL4- 26-  
 + OUTBUS BIT 1.3 — AB002DL6- 28-  
 + OUTBUS BIT 1.4 — AB002DM1- 30-  
 + OUTBUS BIT 1.5 — AB002DM3- 32-  
 + OUTBUS BIT 1.6 — AB002DM5- 34-  
 + OUTBUS BIT 1.7 — AB002DM7- 36-  
 + INBUS BYTE 0 BIT 0 — PA106FB6- 38-  
 + INBUS BYTE 0 BIT 1 — PA106FC6- 40-  
 + INBUS BYTE 0 BIT 2 — PA106FD6- 42-  
 + INBUS BYTE 0 BIT 3 — PA106FE6- 44-  
 + INBUS BYTE 0 BIT 4 — PA106FF6- 46-  
 + INBUS BYTE 0 BIT 5 — PA106FG6- 48-  
 + INBUS BYTE 0 BIT 6 — PA106FH6- 50-  
 + INBUS BYTE 0 BIT 7 — PA106FJ6- 52-  
 + INBUS BYTE 0 BIT P — PA106GL4- 54-  
 + INBUS BYTE 1 BIT 0 — PA107FB6- 56-  
 + INBUS BYTE 1 BIT 1 — PA107FC6- 58-  
 + INBUS BYTE 1 BIT 2 — PA107FD6- 60-  
 + INBUS BYTE 1 BIT 3 — PA107FE6- 62-  
 + INBUS BYTE 1 BIT 4 — PA107FF6- 64-  
 + INBUS BYTE 1 BIT 5 — PA107FG6- 66-  
 + INBUS BYTE 1 BIT 6 — PA107FH6- 68-  
 + INBUS BYTE 1 BIT 7 — PA107FJ6- 70-  
 + INBUS BYTE 1 BIT P — PA107GL4- 72-



171 + INBUS BYTE 0 BIT P — AB001-DB1  
 173 + INBUS BYTE 0 BIT 0 — AB001-DB3  
 175 + INBUS BYTE 0 BIT 1 — AB001-DB5  
 177 + INBUS BYTE 0 BIT 2 — AB001-DB7  
 142 + INBUS BYTE 0 BIT 3 — AB001-DC2  
 144 + INBUS BYTE 0 BIT 4 — AB001-DC4  
 146 + INBUS BYTE 0 BIT 5 — AB001-DC6  
 110 + INBUS BYTE 0 BIT 6 — AB001-DD1  
 112 + INBUS BYTE 0 BIT 7 — AB001-DD3  
 114 + INBUS BYTE 1 BIT P — AB001-DM5  
 116 + INBUS BYTE 1 BIT 0 — AB001-DM7  
 157 + INBUS BYTE 1 BIT 1 — AB001-DE2  
 159 + INBUS BYTE 1 BIT 2 — AB001-DE4  
 161 + INBUS BYTE 1 BIT 3 — AB001-DE6  
 133 + INBUS BYTE 1 BIT 4 — AB001-DF1  
 135 + INBUS BYTE 1 BIT 5 — AB001-DF3  
 137 + INBUS BYTE 1 BIT 6 — AB001-DF5  
 139 + INBUS BYTE 1 BIT 7 — AB001-DF7  
 102 + OUTBUS BIT 0.0 — PK101-DH1  
 104 + OUTBUS BIT 0.0 — PK101-DH3  
 106 + OUTBUS BIT 0.1 — PK101-DH5  
 108 + OUTBUS BIT 0.2 — PK101-DH7  
 164 + OUTBUS BIT 0.3 — PK101-DJ2  
 166 + OUTBUS BIT 0.4 — PK101-DJ4  
 168 + OUTBUS BIT 0.5 — PK101-DJ6  
 125 + OUTBUS BIT 0.6 — PK101-DK1  
 127 + OUTBUS BIT 0.7 — PK101-DK3  
 129 + OUTBUS BIT 1.0 — PM101-DK5  
 131 + OUTBUS BIT 1.0 — PM101-DK7  
 119 + OUTBUS BIT 1.1 — PM101-DL2  
 121 + OUTBUS BIT 1.2 — PM101-DL4  
 123 + OUTBUS BIT 1.3 — PM101-DL6  
 148 + OUTBUS BIT 1.4 — PM101-DM1  
 150 + OUTBUS BIT 1.5 — PM101-DM3  
 152 + OUTBUS BIT 1.6 — PM101-DM5  
 154 + OUTBUS BIT 1.7 — PM101-DM7

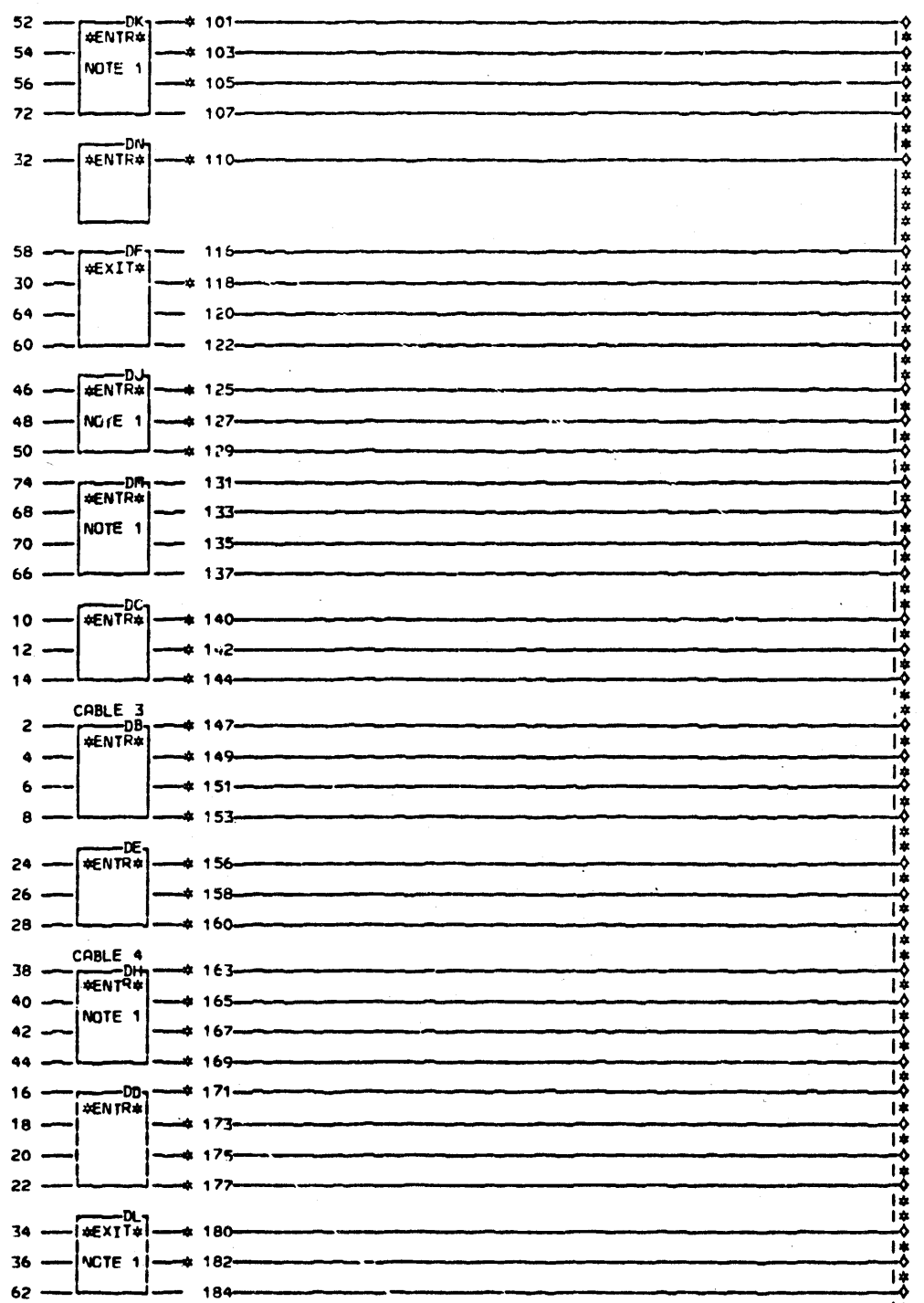
EDGE CO#	01A-E4B4D05	01A-E4B4D02	01A-E4B4D13
102 A-E4B2B02	121 A-E4B2D06	131 A-E4B2D03	164 A-E4B2B08
01A-E4B4B02	01A-E4B4D06	01A-E4B4D03	01A-E4B4B08
104 A-E4B2B04	123 A-E4B2D07	148 A-E4B2D09	166 A-E4B2B09
01A-E4B4B04	01A-E4B4D07	01A-E4B4D09	01A-E4B4B09
106 A-E4B2B05	125 A-E4B2B12	150 A-E4B2D10	168 A-E4B2B10
01A-E4B4B05	01A-E4B4B12	01A-E4B4D10	01A-E4B4B10
108 A-E4B2B06	127 A-E4B2B13	152 A-E4B2D11	
01A-E4B4B06	01A-E4B4B13	01A-E4B4D11	
119 A-E4B2D05	129 A-E4B2D02	154 A-E4B2D13	

PA011  
000

LOC. TYPE

ADAPTER INTERFACE	
E.C. HISTORY	C1 MACH. 27RNB
DATE	LAST EC
02-23-76	314402
IBM CORP. SDD	FRAME 01
P.N. 1755000	000

+ I-O REG ADDR BIT 0 — AB003DB1 — 2—  
 + I-O REG ADDR BIT 1 — AB003DB3 — 4—  
 + I-O REG ADDR BIT 2 — AB003DB5 — 6—  
 + I-O REG ADDR BIT 3 — AB003DB7 — 8—  
 + I-O REG ADDR BIT 4 — AB003DC2 — 10—  
 + I-O REG ADDR BIT 5 — AB003DC4 — 12—  
 + I-O REG ADDR BIT 6 — AB003DC6 — 14—  
 + I-O REG ADDR BIT 7 — AB003DD1 — 16—  
 + I-O REG ADDR BIT P — AB003DD3 — 18—  
 + SAMPLE OUTPUT DATA ON OUTBUS — AB003DD5 — 20—  
 + GATE INPUT DATA ON INBUS — AB003DD7 — 22—  
 + GATE INPUT 76 — AB003DE2 — 24—  
 - GATE INPUT 77 — AB003DE4 — 26—  
 + RESET — AB003DE6 — 28—  
 + BID LEVEL 2 INTERRUPT — AB003DF3 — 30—  
 + BID CSB 3 — AB004DN2 — 32—  
 + BID CHANNEL 2 — PA054DL2 — 34—  
 + BID FROM CA ON SAME BID LINE — PA054DL4 — 36—  
 + CHANNEL 1 ENABLE INTF A POS — PA061DH1 — 38—  
 + CHANNEL 2 ENABLE INTF A POS — PA061DH3 — 40—  
 + CHANNEL 1 DISABLE INTF A POS — PA061DH5 — 42—  
 + CHANNEL 2 DISABLE INTF A POS — PA061DH7 — 44—  
 + CHANNEL 1 ENABLE INTF B POS — PA061DJ2 — 46—  
 + CHANNEL 2 ENABLE INTF B POS — PA061DJ4 — 48—  
 + CHANNEL 1 DISABLE INTF B POS — PA061DJ6 — 50—  
 + CHANNEL 2 DISABLE INTF B POS — PA061DK1 — 52—  
 + CHANNEL 2 INTF A ENABLED — PA061DK3 — 54—  
 + CHANNEL 2 INTF B ENABLED — PA061DK5 — 56—  
 + BID LVL 1 INTERRUPT — PA102CL6 — 58—  
 + ADAPTER I-O ADDRESS DECODED — PA102ED6 — 60—  
 + CHANNEL 1 INTF A ENABLED — PB103FK2 — 62—  
 + BID L3 TO CCU — PM103DB6 — 64—  
 + ADBUS BIT X.7 — PP103DL2 — 66—  
 + ADBUS BIT X.P — PP103DN2 — 68—  
 + ADBUS BIT X.6 — PP103EM2 — 70—  
 + BID CHANNEL 1 — PQ101GA2 — 72—  
 + CHANNEL 1 INTF B ENABLED — PR105GD2 — 74—



000 PA012

147 + I-O REG ADDR BIT 0 — PA101-DB1  
 149 + I-O REG ADDR BIT 1 — PA101-DB3  
 151 + I-O REG ADDR BIT 2 — PA101-DB5  
 153 + I-O REG ADDR BIT 3 — PA101-DB7  
 140 + I-O REG ADDR BIT 4 — PA101-DC2  
 142 + I-O REG ADDR BIT 5 — PA101-DC4  
 144 + I-O REG ADDR BIT 6 — PA101-DC6  
 171 + I-O REG ADDR BIT 7 — PA101-DD1  
 173 + I-O REG ADDR BIT P — PA101-DD3  
 175 + SAMPLE OUTPUT DATA ON OUTBUS — DD5  
 PA104  
 177 + GATE INPUT DATA ON INBUS — DD7  
 PA104  
 156 - GATE INPUT 76 — PA102-DE2  
 158 - GATE INPUT 77 — PA104-DE4  
 160 + RESET — DE6  
 AB003 PA104  
 116 + BID PROGRAM LEVEL 1 — AB003-DF1  
 118 + BID PROGRAM LEVEL 2 — AB003-DF3  
 120 + BID PROGRAM LEVEL 3 — AB003-DF5  
 122 + ADAPTER I-O ADDRESS DECODED — DF7  
 AB003  
 163 + CHANNEL 1 ENABLE INTF A POS — DH1  
 PA103  
 165 + CHANNEL 2 ENABLE INTF A POS — DH3  
 AB004  
 167 + CHANNEL 1 DISABLE INTF A POS — DH5  
 PA103  
 169 + CHANNEL 2 DISABLE INTF A POS — DH7  
 AB004  
 125 + CHANNEL 1 ENABLE INTF B POS — DJ2  
 PA105  
 127 + CHANNEL 2 ENABLE INTF B POS — DJ4  
 AB004  
 129 + CHANNEL 1 DISABLE INTF B POS — DJ6  
 PA105  
 101 + CHANNEL 2 DISABLE INTF B POS — DK1  
 AB004  
 103 + CHANNEL 2 INTF A ENABLED — DK3  
 AB004  
 105 + CHANNEL 2 INTF B ENABLED — DK5  
 AB004  
 107 + BID CHANNEL 1 — PA054-DK7  
 180 + BID CHANNEL 2 — PA054-DL2  
 182 + BID FROM CA ON SAME BID LINE — DL4  
 PA101  
 184 + CHANNEL 1 INTF A ENABLED — DL6  
 PA061  
 131 + CHANNEL 1 INTF B ENABLED — DM1  
 PA061  
 133 + ADBUS BYTE X BIT P — AB004-DM3  
 135 + ADBUS BYTE X BIT 6 — AB004-DM5  
 137 + ADBUS BYTE X BIT 7 — AB004-DM7  
 110 + BID CSB 3 — AB004-DN2

NOTE 1 SEE PA061 PGS 102 AND 3 FOR ENABLE, DISABLE BOARD WIRING CHANGES IF MORE THAN 2 CA-45 ARE INSTALLED

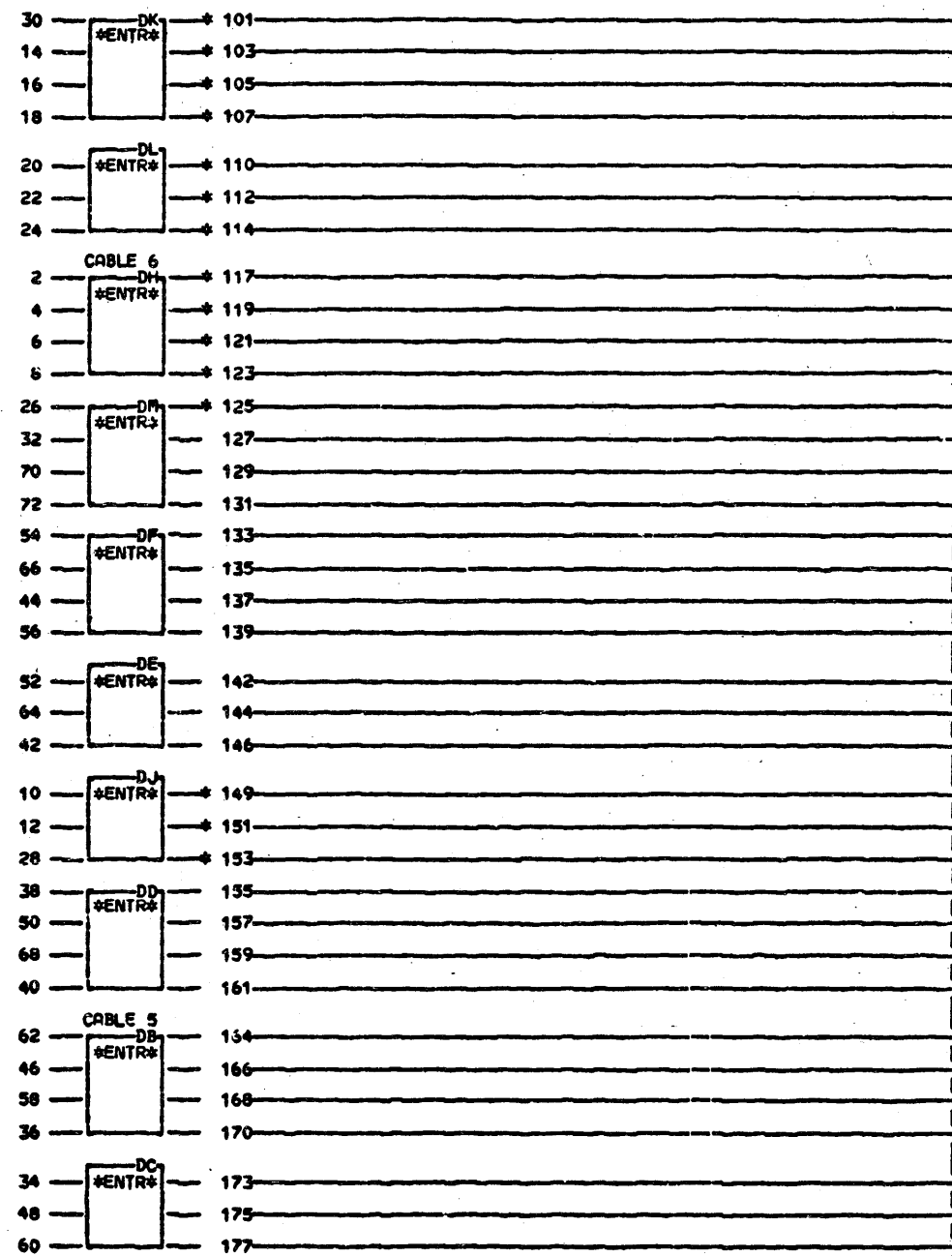
PA012  
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EDGE CONN.	01A-E4C4D11	01A-E4C4B09	01A-E4C4B06	01A-E4C5B02	01A-E4C4D02
101	A-E4C3B12	A-E4C3B08	A-E4C2B10	A-E4C3B05	A-E4C2D03
	A-E4C5B10	A-E4C5B09	A-E4C4B10	A-E4C4D05	A-E4C5B06
103	A-E4C3D02	A-E4C3B09	A-E4C2B22	A-E4C2D06	A-E4C3B06
	A-E4C5B13	A-E4C5B08	A-E4C4B02	A-E4C4D06	A-E4C5B05
105	A-E4C3D05	A-E4C3B10	A-E4C2B04	A-E4C2D07	A-E4C2B12
	A-E4C5D03	A-E4C5B12	A-E4C4B04	A-E4C4D07	A-E4C4B12
110	A-E4C3D09	A-E4C2B08	A-E4C2B05	A-E4C3B02	A-E4C2B13
	A-E4C5D09	A-E4C4B08	A-E4C4B05	A-E4C5B04	A-E4C4B13
118	A-E4C2D11	A-E4C2B09	A-E4C2B06	A-E4C3B04	A-E4C2D02

LOC. TYPE

ADAPTER INTERFACE	
E.C. HISTORY — 314402 314424	D. MACH. 27RN5 FRAME 01
DATE LAST EC 11-19-76 316677	IBM CORP. SDD PA012 P.N. 1755001 000

- ALLOW CHANNELS ON LINE AB006DH1- 2  
 - NOT INITIALIZED AB006DH3- 4  
 + POR OR RESET SW AB006DH5- 6  
 + INTERLOCK AB006DH7- 8  
 - T0 OR T1 TIME AB006DJ2- 10  
 - T1 OR T2 TIME AB006DJ4- 12  
 - BAD DATA AB006DK3- 14  
 - ADDRESS ERROR AB006DK5- 16  
 - SAR EVEN PARITY AB006DK7- 18  
 - GATE C.S. DATA ON INBUS AB006DL2- 20  
 - SAMPLE C.S. DATA ON OUTBUS AB006DL4- 22  
 + POWER ON RESET AB006DL6- 24  
 + GO CSB-3 AB006DM1- 26  
 + GO CHAN 1 PA054DJ6- 28  
 + GO CHAN 2 PA054DK1- 30  
 + CHAN IPL REQUEST PC104GH2- 32  
 + ADBUS BIT 0.3 PP103DC2- 34  
 + ADBUS BIT 0.2 PP103EB2- 36  
 + ADBUS BIT 0.6 PP103ED2- 38  
 + ADBUS BIT 1.0 PP103EF2- 40  
 + ADBUS BIT 1.3 PP103EH2- 42  
 + ADBUS BIT 1.6 PP103EK2- 44  
 + ADBUS BIT 0.0 PP103FA2- 46  
 + ADBUS BIT 0.4 PP103FC2- 48  
 + ADBUS BIT 0.7 PP103FE2- 50  
 + ADBUS BIT 1.1 PP103FG2- 52  
 + ADBUS BIT 1.4 PP103FJ2- 54  
 + ADBUS BIT 1.7 PP103FL2- 56  
 + ADBUS BIT 0.1 PP103GA2- 58  
 + ADBUS BIT 0.5 PP103GC2- 60  
 + ADBUS BIT 0.P PP103GE2- 62  
 + ADBUS BIT 1.2 PP103GG2- 64  
 + ADBUS BIT 1.5 PP103GJ2- 66  
 + ADBUS BIT 1.P PP103GL2- 68  
 + STORE BYTE 0 PQ102GB2- 70  
 + STORE BYTE 1 PQ102GC2- 72



000 PA013

164 + ADBUS BIT 0.P AB005-DB1  
 166 + ADBUS BIT 0.0 AB005-DB3  
 168 + ADBUS BIT 0.1 AB005-DB5  
 170 + ADBUS BIT 0.2 AB005-DB7  
 173 + ADBUS BIT 0.3 AB005-DC2  
 175 + ADBUS BIT 0.4 AB005-DC4  
 177 + ADBUS BIT 0.5 AB005-DC6  
 155 + ADBUS BIT 0.6 AB005-DD1  
 157 + ADBUS BIT 0.7 AB005-DD3  
 159 + ADBUS BIT 1.P AB005-DD5  
 161 + ADBUS BIT 1.0 AB005-DD7  
 142 + ADBUS BIT 1.1 AB005-DE2  
 144 + ADBUS BIT 1.2 AB005-DE4  
 146 + ADBUS BIT 1.3 AB005-DE6  
 133 + ADBUS BIT 1.4 AB005-DF1  
 135 + ADBUS BIT 1.5 AB005-DF3  
 137 + ADBUS BIT 1.6 AB005-DF5  
 139 + ADBUS BIT 1.7 AB005-DF7  
 117 - ALLOW CHANNELS ON LINE- PA104-DH1  
 119 - NOT INITIALIZED- PA104-DH3  
 121 + POR OR RESET SW DH5  
 PA107 PA104  
 123 + INTERLOCK PA106-DH7  
 149 + T2 OR T3 TIME A4 BOARD- PA103-DJ2  
 151 + T3 OR T0 TIME A4 BOARD- PA103-DJ4  
 153 + GO CHAN 1 PA101-DJ6  
 101 + GO CHAN 2 PA054-DK1  
 103 - BAD DATA PA105-DK3  
 105 - ADDRESS ERROR PA105-DK5  
 107 + SAR EVEN PARITY PA105-DK7  
 110 - GATE C.S. DATA ON INBUS PA101-DL2  
 112 - SAMPLE C.S. DATA ON OUTBUS DL4  
 PA106  
 114 + POWER ON RESET AB006-DL6  
 125 + GO CSB-3 AB006-DM1  
 127 + CHAN IPL REQUEST AB006-DM3  
 129 + STORE BYTE 0 AB006-DM5  
 131 + STORE BYTE 1 AB006-DM7

EDGE CONN.

101 A-E483B12	112 A-E483D06	123 A-E483B06
01A-E485B10	01A-E485D06	01A-E485B06
103 A-E483B13	114 A-E483D07	125 A-E483D09
01A-E485B13	01A-E485D07	01A-E485D09
105 A-E483D02	117 A-E483B02	149 A-E483B08
01A-E485D02	01A-E485B02	01A-E485B08
107 A-E483D03	119 A-E483B04	151 A-E483B09
01A-E485D03	01A-E485B04	01A-E485B09
110 A-E483D05	121 A-E483B05	153 A-E483B10

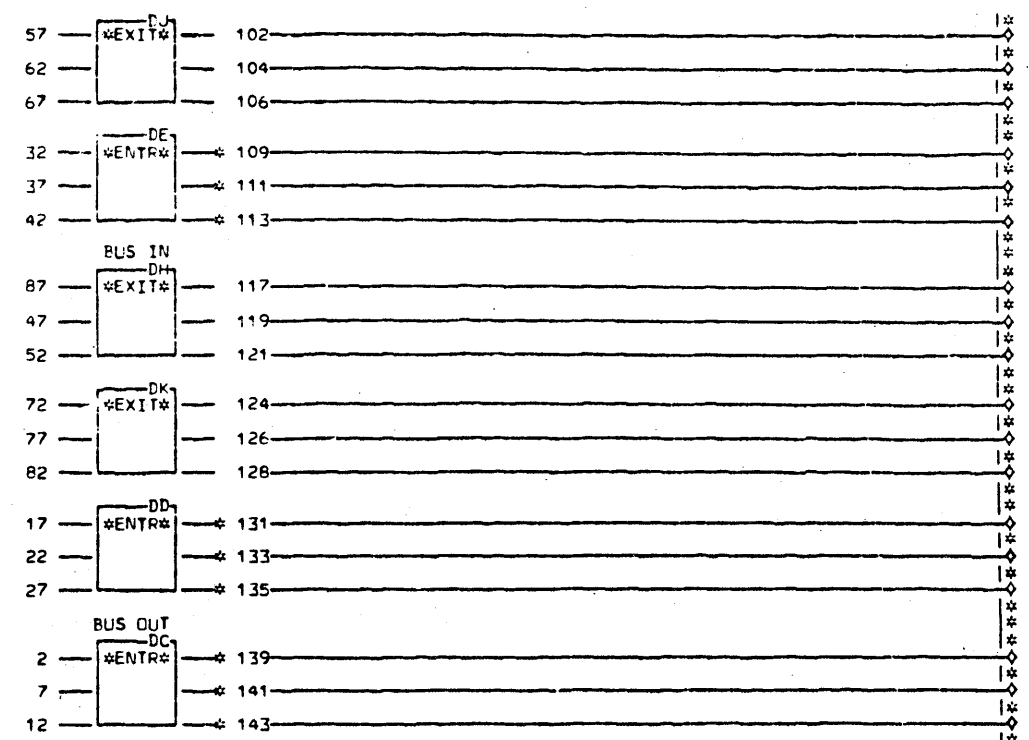
LOC. TYPE

PA013  
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ADAPTER INTERFACE	
E.C. HISTORY	E. MACH. 27RNB
314402	FRAME 01
314424	IBN CORP. SDD PA013
316677	P.N. 1755002 000
DATE LAST EC	
01-09-78 318552	

1755002

+ BUS OUT P — PA052DC2- 2-1  
 + BUS OUT 0 — PA052DC4- 7-1  
 + BUS OUT 1 — PA052DC6- 12-1  
 + BUS OUT 2 — PA052DD2- 17-1  
 + BUS OUT 3 — PA052DD4- 22-1  
 + BUS OUT 4 — PA052DD6- 27-1  
 + BUS OUT 5 — PA052DE2- 32-1  
 + BUS OUT 6 — PA052DE4- 37-1  
 + BUS OUT 7 — PA052DE6- 42-1  
  
 + NPL BIT 0 TO INTF A — PH101EC4- 47-1  
 + NPL BIT 1 TO INTF A — PH101EG4- 52-1  
 + NPL BIT 2 TO INTF A — PH101EL4- 57-1  
 + NPL BIT 3 TO INTF A — PH102EC4- 62-1  
 + NPL BIT 4 TO INTF A — PH102EG4- 67-1  
 + NPL BIT 5 TO INTF A — PH102EL4- 72-1  
 + NPL BIT 6 TO INTF A — PH103EC4- 77-1  
 + NPL BIT 7 TO INTF A — PH103EG4- 82-1  
 + NPL BIT P TO INTF A — PH103EL4- 87-1



000 PA014

139 + INTF A NPL BUS OUT BIT P — DC2  
 4PB103  
 141 + INTF A NPL BUS OUT BIT 0 — DC4  
 4PB102  
 143 + INTF A NPL BUS OUT BIT 1 — DC6  
 4PB102  
 131 + INTF A NPL BUS OUT BIT 2 — DD2  
 4PB102  
 133 + INTF A NPL BUS OUT BIT 3 — DD4  
 4PB102  
 135 + INTF A NPL BUS OUT BIT 4 — DD6  
 4PB102  
 109 + INTF A NPL BUS OUT BIT 5 — DE2  
 4PB102  
 111 + INTF A NPL BUS OUT BIT 6 — DE4  
 4PB102  
 113 + INTF A NPL BUS OUT BIT 7 — DE6  
 4PB102  
  
 117 + BUS IN P — PA052-DH2  
 119 + BUS IN 0 — PA052-DH4  
 121 + BUS IN 1 — PA052-DH6  
 102 + BUS IN 2 — PA052-DJ2  
 104 + BUS IN 3 — PA052-DJ4  
 106 + BUS IN 4 — PA052-DJ6  
 124 + BUS IN 5 — PA052-DK2  
 126 + BUS IN 6 — PA052-DK4  
 128 + BUS IN 7 — PA052-DK6

NOTE 1 SEE PAGE PA052 FOR A COMPLETE DESCRIPTION OF THE LOGIC BOARD TO TAILGATE FLAT CABLE  
 NOTE 2 ALL 01T TAILGATE PIN NOMENCLATURE IS THAT OF THE FLAT CABLE END— NOT NECESSARILY WHAT IS PRINTED ON THE TAILGATE CONNECTOR

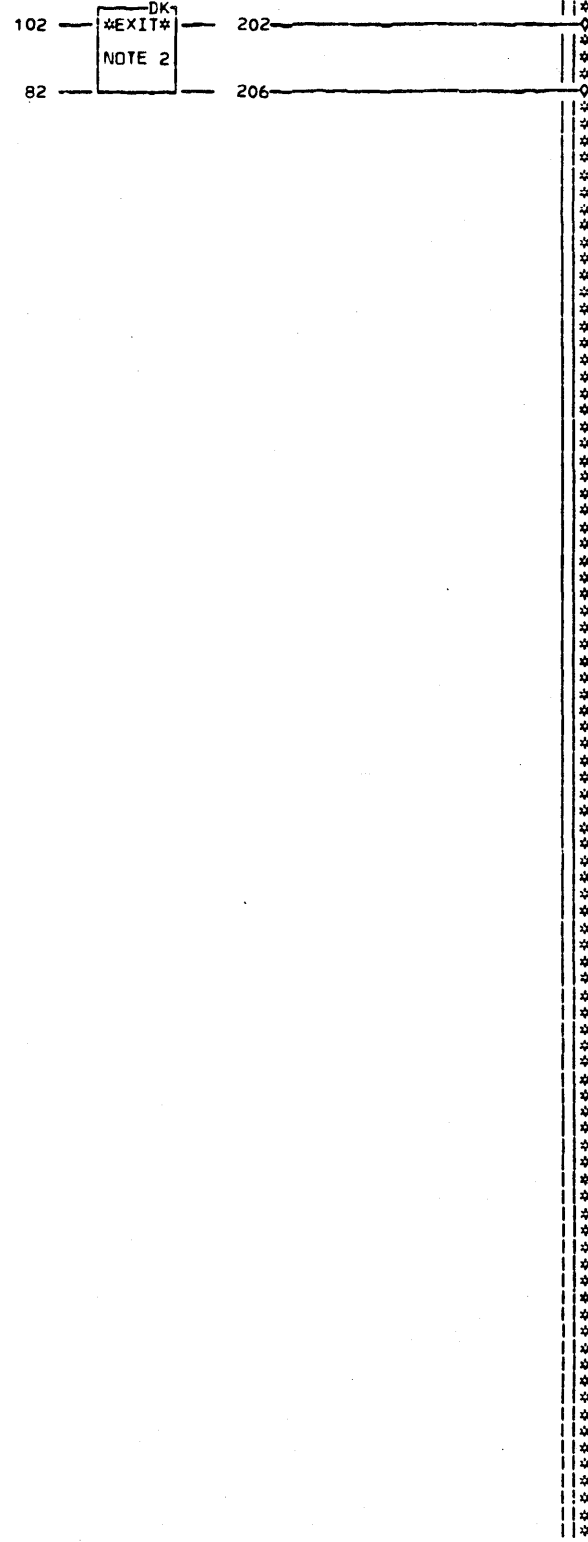
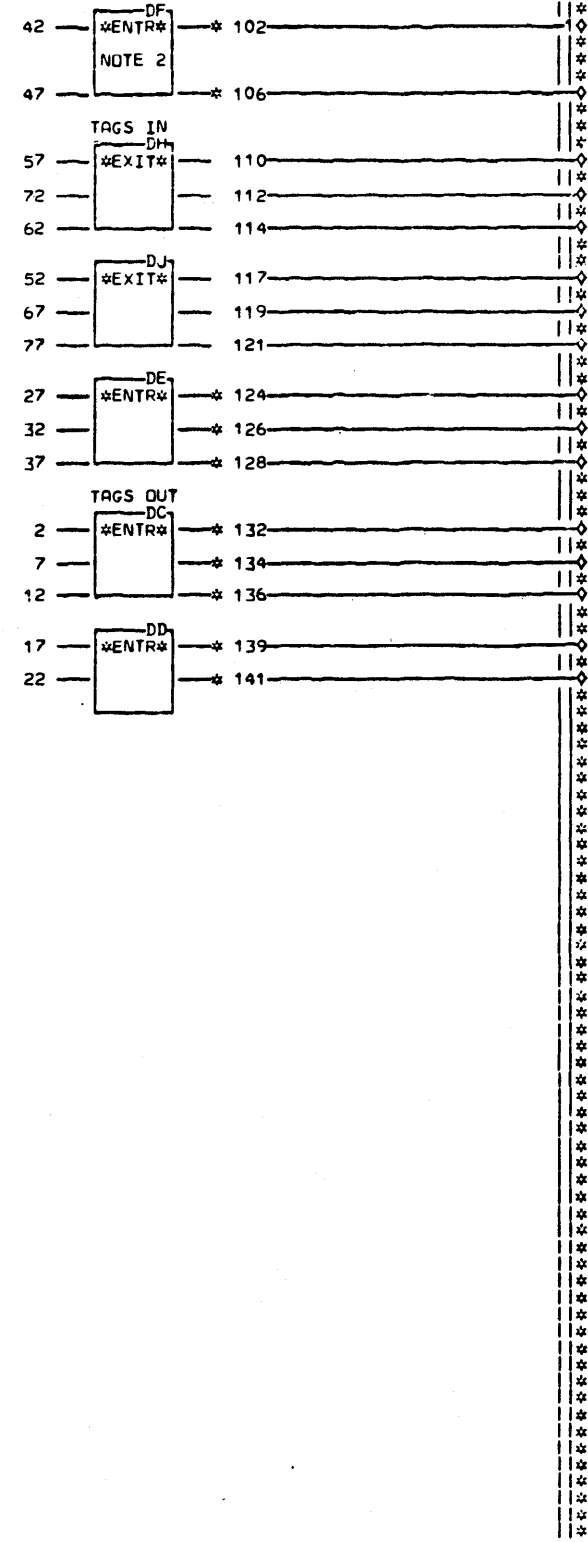
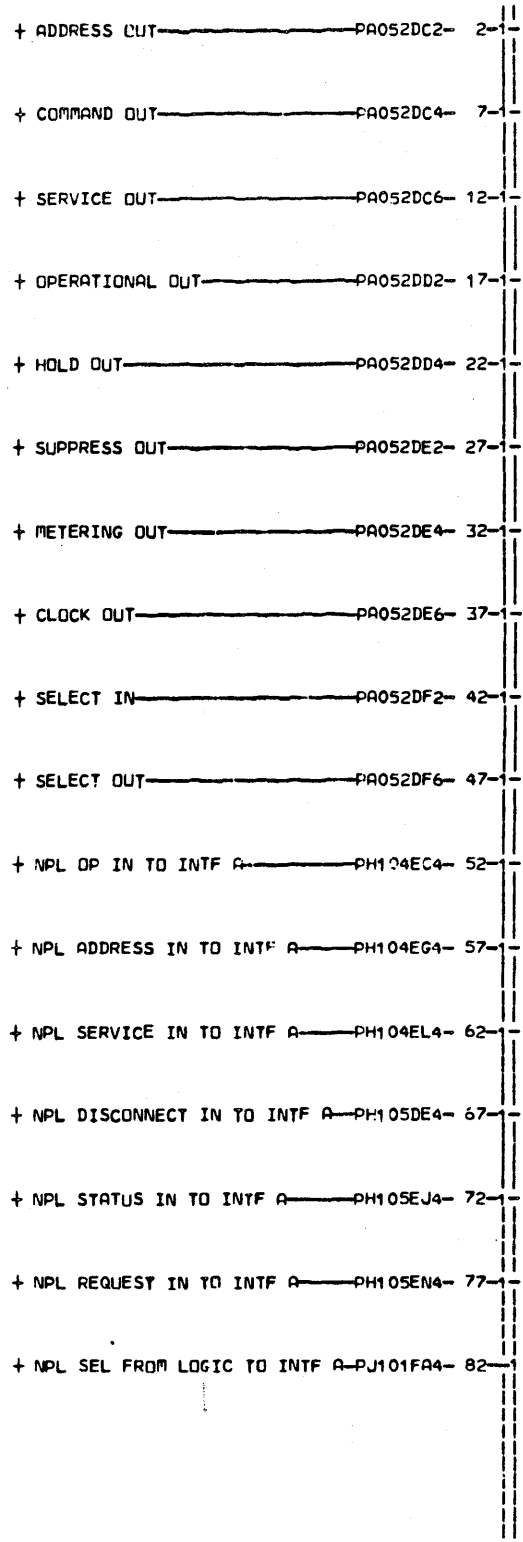
PA014  
 000

EDGE CONN.	131 A-E4V2D06	01T-A1A1D09
109 A-E4V2B10	01T-A1A2B06	139 A-E4V2B02
01T-A1A2D10	01T-A1A1D06	01T-A1A2D03
01T-A1A1B10	133 A-E4V2B08	01T-A1A1E03
111 A-E4V2D11	01A-E4V2E09	141 A-E4V2U05
01T-A1A2B11	01T-A1A2D08	01T-A1A2B04
01T-A1A1D11	01T-A1A1B08	01T-A1A1D04
113 A-E4V2B12	135 A-E4V2D09	143 A-E4V2B05
01T-A1A2D12	01A-E4V2D10	01T-A1A2D05
01T-A1A1B12	01T-A1A2B09	01T-A1A1B05

LOC. TYPE

TYPE 4 CHANNEL ADAPTER		CHANNEL INTERFACE A BUS	
E.C. HISTORY		C. PACH. 27RNB	
DATE	LAST EC	FRAME	01
02-23-76	314402	IBM CORP. SDD	PA014
		P.N. 1755003	000





- 000 PA015
- 132 + INTF A NPL ADDRESS OUT- PB101-DC2
  - 134 + INTF A NPL COMMAND OUT- PB101-DC4
  - 136 + INTF A NPL SERVICE OUT- PB101-DC6
  - 139 + INTF A NPL OPERATIONAL OUT- DD2  
PB101
  - 141 + INTF A NPL HOLD OUT- PB101-DD4
  - 124 + INTF A NPL SUPPRESS OUT PB101-DE2
  - 126 + INTF A NPL METERING OUT PA052-DE4
  - 128 + INTF A NPL CLOCK OUT- PA052-DE6
  - 102 + INTF A NPL SEL BYPASSED- DF2
  - 106 + INTF A NPL SEL TO LOGIC- DF6  
PB101 PJ101
  - 110 + ADDRESS IN- PA052-DH2
  - 112 + STATUS IN- PA052-DH4
  - 114 + SERVICE IN- PA052-DH6
  - 117 + OPERATIONAL IN- PA052-DJ2
  - 119 + DISCONNECT IN- PA052-DJ4
  - 121 + REQUEST IN- PA052-DJ6
  - 202 + SELECT IN- PA052-DK2
  - 206 + SELECT OUT- PA052-DK6

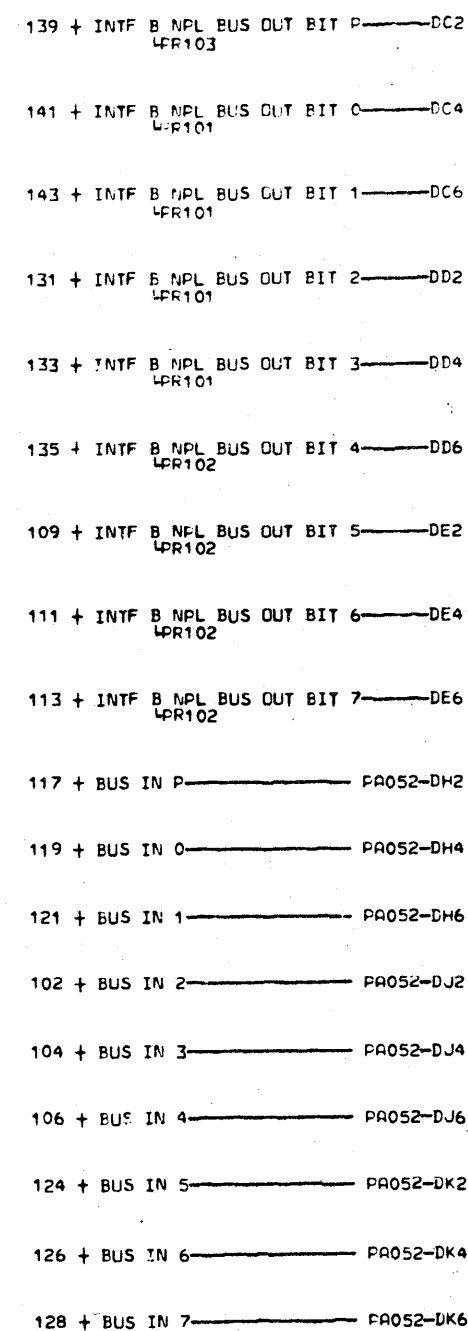
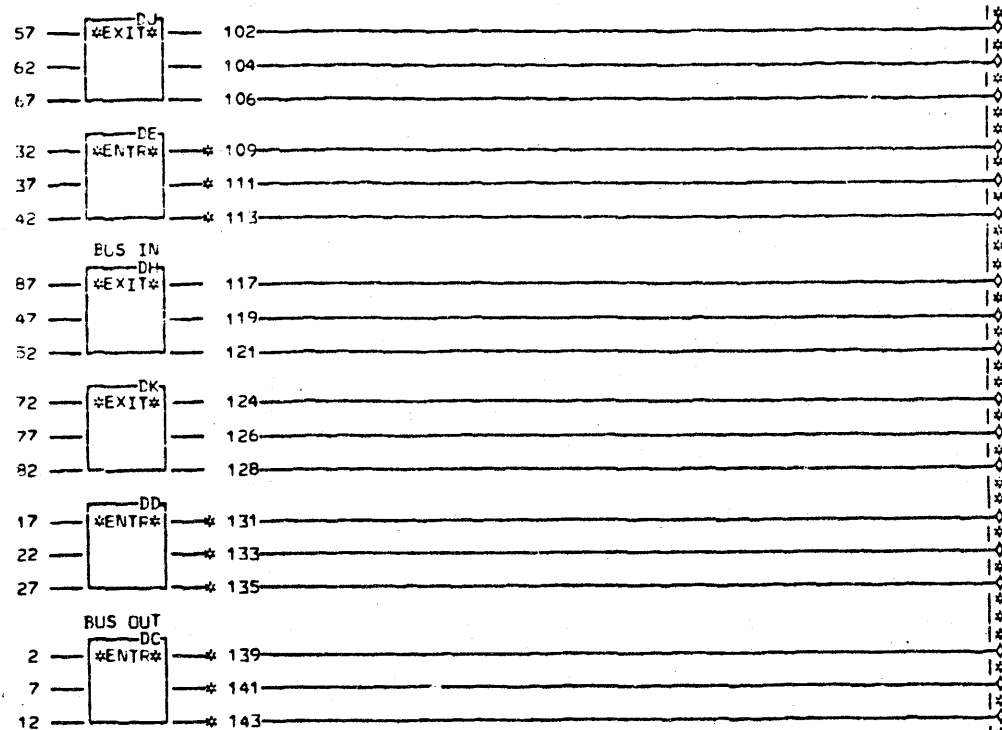
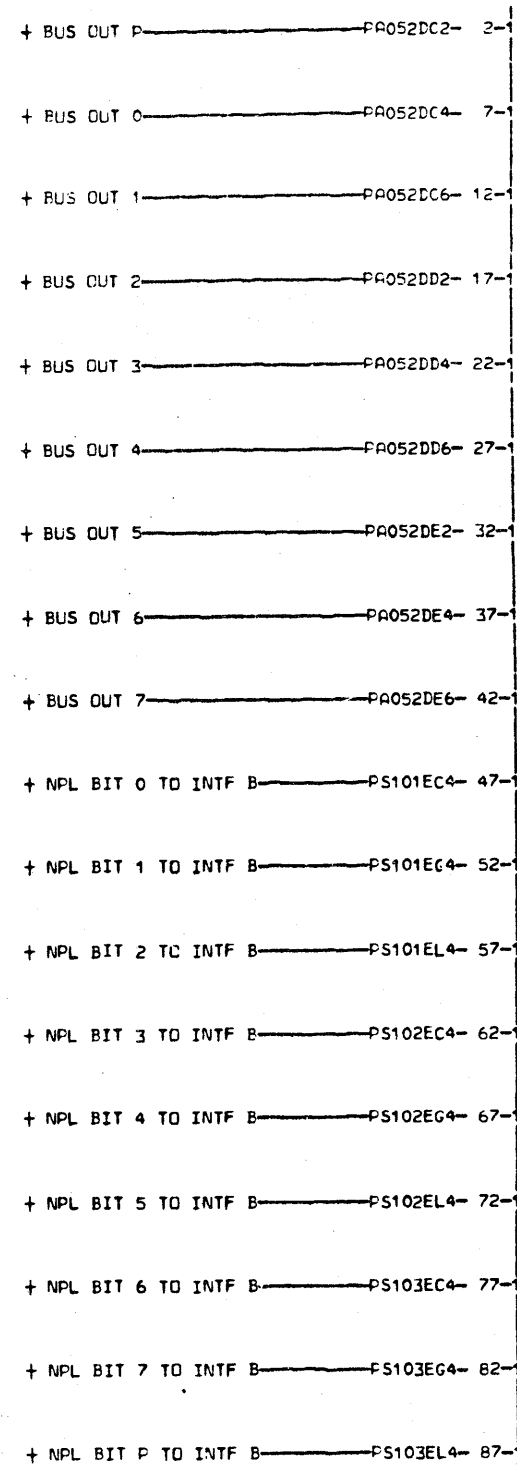
NOTE 1 SEE PAGE PA014 NOTES  
NOTE 2 FOR TRAPPING ON  
SELECT OUT THE SIGNALS  
PROPAGATE DIRECTLY ACROSS  
THE ENTR AND EXIT BLOCKS.  
FOR TRAPPING ON SELECT  
IN THE SIGNALS CROSS  
IN THE ENTR AND EXIT  
BLOCKS.

PA015  
000

EDGE CONN.	126 A-E4V5D05	01T-A1A4B11	01T-A1B3B12
102 A-E4V4B08	01T-A1B4B04	01T-A1A3D11	
01T-A1A3B08	01T-A1B3D04	136 A-E4V4D13	
01A-E4V4B09	128 A-E4V5B02	01T-A1A4B13	
01T-A1A4D08	01T-A1B4D03	01T-A1A3D13	
106 A-E4V4D10	01T-A1B3B03	139 A-E4V5D13	
01T-A1A4B09	132 A-E4V4B10	01T-A1B4B13	
124 A-E4V4B12	01T-A1A4D10	01T-A1B3D13	
01T-A1A4D12	01T-A1A3B10	141 A-E4V5B12	
01T-A1A3B12	134 A-E4V4D11	01T-A1B4D12	

LOC. TYPE

TYPE 4 CHANNEL ADAPTER	
CHANNEL INTERFACE A TAGS	
E.C. HISTORY	C. MACH. 27RNB
314402	
FRAME	01
DATE	LAST EC
05-17-76	314424
IBM CORP. SDD	PA015
P.No. 1755004	000

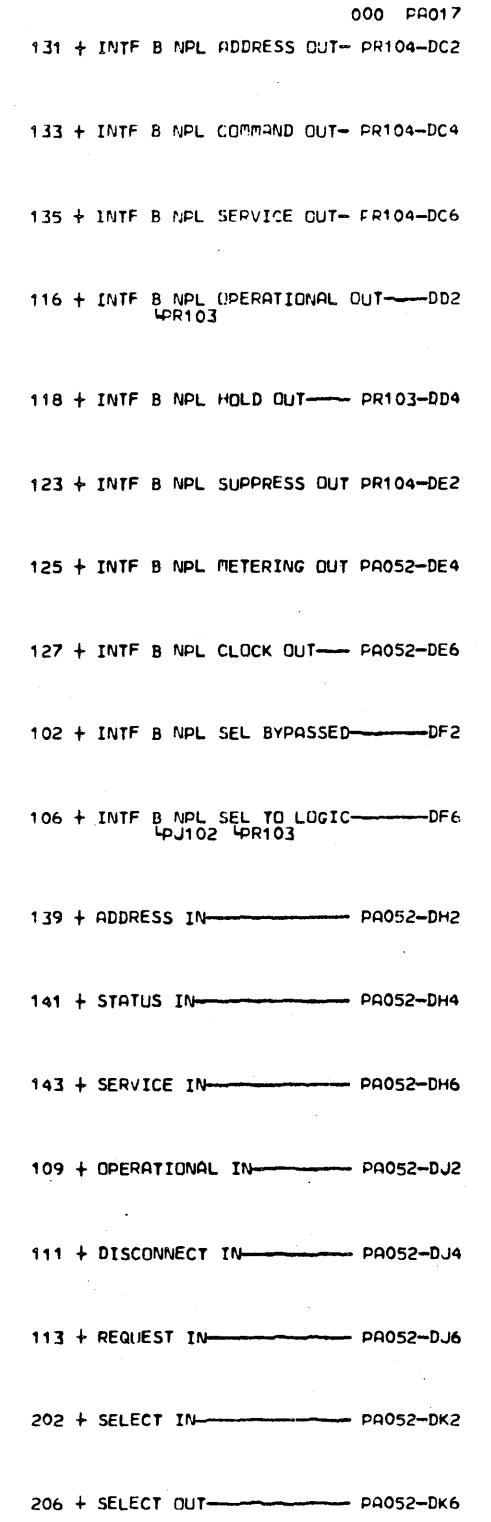
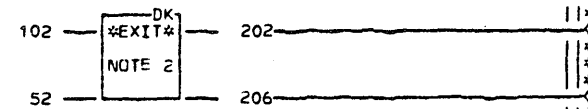
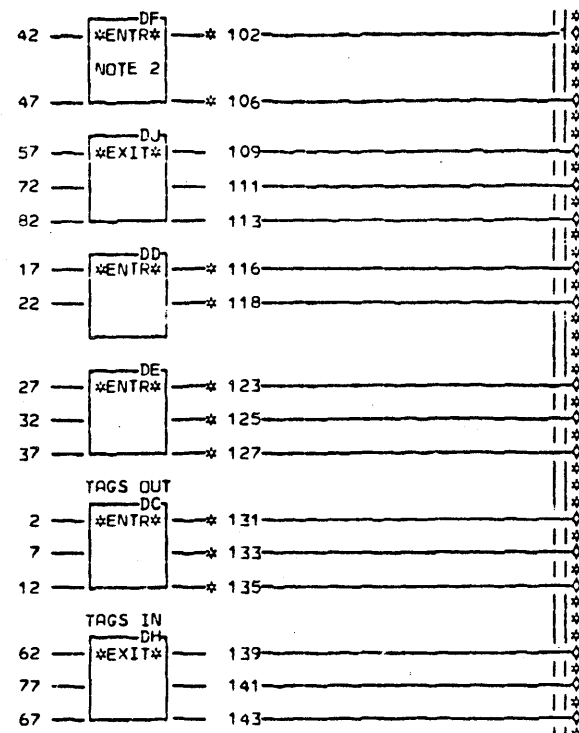
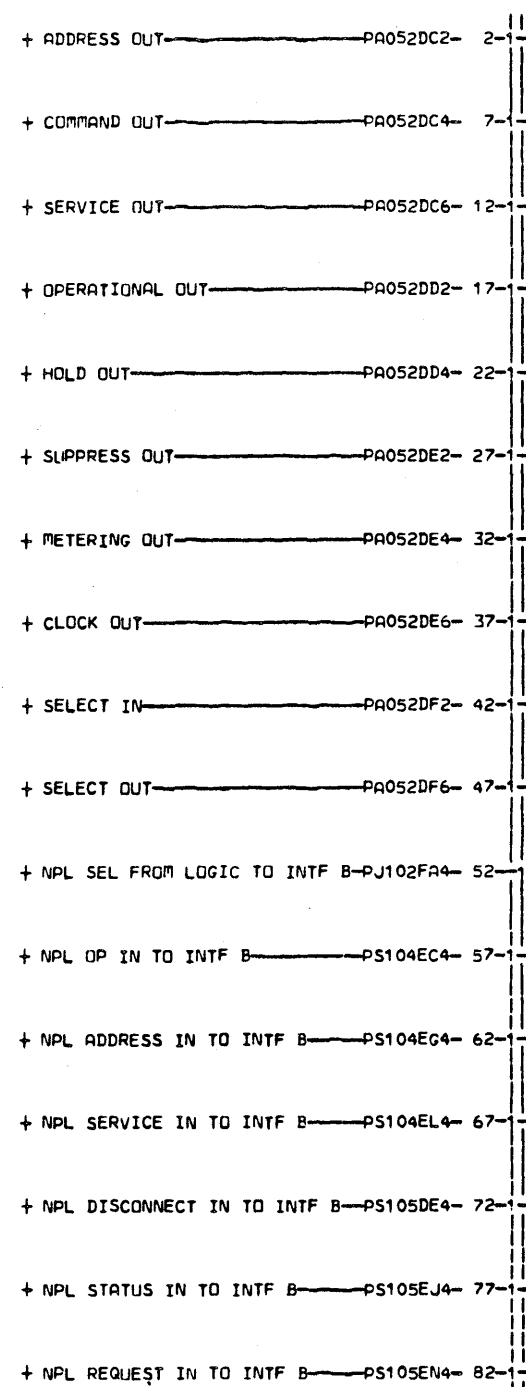


NOTE 1 SEE PAGE PA052 FOR A COMPLETE DESCRIPTION OF THE LOGIC BOARD TO TAILGATE FLAT CABLE  
 NOTE 2 ALL 01U TAILGATE PIN NOMENCLATURE IS THAT OF THE FLAT CABLE END— NOT NECESSARILY WHAT IS PRINTED ON THE TAILGATE CONNECTOR  
 PA016  
 000

EDGE CONN.	131 A-E4U2D06	01U-A1A1D09
109 A-E4U2B10	01U-A1A2B06	139 A-E4U2B02
01U-A1A2D10	01U-A1A1D06	01U-A1A2D03
01U-A1A1B10	133 A-E4U2B08	01U-A1A1B03
111 A-E4U2D11	01A-E4U2B09	141 A-E4U2B05
01U-A1A2B11	01U-A1A2B08	01U-A1A2B04
01U-A1A1D11	01U-A1A1B08	01U-A1A1D04
113 A-E4U2B12	135 A-E4U2D09	143 A-E4U2B05
01U-A1A2D12	01U-A1A2D10	01U-A1A2D05
01U-A1A1B12	01U-A1A2B09	01U-A1A1B05

LUC. TYPE

TYPE 4 CHANNEL ADAPTER	
CHANNEL INTERFACE B BUS	
—E.C.—HISTORY—C.MACH.#27RNB	
FRAME	01
DATE LAST EC	IBM CORP.SDD PA016
02-23-76 314402	P.No. 1755005 000



NOTE 1 SEE PAGE PA016 NOTES  
NOTE 2 FOR TRAPPING ON  
SELECT OUT THE SIGNALS  
PROPAGATE DIRECTLY ACROSS  
THE ENTR AND EXIT BLOCKS.  
FOR TRAPPING ON SELECT  
IN THE SIGNALS CROSS  
IN THE ENTR AND EXIT  
BLOCKS.

PA017  
000

EDGE CONN.

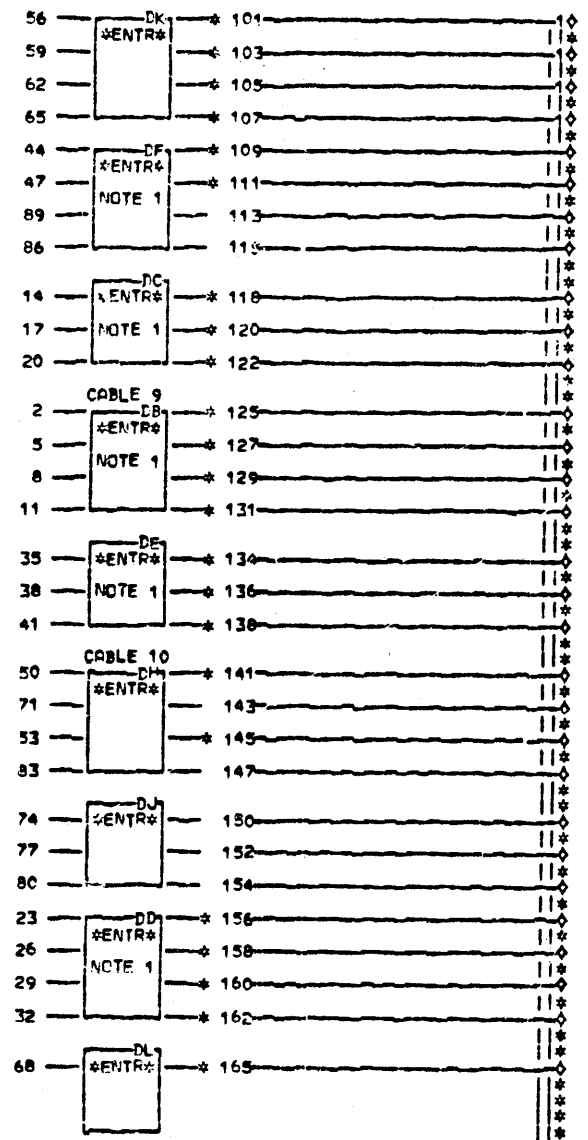
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01U-A1A3B08	01U-A1B3B12	131 A-E4U4B10	
01A-E4U4B09	123 A-E4U4B12	01U-A1A4D10	
01U-A1A4D08	01U-A1A4D12	01U-A1A3B10	
106 A-E4U4D10	01U-A1A3B12	133 A-E4U4D11	
01U-A1A4B09	125 A-E4U5D05	01U-A1A4B11	
116 A-E4U5D13	01U-A1B4B04	01U-A1A3D11	
01U-A1B4B13	01U-A1B3D04	135 A-E4U4D13	
01U-A1B3D13	127 A-E4U5B02	01U-A1A4B13	

LOC. TYPE

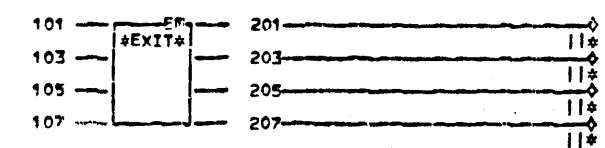
TYPE 4 CHANNEL ADAPTER	
CHANNEL INTERFACE B TAGS	
E.C. HISTORY — C-MACH. 27RNB	
314402	FRAME 01
DATE LAST EC	IBM CORP. SDD PA017
05-17-76 314424	P.No. 1755006 000

+ RESERVED PA018001- 2  
 + SPARE PA018002- 5  
 + RESERVED PA018003- 8  
 + RESERVED PA018004- 11  
 + RESERVED PA018005- 14  
 + RESERVED PA018006- 17  
 + RESERVED PA018007- 20  
 + RESERVED PA018008- 23  
 + RESERVED PA018009- 26  
 + RESERVED PA018010- 29  
 + SPARE PA018011- 32  
 + RESERVED PA018012- 35  
 + RESERVED PA018013- 38  
 + SPARE PA018014- 41  
 + SPARE PA018015- 44  
 + RESERVED PA018016- 47  
 + SYNC PULSE IN PA060DH1- 50  
 - SELECTED FROM PREVIOUS CA PA060DH5- 53  
 + SELECT CA# 1 FOR IPL PA060DK1- 56  
 + SELECT CA# 2 FOR IPL PA060DK3- 59  
 + RESERVED 10 PA060DK5- 62  
 + RESERVED 11 PA060DK7- 65  
 - SELECTED FROM PREV CA RETRN PA060DL2- 68  
 + SYNC PULSE TO NEXT CA PM101CM2- 71  
 + PRIORITY BUS BIT 4 PM102EB7- 74  
 + PRIORITY BUS BIT 2 PM102ED2- 77  
 + PRIORITY BUS BIT 1 PM102EF2- 80  
 - SELECTED TO NEXT CA PM103FF6- 83  
 + ADBUS BIT X.5 PP103FN2- 86  
 + ADBUS BIT X.4 PP103GN2- 89

NOTE 1. CABLE NO. 9 IS PRESENT  
 IN 3705 MODELS  
 JO K OR L ONLY.



EDGE CONN.	01A-E4E1D13	01A-E4F1C11	01A-E4E1C13	01A-E4F1C13
101	A-E4D6E02	111 A-E4H1D13	127 A-E4J1E11	138 A-E4J1B13
102	A-E4M1C11	01A-E4F1B13	01A-E4F1D11	01A-E4F1A13
103	A-E4E6D02	118 A-E4J1A11	129 A-E4J1D11	141 A-E4N1E11
104	A-E4M1B11	01A-E4F1B11	01A-E4G1A11	145 A-E4N1B13
105	A-E4E6C02	120 A-E4H1E11	131 A-E4J1C11	156 A-E4H1B11
106	A-E4L1E11	01A-E4E1C11	01A-E4E1E11	01A-E4D1E11
107	A-E4E6A02	122 A-E4H1D11	134 A-E4J1F13	158 A-E4H1A11
108	A-E4L1D11	01A-E4E1D11	01A-E4G1A13	01A-E4E1A11
109	A-E4H1E13	125 A-E4K1B11	136 A-E4J1C13	160 A-E4K1B13

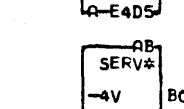
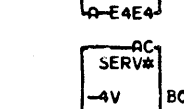
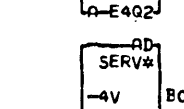
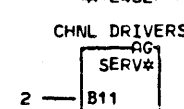
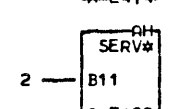
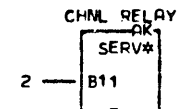
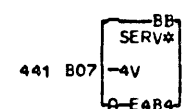
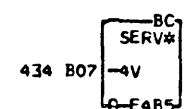
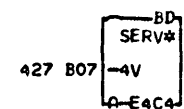
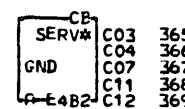
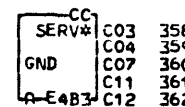
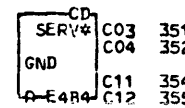
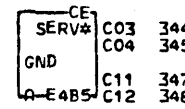
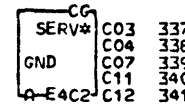
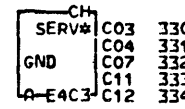
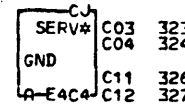
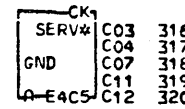
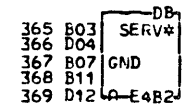
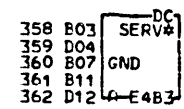
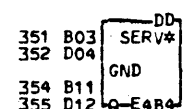
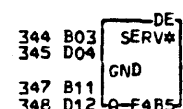
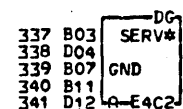
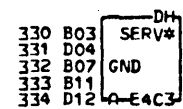
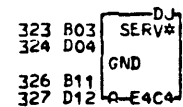
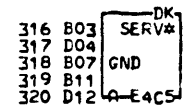
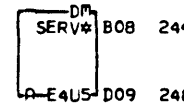
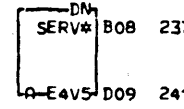
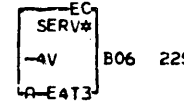
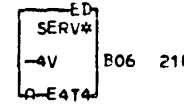
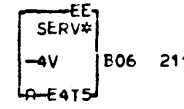
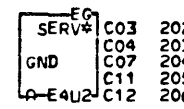
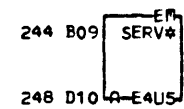
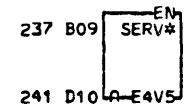
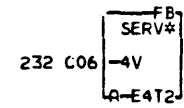
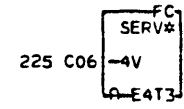
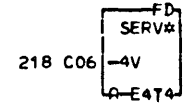
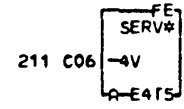
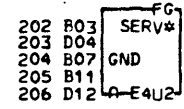
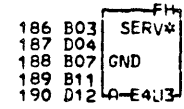
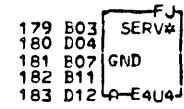
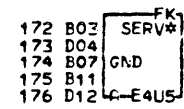


000 PA018  
 125 + RESERVED EN Y3D13 EX Y2D10 DB1  
 + SPARE 1 DB3  
 129 + RESERVED EN Y3D10 EX Y2D13 DB5  
 131 + RESERVED EN Y3D09 EX Y2D07 DB7  
 118 + RESERVED EN Y3D07 EX Y2D09 DC2  
 120 + RESERVED EN Y3D06 EX Y3D05 DC4  
 122 + RESERVED EN Y3D05 EX Y2D06 DC6  
 156 + RESERVED EN Y3D03 EX Y2D02 DD1  
 158 + RESERVED EN Y3D02 EX Y2D03 DD3  
 160 + RESERVED EN Y3B13 EX Y2B10 DD5  
 162 + SPARE DD7  
 134 + RESERVED EN Y3B10 EX Y2B13 DE2  
 136 + RESERVED EN Y3B09 EX Y2B05 DE4  
 138 + SPARE DE6  
 109 + SPARE DF1  
 111 + RESERVED EN Y3B05 EX Y2B09 DF3  
 113 + ADBUS BYTE X BIT 4 ABC09-DF5  
 115 + ADBUS BYTE X BIT 5 AP009-DF7  
 141 + SYNC PULSE IN PM101-DH1  
 143 + SYNC PULSE TO NEXT CA PA060-DH3  
 145 - SELECTED FROM PREVIOUS CA LPM103 DH5  
 147 - SELECTED TO NEXT CA PA060-DH7  
 150 + PRIORITY BUS BIT 4 PA060-DJ2  
 152 + PRIORITY BUS BIT 2 PA060-DJ4  
 154 + PRIORITY BUS BIT 1 PA060-DJ6  
 101 + SELECT CA# 1 FOR IPL PA108-CK1  
 103 + SELECT CA# 2 FOR IPL PA060-CK3  
 105 + RESERVED 10 PA060-DK5  
 107 + RESERVED 11 PA060-DK7  
 165 - SELECTED FROM PREV CA RETRN LPA060 DL2  
 201 + RESERVED 11 PA060-EM1  
 203 + SELECT CA# 1 FOR IPL PA060-EM3  
 205 + SELECT CA# 2 FOR IPL PA060-EM5  
 207 + RESERVED 10 PA060-EM7

LOC. TYPE

CS3 CABLE AND MULTIPLE CA# CONNECT CABLE	
E.C. HISTORY	E. PACH. 27RNB
314402	FRAME 01
316677	IRN CORR. SCD
DATE LAST EC	01-24-79 318589
P.N.	1755007
000	000

+ 6 VOLTS FROM VOLTAGE BUSS PA020001- 2-3



LOC. TYPE

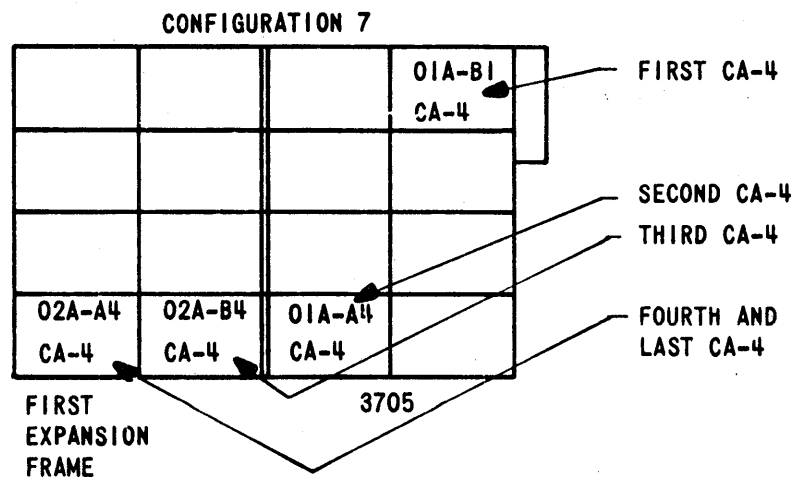
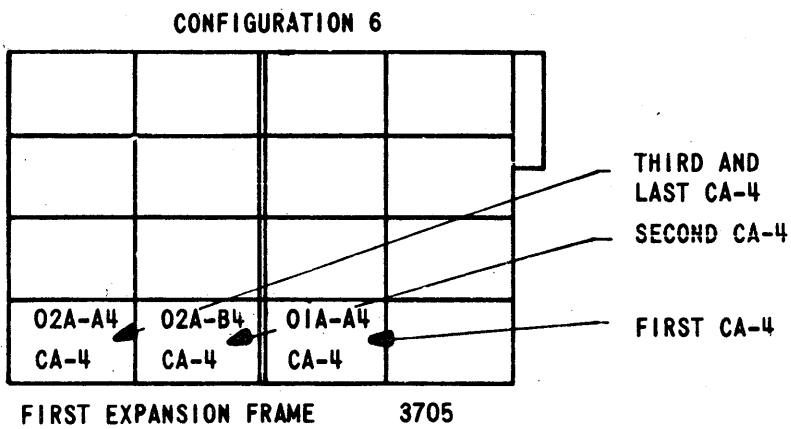
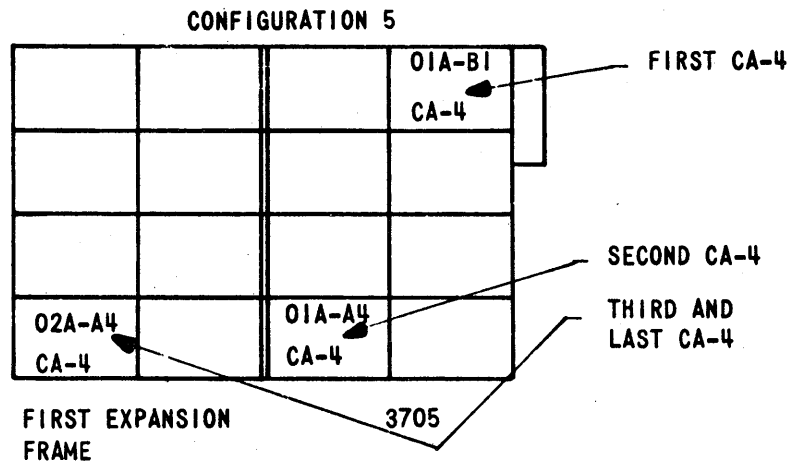
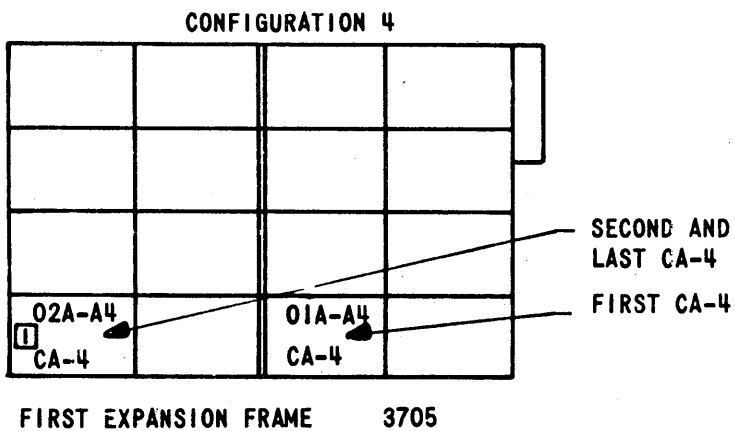
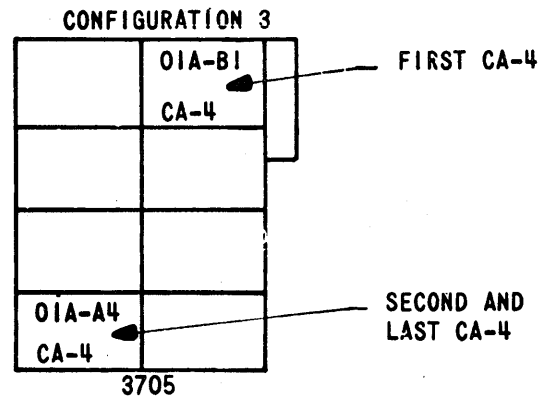
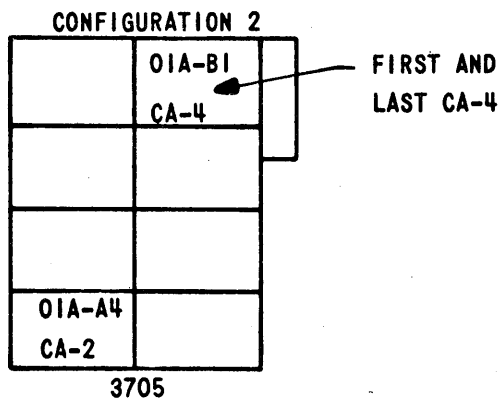
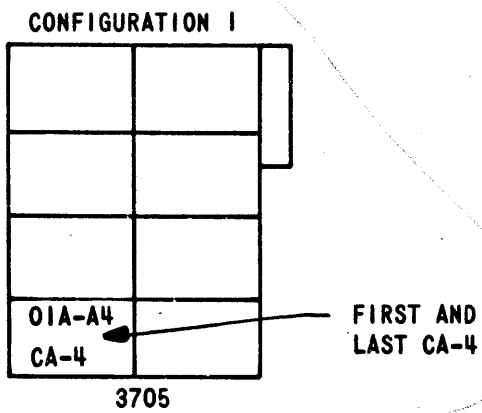
SERVICE WIRING  
 -E.C.-HISTORY-C MACH.27RNB  
 FRAME 01  
 IBM CORP.SDD PA020  
 DATE LAST EC 02-23-76 314402  
 P.No. 1755008 000

**CA-4 JUMPING**

EXAMINE THE CONFIGURATIONS BELOW TO DETERMINE YOUR CHANNEL ADAPTER CONFIGURATION. WHEN PLUGGING CERTAIN CA-4 CARDS AND WHEN PERFORMING THE BOARD ACTIONS REQUIRED, IT WILL BE NECESSARY TO DETERMINE WHICH IS THE FIRST, SECOND, THIRD, FOURTH OR LAST CA-4. AFTER YOUR CONFIGURATION HAS BEEN DETERMINED, PROCEED TO PAGE 2 OF PA048. THE FOLLOWING B/M NUMBERS ARE APPLICABLE FOR THE CONFIGURATIONS BELOW:

- B/M 5993339 - CA-2 O1A-A4 BOARD POSITION
- B/M 1749611 OR 1750267 - CA-4 O1A-A4 BOARD POSITION
- B/M 1648282 OR 1648322 - CA-4 O1A-B1 BOARD POSITION
- B/M 1648283 OR 1648323 - CA-4 O2A-B4 BOARD POSITION
- B/M 1749616 OR 1750268 - CA-4 O2A-A4 BOARD POSITION
- B/M 5997515 - CA-2 O2A-A4 BOARD POSITION
- B/M 1785212 - CA-3 O2A-A4 BOARD POSITION

**I** FOR CONFIGURATION 4 A CA-2 OR CA-3 MAY BE INSTALLED IN THE O2A-A4 BOARD POSITION INSTEAD OF A CA-4. IF A CA-2 OR CA-3 IS INSTALLED IN THE O2A-A4 BOARD POSITION, THEN THE CA-4 IN THE O1A-A4 BOARD POSITION IS THE FIRST AND LAST CA-4.



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620-0133-1 MRO# 780522203 VERTICAL ELECTRICAL FORMAT

<b>IBM</b>		DATE	CHANGE NO	DATE	CHANGE NO
NAME		NOV76	316677		
DESIGN		SHT 1 OF 3			
DETAIL					
CHECK		CLASSIFICATION	MUST CONFORM TO ENG SPEC	DEVELOPMENT NO	LOGIC PG NO
APPRO					PA048

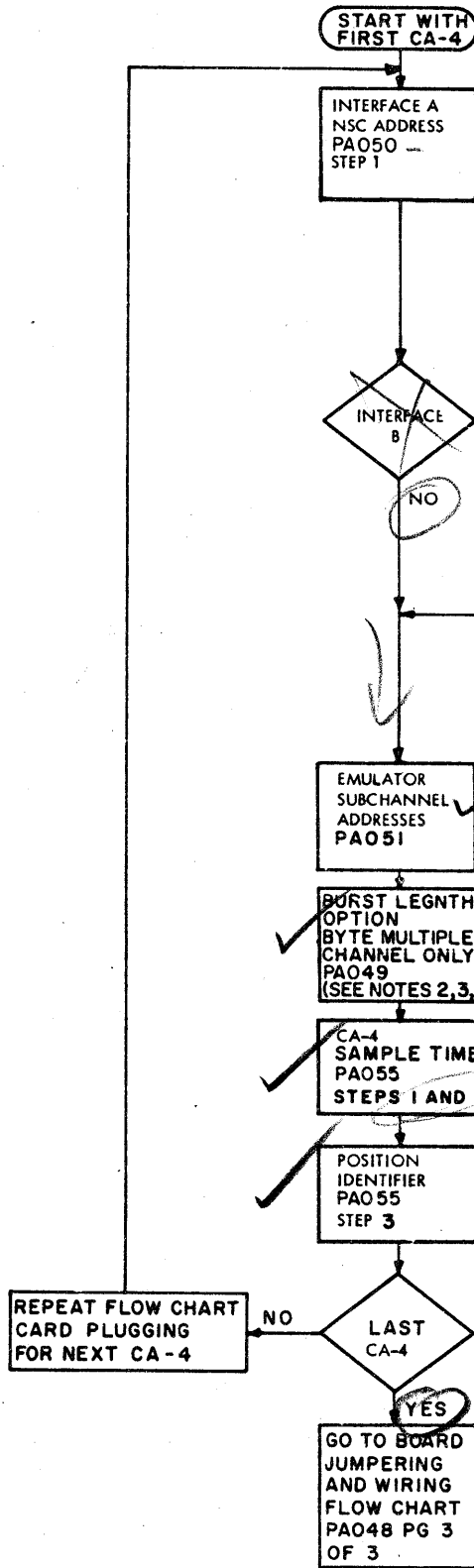
1755104 B

STEP THROUGH THIS CHART (SEE PA048 PG. 1 OF 3) FOR EACH CA-4 INSTALLED (BEFORE PROCEEDING)

PART NO. 1755104

LOGIC PG. NO. PA048

PAGE 2 OF 3



NOTE 1: INTERFACE B NSC ADDRESS JUMPERING FOR STEPS 2 AND 3 MUST BE THE SAME

NOTES

- 2 REFER TO CHART AT BOTTOM OF PAGE PA049 FOR SYSTEM ATTACHMENT PLUGGING
- 3 FOR A DETAILED EXPLANATION OF THE BURST LENGTH PLUGGING OPTION SEE PAGE PA056 AND H-000 OF THE FETMM
- 4 THE BURST LENGTH OPTION MUST NOT BE PLUGGED (NO JUMPERS!), IF THE CA-4 IS ATTACHED TO A BLOCK MULTIPLEX OR SELECTOR CHANNEL, INTERFACE CONTROL CHECKS WILL RESULT IF ANY JUMPERS ARE PRESENT.

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IBM				DATE	CHANGE NO	DATE	CHANGE NO
NAME	TYPE 4 CA CARD AND			FEB76	314402		
BOARD JUMPER OPTIONS				MAY76	314424		
DESIGN	DJR	FEB76	SHT 2 OF 3	SEP 76	315620		
DETAIL	RB	FEB76		NOV76	316677		
CHECK			CLASSIFICATION	MUST CONFORM TO ENG SPEC		DEVELOPMENT NO	
APPRO						LOGIC PG NO PA048	

1755104 B

1755104 B

PART NO 1755104 LOGIC PG NO PA048

PG. 3 OF 3

NOTE 1: IF A DEFECTIVE BOARD IS BEING REPLACED, THE NEW BOARD SHOULD BE WIRED TO THE SAME OPTION. IF THIS IS A MANUFACTURED MACHINE INSTALLATION, WIRE THE CA-4 BOARD TO THE OPTION DESIRED. THIS STEP HAS ALREADY BEEN COMPLETED IF THIS IS A CA-4 MES.

BOARD JUMPERING AND WIRING

(BEGIN WITH FIRST CA-4)  
INTERFACE A TRAP SELECT IN OUT OPTION (SEE NOTE 1)  
PA052 PG. 1 OF 2  
STEP 1

INTERFACE B INSTALLED

INTERFACE B TRAP SELECT IN/OUT OPTION (SEE NOTE 1)  
PA052 PG. 1 OF 2  
STEP 2

LAST CA-4

REPEAT TRAP SELECT BOARD WIRING FOR NEXT CA-4

PERFORM SELECTED SIGNAL PROPAGATION AND IPL SELECT JUMPERING. (SEE NOTE 2)  
PA053 PG. 1 OF 6

N-CHANNEL ROS INSTALLED, B/M1648305, CARD P/N8251990 OR 8252028, O1A-B4F2 SOCKET

PERFORM N-CHANNEL ROS CA-4 REWORK  
PA053 PG. 3 OF 6  
(SEE NOTE 2)

MORE THAN 2 CA-4'S INSTALLED

4 CA-4'S INSTALLED

PERFORM ENABLE DISABLE BOARD REWORK FOR 4 CA-4'S INSTALLED.  
PA053 PGS. 4&5 OF 6  
(SEE NOTE 2)

PERFORM ENABLE DISABLE BOARD REWORK FOR 3 CA-4'S INSTALLED.  
PA053 PG. 6 OF 6  
(SEE NOTE 2)

NOTE 2: THIS REWORK ONLY HAS TO BE PERFORMED IF A DEFECTIVE BOARD IS BEING REPLACED OR IF AN ENTIRE 1ST EXPANSION FRAME IS BEING ADDED TO AN ALREADY INSTALLED 3705 AND THE 1ST EXPANSION CONTAINS ONE OR TWO CA-4'S. OTHERWISE, THE REWORK HAS BEEN COMPLETED BY MANUFACTURING, IF THIS IS A NEW MACHINE OR ACCOMPLISHED THROUGH CA-4 OR N-CHANNEL ROS MES INSTALLATION INSTRUCTIONS.

END

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620-0133-1 MRO# 780522203 VERTICAL ELECTRICAL FORMAT

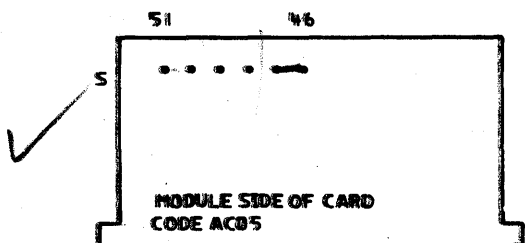
IBM		DATE	CHANGE NO	DATE	CHANGE NO
NAME		NOV76	316677		
DESIGN	SHT 3 OF 3				
DETAIL					
CHECK	CLASSIFICATION	MUST CONFORM TO ENG SPEC	DEVELOPMENT NO	LOGIC PG NO	
APPRO				PA048	

1755104 B

ASTROCLOTH NAS01



TYPE 4 CHANNEL ADAPTER JUMPER INFORMATION  
(USE JUMPER P/N 816645 IN SHIPPING GROUP)

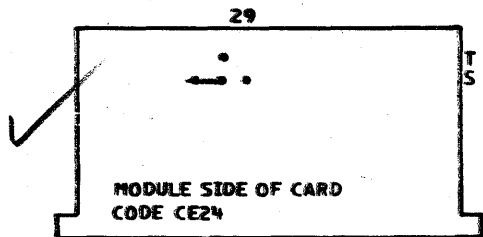


MODULE SIDE OF CARD  
CODE AC05

CARD LOCATION: J2  
LOGIC PAGE PK106

AC05—REF. PLUGGING AT BOTTOM OF PAGE.  
EB BYTE CHANNEL  
BURST LENGTH OPTION (AC05)  
S50 TO S51 FOR 4 BYTE BURST  
S48 TO S49 FOR 8 BYTE BURST  
S46 TO S47 FOR 16 BYTE BURST

NO JUMPER FOR 32 BYTE BURST  
NOTE TO MANUFACTURING: REMOVE JUMPERS  
AFTER COMPLETION OF TESTING

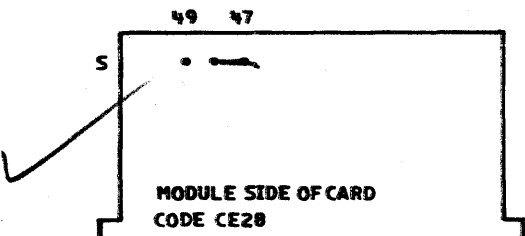


MODULE SIDE OF CARD  
CODE CE24

CARD LOCATION: D2  
LOGIC PAGE PP102

CE24—REF. PLUGGING AT BOTTOM OF PAGE.  
CS BYTE CHANNEL BURST LENGTH OPTION  
(CE24)  
S29 TO S30 FOR 4 BYTE BURST  
T30 TO S30 FOR 8 BYTE BURST  
S31 TO S30 FOR 16 BYTE BURST

NO JUMPER FOR FULL BYTE CNT BURST  
NOTE TO MANUFACTURING: REMOVE JUMPERS  
AFTER COMPLETION OF TESTING



MODULE SIDE OF CARD  
CODE CE28

CARD LOCATION: H2  
LOGIC PAGE PL104

CE28—REF. PLUGGING AT BOTTOM OF PAGE.  
DURATION OF DELAY BETWEEN BURSTS ON  
BYTE CHANNEL (CE28)  
S47 TO S48 FOR LESS THAN 1 MACH CYCLE  
APPROXIMATE 0.5 μ SEC  
S48 TO S49 FOR 32 MACH CYCLES  
APPROXIMATE 32 μ SEC

NOTE TO MANUFACTURING: REMOVE JUMPERS  
AFTER COMPLETION OF TESTING

BYTE MULTIPLEX CHANNEL BURST LENGTH PLUGGING <sup>3</sup>

FOR MAXIMUM THRUPT ON SYSTEM TYPE (SEE <sup>4</sup> IF SYS TYPE NOT PRESENT)	REF AC05 ABOVE			REF CE24 ABOVE			REF CE28 ABOVE	
	S50-S51	S48-S49	S46-S47	S29-S30	T30-S30	S31-S30	S47-S48	S48-S49
360-2870A; 370-3115, 3125	JMP	FLT	FLT	JMP	FLT	FLT	JMP	FLT
360-2050; 370-3155, 3158, 4331, <u>4341</u>	FLT	FLT	JMP	FLT	FLT	JMP	JMP	FLT
370-3031, 3032, 3033 <sup>5</sup>	FLT	FLT	FLT	FLT	FLT	JMP	JMP	FLT
360-2025, 2030, 2040; 370-3135, 3138, 3145, 3148, 2870B	FLT	FLT	FLT	FLT	FLT	FLT	FLT	FLT

FLT=FLOAT  
(NO JUMPER)  
JMP=JUMPER  
BYTE MULTIPLEX  
CHANNEL ONLY

NOTES

- 1 PLUG CARD CODES AC05, CE24 AND CE28 ACCORDING TO THE CPU BEING ATTACHED TO. IF 2 OR MORE DIFFERENT CPU MODELS CAN ACCESS THE CA4 VIA THE 2 PROCESSOR SWITCH FEATURE OR CHANNEL SWITCH BOX, PLUG ACCORDING TO THE CPU REQUIRING THE LOWEST NUMBER OF BYTES IN A BURST: REF PLUGGING CHART ABOVE.
- 2 NOTE TO MANUFACTURING—REMOVE JUMPERS AFTER COMPLETION OF TESTING
- <sup>3</sup> INTERFACE CONTROL CHECKS WILL RESULT IF JUMPERS ARE INSTALLED AND THE CA4 IS ATTACHED TO A BLOCK MULTIPLEX OR SELECTOR CHANNEL
- <sup>4</sup> IF CPU TYPE IS NOT IN CHART, PLUG FOR 4 BYTE BURSTS (TOP ENTRY)
- <sup>5</sup> 3031, 3032, 3033 MUST HAVE DIRECTOR MICROCODE E/C'S 276270A, 274609, 278171 INSTALLED RESPECTIVELY. IF NOT, PLUG FOR 16 BYTE BURSTS.

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IBM				DATE	CHANGE NO	DATE	CHANGE NO
NAME				SEE E/C HISTORY			
				NOV76	316677		
DESIGN	DJR	FEB76	SHEET 1 OF 1	RED	JAN80	317524	
DETAIL	TS	JAN80					
CHECK				MUST CONFORM TO ENG SPEC		DEVELOPMENT NO	LOGIC PG NO
APPRO		DJR	FEB76				PA049

B

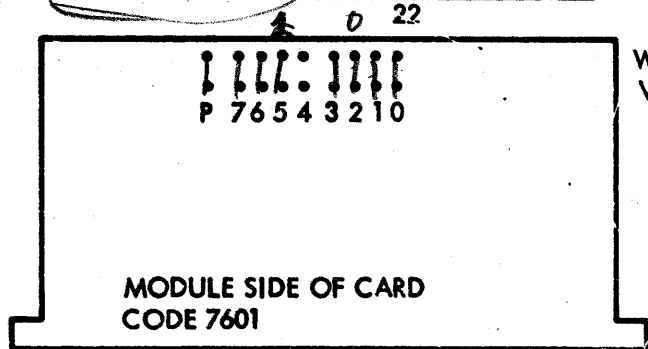
PART NO  
1755106

LOGIC PG NO  
PA050

1755106

**TYPE 4 CHANNEL ADAPTER JUMPER INFORMATION**  
(USE JUMPER P/N 816645 IN SHIPPING GROUP)

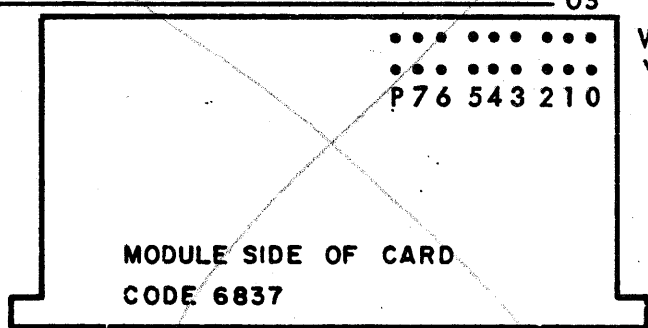
(STEP 1) INTERFACE A NSC CHANNEL ADDRESS (SEE 2)



CARD LOCATION: P2  
INSERT JUMPER FOR 0, REMOVE FOR 1  
LOGIC PAGE: PB104

= 010

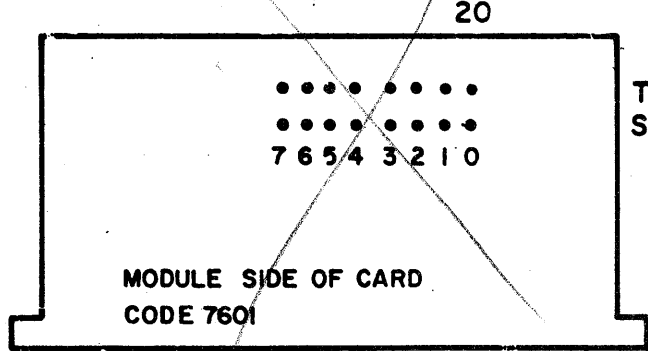
(STEP 2) INTERFACE B NSC CHANNEL ADDRESS (SEE 2)  
TRAP SELECT



CARD LOCATION: R2  
INSERT JUMPER FOR 0, REMOVE FOR 1  
LOGIC PAGE: PR106

None

(STEP 3) INTERFACE B INPUT 67 NSC CHANNEL ADDRESS (SEE 1 & 2)



CARD LOCATION: P2  
INSERT JUMPER FOR 0, REMOVE FOR 1  
LOGIC PAGE: PB107

1 THE INTERFACE B INPUT 67 NSC ADDRESS MUST BE PLUGGED IDENTICAL TO THE INTERFACE B NSC ADDRESS TRAP SELECT PLUGGING.

2 FOR A 3705 WITH A SINGLE CA-4 THE NSC ADDRESS MAY BE PLUGGED WITHIN THE ESC RANGE BUT CANNOT ALSO BE USED FOR EMULATION.

FOR A 3705 WITH TWO CA-4'S RUNNING STANDALONE EP, THE NSC ADDRESS MAY BE PLUGGED WITHIN THE ESC RANGE, PROVIDING NO ATTEMPT IS MADE TO USE THE NSC ADDRESS FROM THE CHANNEL THAT DID NOT IPL THE 3705: (EXAMPLE-DYNADUMP FROM THE NON-IPL'ED CHANNEL):

FOR A 3705 WITH TWO CA-4'S RUNNING PEP AND AN IPL SOURCE SWITCH INSTALLED, THE NSC ADDRESS MUST NOT BE WITHIN THE ESC RANGE. TO DETERMINE IF AN IPL SOURCE SWITCH IS INSTALLED SEE PA953 PG 2 OF 6:

FOR A 3705 WITH TWO OR MORE CA-4'S AND WITH N-CHANNEL ROS INSTALLED, CARD P/N 8251990 OR 8252028, 01A-84F2 SOCKET, B/M 1648305, THE NSC ADDRESS MAY BE PLUGGED WITHIN THE ESC RANGE BUT CANNOT ALSO BE USED FOR EMULATION.

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VERTICAL ELECTRICAL FORMAT  
MRO2 780922203  
820-0133-1

<b>IBM</b>				DATE	CHANGE NO	DATE	CHANGE NO
NAME	TYPE 4 CA REFERENCE PAGE			FEB76	314402		
				MAY76	314424		
DESIGN	DJR	FEB76	SHT 1 OF 1	NOV76	316677		
DETAIL	RB	FEB76		FEB79	318589		
CHECK			CLASSIFICATION	MUST CONFORM TO ENG SPEC		DEVELOPMENT NO	LOGIC PG NO
APPRO							PA050

1755106

E

1755107

LOW VALID ESC ADDRESS

PART NO 1755107 LOGIC PG NO PA051

WRITE IN LOW ADDRESS

020

FIRST CHANNEL ADDRESS ASSIGNED (MUST BE MULTIPLES OF 16 SUCH AS 0, 16, 32, 48, ETC.)

HI VALID ESC ADDRESS

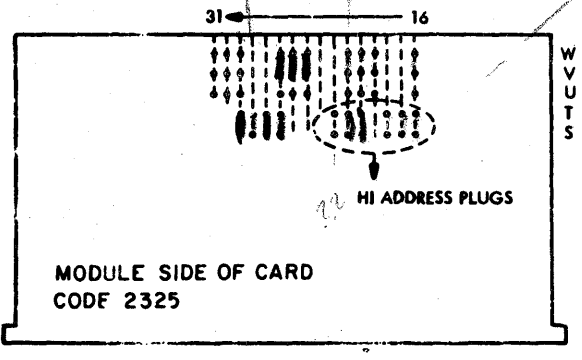
WRITE IN HIGH ADDRESS

02F

FIRST ADDRESS	PLUG M2(CODE 2325) TO THIS CONFIGURATION							
HEX								
00	T29-S29	T28-S28	T27-S27	T26-S26	W21-V21	W20-V20	W19-V19	W16-V16
10	T28-S28	T27-S27	T26-S26	W21-V21	W20-V20	W19-V19	W16-V16	
20	T29-S29	T27-S27	T26-S26	W26-V26	W25-V25	W24-V24	W21-V21	W16-V16
30	T27-S27	T26-S26	W26-V26	W25-V25	W24-V24	W21-V21	W20-V20	W16-V16
40	W30-V30	W29-V29	T29-S29	T28-S28	T26-S26			
50	W30-V30	W29-V29	T28-S28	T26-S26	W21-V21	W19-V19	W16-V16	
60	W30-V30	W29-V29	T29-S29	T26-S26	W26-V26	W24-V24	W21-V21	W16-V16
70	W30-V30	W29-V29	T26-S26	W26-V26	W24-V24	W21-V21	W16-V16	
80	W31-V31	T29-S29	T28-S28	T27-S27				
90	W31-V31	T28-S28	T27-S27	W20-V20	W19-V19	W16-V16		
A0	W31-V31	T29-S29	T27-S27	W25-V25	W24-V24	W20-V20	W16-V16	
B0	W31-V31	T27-S27	W25-V25	W24-V24	W20-V20	W16-V16		
C0	W31-V31	W29-V29	T29-S29	T28-S28				
D0	W31-V31	W29-V29	T28-S28	W19-V19	W16-V16			
E0	W31-V31	W29-V29	T29-S29	W24-V24				
F0	W31-V31	W29-V29	W24-V24	W16-V16				

LAST CH ADDRESS		PLUG M2(CODE 2325) TO THIS CONFIGURATION						
HEX	DEC	S22-T22	S21-T21	S20-T20	S18-T18	S17-T17	S16-T16	
03	3	FLT	JMP	FLT	JMP	FLT	FLT	
07	7	FLT	JMP	FLT	JMP	FLT	JMP	
0B	11	FLT	JMP	FLT	JMP	JMP	FLT	
0F	15	FLT	JMP	FLT	JMP	JMP	JMP	
13	19	FLT	JMP	FLT	FLT	FLT	FLT	
17	23	FLT	JMP	FLT	FLT	FLT	JMP	
1B	27	FLT	JMP	FLT	FLT	JMP	FLT	
1F	31	FLT	JMP	FLT	FLT	JMP	JMP	
23	35	FLT	JMP	JMP	JMP	FLT	FLT	
27	39	FLT	JMP	JMP	JMP	FLT	JMP	
2B	43	FLT	JMP	JMP	JMP	JMP	FLT	
2F	47	FLT	JMP	JMP	JMP	JMP	JMP	
33	51	FLT	JMP	JMP	FLT	FLT	FLT	
37	55	FLT	JMP	JMP	FLT	FLT	JMP	
3B	59	FLT	JMP	JMP	FLT	JMP	FLT	
3F	63	FLT	JMP	JMP	FLT	JMP	JMP	
43	67	FLT	FLT	FLT	JMP	FLT	FLT	
47	71	FLT	FLT	FLT	JMP	FLT	FLT	
4B	75	FLT	FLT	FLT	JMP	JMP	JMP	
4F	79	FLT	FLT	FLT	JMP	JMP	JMP	
53	83	FLT	FLT	FLT	FLT	FLT	FLT	
57	87	FLT	FLT	FLT	FLT	FLT	JMP	
5B	91	FLT	FLT	FLT	FLT	JMP	FLT	
5F	95	FLT	FLT	FLT	FLT	JMP	JMP	
63	99	FLT	FLT	JMP	JMP	FLT	FLT	
67	103	FLT	FLT	JMP	JMP	FLT	JMP	
6B	107	FLT	FLT	JMP	JMP	JMP	FLT	
6F	111	FLT	FLT	JMP	JMP	JMP	JMP	
73	115	FLT	FLT	JMP	FLT	FLT	FLT	
77	119	FLT	FLT	JMP	FLT	FLT	JMP	
7B	123	FLT	FLT	JMP	FLT	JMP	FLT	
7F	127	FLT	FLT	JMP	FLT	JMP	JMP	
83	131	JMP	JMP	FLT	JMP	FLT	FLT	
87	135	JMP	JMP	FLT	JMP	FLT	JMP	
8B	139	JMP	JMP	FLT	JMP	JMP	FLT	
8F	143	JMP	JMP	FLT	JMP	JMP	JMP	
93	147	JMP	JMP	FLT	FLT	FLT	FLT	
97	151	JMP	JMP	FLT	FLT	FLT	JMP	
9B	155	JMP	JMP	FLT	FLT	JMP	FLT	
9F	159	JMP	JMP	FLT	FLT	JMP	JMP	
A3	163	JMP	JMP	JMP	JMP	FLT	FLT	
A7	167	JMP	JMP	JMP	JMP	FLT	JMP	
AB	171	JMP	JMP	JMP	JMP	JMP	FLT	
AF	175	JMP	JMP	JMP	JMP	JMP	JMP	
B3	179	JMP	JMP	JMP	FLT	FLT	FLT	
B7	183	JMP	JMP	JMP	FLT	FLT	JMP	
BB	187	JMP	JMP	JMP	FLT	JMP	FLT	
BF	191	JMP	JMP	JMP	FLT	JMP	JMP	
C3	195	JMP	FLT	FLT	JMP	FLT	FLT	
C7	199	JMP	FLT	FLT	JMP	FLT	JMP	
CB	203	JMP	FLT	FLT	JMP	JMP	FLT	
CF	207	JMP	FLT	FLT	JMP	JMP	JMP	
D3	211	JMP	FLT	FLT	FLT	FLT	FLT	
D7	215	JMP	FLT	FLT	FLT	FLT	JMP	
DB	219	JMP	FLT	FLT	FLT	JMP	FLT	
DF	223	JMP	FLT	FLT	FLT	JMP	JMP	
E3	227	JMP	FLT	JMP	JMP	FLT	FLT	
E7	231	JMP	FLT	JMP	JMP	FLT	JMP	
EB	235	JMP	FLT	JMP	JMP	JMP	FLT	
EF	239	JMP	FLT	JMP	JMP	JMP	JMP	
F3	243	JMP	FLT	JMP	FLT	FLT	FLT	
F7	247	JMP	FLT	JMP	FLT	FLT	JMP	
FB	251	JMP	FLT	JMP	FLT	JMP	FLT	
FF	255	JMP	FLT	JMP	FLT	JMP	JMP	

REFER TO LOGIC PAGES PD102 & PD103 FOR LOW VALID ESC ADDRESSES  
REFER TO LOGIC PAGE PD104 FOR HIGH VALID ESC ADDRESSES



FILL IN ABOVE PLUG CARD ACCORDING TO CHARTS FOR LOW & HIGH VALID ADDRESS. PLUG ONLY THOSE SPECIFIED FOR EACH ADDRESS.

NOTES 1:

THE ESC ADDRESSES MUST BE PLUGGED EQUAL TO THE RANGE SPECIFIED IN THE EP SYSGEN

2: WHEN RUNNING IN NCP MODE ONLY ON A CA-4, PLUG THE ESC ADDRESS RANGE AS FOLLOWS: LOW = HEX 'C0', HIGH = '53'

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<b>IBM</b>		DATE	CHANGE NO	DATE	CHANGE NO
NAME	TYPE 4 CA REFERENCE PAGE	FEB 76	314402		
		MAY 76	314424		
DESIGN		SEP 76	315620		
DETAIL		NOV 76	316677		
CHECK	CLASSIFICATION	MUST CONFORM TO ENG SPEC		LOGIC PG NO	
APPRO		DEVELOPMENT NO		PA051	

1755107 B

**1755108 B**

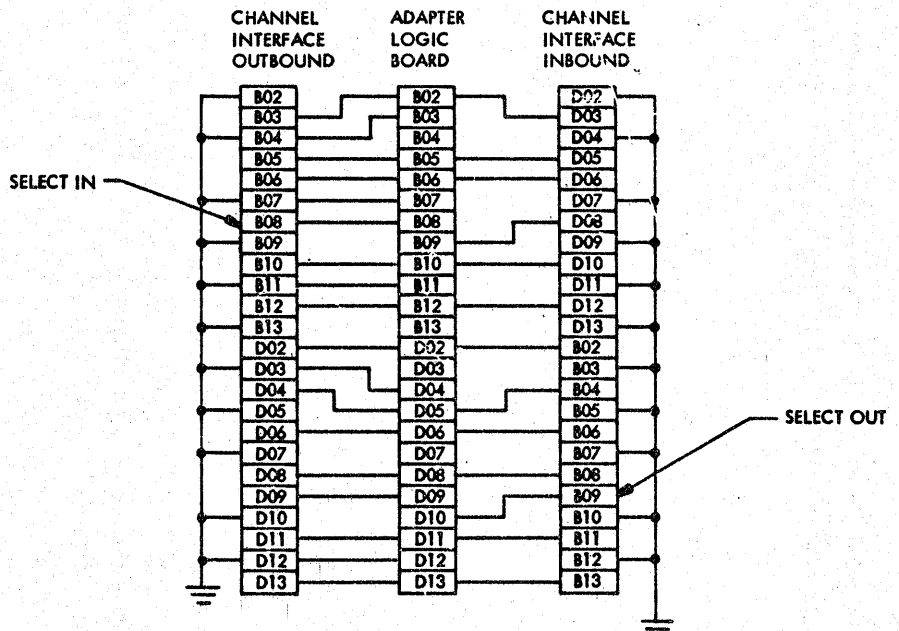
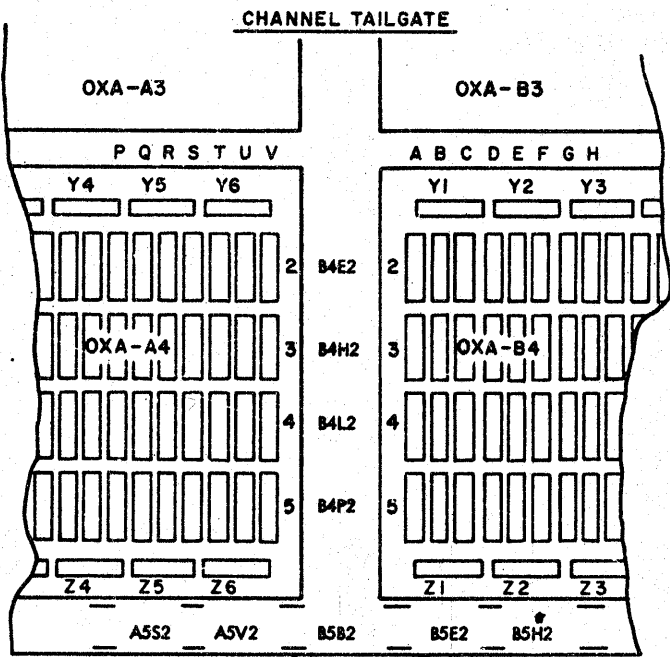
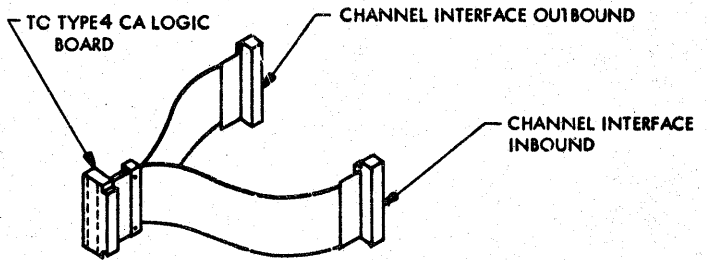
**SELECT IN - SELECT OUT JUMPERING**

THE 3705 IS FACTORY WIRED TO TRAP ON SELECT-OUT. IN ORDER TO TRAP ON SELECT-IN, THE FOLLOWING BOARD WIRING CHANGES MUST BE MADE TO THE TYPE 4 CHANNEL ADAPTERS:

DELETE	PIN	IMAGE	NET NUMBER	ADD	TYPE	FROM	TO
(STEP 1) INTERFACE	A						
	V4808	W	PA015DF2	ADD	YEL	V4D10	V4D09
	V4809	W					
	V4D10	W	PA015DF6	ADD	YEL	V4808	T4807
	T4807	W					
	P2D02	W					
	V4D09	W	PJIO1FA4	ADD	YEL	V4809	T4802
	T4802	W					
	Q2M12	W					
(STEP 2) INTERFACE	B						
	U4808	W	PA017DF2	ADD	YEL	U4D10	U4D09
	U4809	W					
	U4D10	W	PA017DF6	ADD	YEL	U4808	T4G09
	T4G09	W					
	R2M04	W					
	U4D09	W	PJIO2FA4	ADD	YEL	U4809	T4G03
	T4G03	W					
	S2M12	W					

INDICATE CHANGES LISTED ON APPROPRIATE ALD PAGES.

TAILGATE "Y" CABLE - P/N 5997539



	A	B	A	B	
BUS OUT	NTO AAV2	NTO AAV3	NTO AAU2	NTO AAU3	1 LIGHT GY CONN
BUS IN	NTO AAV2	NTO AAV3	NTO AAU2	NTO AAU3	2 DARK GY CONN
TAG OUT	NTO AAV4	NTO AAV5	NTO AAU4	NTO AAU5	3 LIGHT GY CONN
TAG IN	NTO AAV4	NTO AAV5	NTO AAU4	NTO AAU5	4 DARK GY CONN

OXT-A1 INTERFACE A      OXU-A1 INTERFACE B (TPS)

CARD SIDE - LOOKING INTO I/O GATE  
LIGHT GRAY - 5362304 (A STYLE)  
DARK GRAY - 5362305 (B STYLE)

NOTE: SEE PAGE 2 OF PA052 TO REFERENCE THE CHANNEL TAILGATE ASSEMBLIES WHEN CA-4'S ARE INSTALLED IN THE O1A-B1 AND/OR O2A-B4 BOARD POSITIONS.

	FROM OXA-A4 BOARD		TO RACEWAY		TO RACEWAY		TO I/O PANEL SOCKET LOCATION
	SOCKET LOCATION	DIRECTION OF FOLD	LOCATION	DIRECTION OF FOLD	LOCATION	DIRECTION OF FOLD	
FIRST CHANNEL	V2	↘	B5B2	↘	A5V2	↘	OXT-A1A1 A1A2
	V3	↘	B5B2	NO FOLD			OXT-A1B1 A1B2
	V4	↘	B5B2	↘	B5V2	↘	OXT-A1A3 A1A4
	V5	↘	B5B2	NO FOLD			OXT-A1B3 A1B4
SECOND CHANNEL (TPS)	U2	↘	B5B2	↘	B5E2	↘	OXU-A1A1 A1A2
	U3	↘	B5B2	↘	B5H2	↘	OXU-A1B1 A1B2
	U4	↘	B5B2	↘	B5E2	↘	OXU-A1A3 A1A4
	U5	↘	B5B2	↘	B5H2	↘	OXU-A1B3 A1B4

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IBM				DATE	CHANGE NO	DATE	CHANGE NO
NAME	JUMPERING TYPE 4 CA			FEB 76	314402		
REFERENCE PAGE				MAY 76	314424		
DESIGN	DJR	FEB 76	SHT 1 OF 2	NOV 76	316677		
DETAIL	AWL	FEB 76					
CHECK			CLASSIFICATION	MUST CONFORM TO ENG SPEC		DEVELOPMENT NO	
APPRO	DJR	FEB 76				LOGIC PG NO	PA052

**1755108 B**

1755108 B

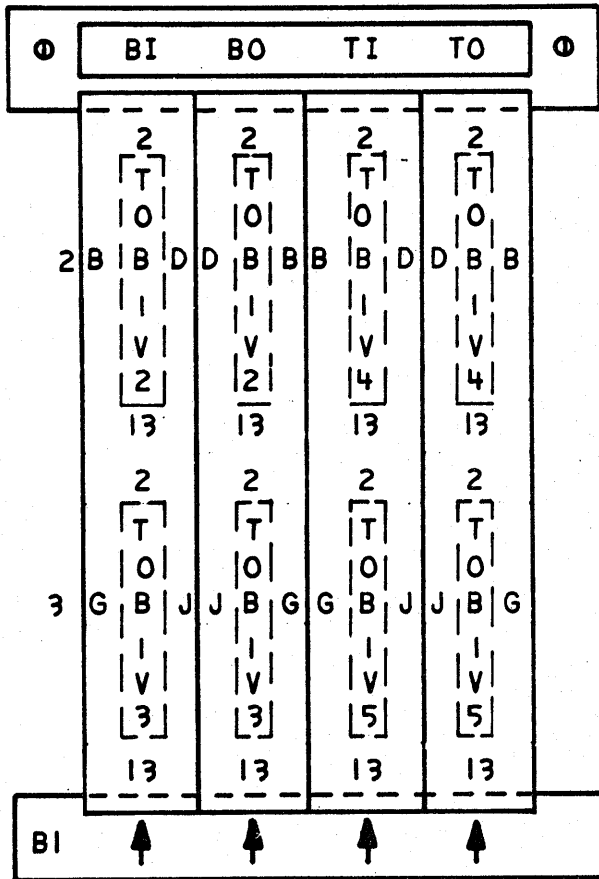
PART NO  
**1755108**

LOGIC PG NO  
PA052

PAGE 2 OF 2

CA-4 01A-B1 POSITION  
TAILGATE ASSEMBLY

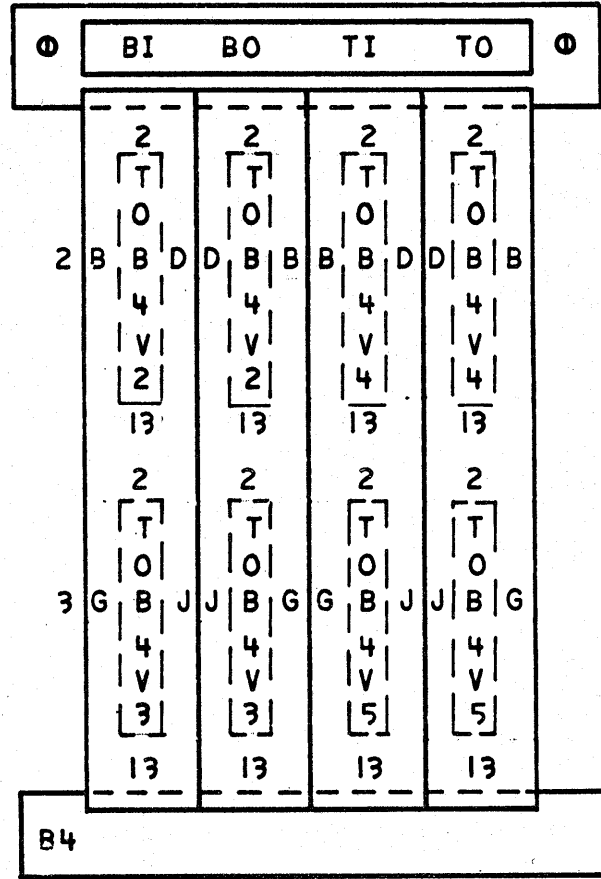
R S T U



5	5	5	5
3	3	3	3
6	6	6	6
2	2	2	2
3	3	3	3
0	0	0	0
5	4	5	4
D	L	D	L
R	T	R	T
K		K	
	G		G
G	R	G	R
R	A	R	A
A	Y	A	Y
Y		Y	

CA-4 02A-B4 POSITION  
TAILGATE ASSEMBLY

R S T U



5	5	5	5
3	3	3	3
6	6	6	6
2	2	2	2
3	3	3	3
0	0	0	0
5	4	5	4
D	L	D	L
R	T	R	T
K		K	
	G		G
G	R	G	R
R	A	R	A
A	Y	A	Y
Y		Y	

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<b>IBM</b>		DATE	CHANGE NO	DATE	CHANGE NO
NAME		NOV76	316677		
DESIGN		SHT 2 OF 2			
DETAIL					
CHECK		CLASSIFICATION	MUST CONFORM TO ENG SPEC	DEVELOPMENT NO	LOGIC PG NO
APPRO					PA052

1755108 B

VERTICAL ELECTRICAL FORMAT

MRO# 78022203

620-0133-1

ASTROCLON 19801

1755109 B

PART NO  
**1755109**

LOGIC PG NO  
**PA053**

SELECTED SIGNAL PROPAGATION, IPL SELECT JUMPERING AND ROS REQUIREMENTS

PG. 1 OF 6

FOR A LOGICAL REPRESENTATION OF THIS JUMPERING SEE PAGE PA060, PAGE 1. THE JUMPER TO BE USED IS P/N 816645. REFER TO PAGE PA053 PG. 2 OF 6 FOR BOARD PIN LOCATIONS, IF NECESSARY. IGNORE THE JUMPER REMOVALS AND ADDS IF JUMPERS ARE NOT PRESENT OR ALREADY ON. THE ROS CARD IS LOCATED IN THE 01A-B4F2 SOCKET. DETERMINE YOUR CHANNEL CONFIGURATION AND JUMPER ACCORDINGLY.

- FRAME BOARD POSITION
- CONFIGURATION 1  
(TYPE 1 ROS **2**)  
OR (N-CHANNEL ROS **2**)  
OR (DUAL CA ROS **2**)
- CONFIGURATION 2  
(DUAL CA ROS **2**)
- CONFIGURATION 3  
WITH IPL SOURCE SW **1**  
(TYPE 1 ROS **2**)
- CONFIGURATION 3  
(N-CHANNEL ROS **2**)
- CONFIGURATION 4  
WITH IPL SOURCE SW **1**  
(TYPE 1 ROS **2**)
- CONFIGURATION 4  
(N-CHANNEL ROS **2**)
- CONFIGURATION 5  
WITH CA-2 OR 3  
(DUAL CA ROS **2**)
- CONFIGURATION 6  
(N-CHANNEL ROS **2**)
- CONFIGURATION 7  
(N-CHANNEL ROS **2**)
- CONFIGURATION 8  
(N-CHANNEL ROS **2**)

3705		3705 1ST EXPANSION FRAME	
01A-B1	01A-A4	02A-B4	02A-A4
	CA-4 ADD F2J09-F2J08 ADD N1A13-N1B13 ADD F6C04-F6B04		
CA-4 ADD F2J09-F2J08 ADD N1A13-N1B13 ADD F6C04-F6B04	CA-2		
CA-4 RMV F2J09-F2J08 RMV F6C04-F6B04 ADD N1A13-N1B13	CA-4 RMV F2J09-F2J08 RMV N1A13-N1B13 ADD F6C04-F6B04		
CA-4 RMV F6C04-F6B04 ADD F2J09-F2J08 ADD N1A13-N1B13	CA-4 RMV N1A13-N1B13 ADD F2J09-F2J08 ADD F6C04-F6B04		
	CA-4 RMV F6C04-F6B04 RMV F2J09-F2J08 ADD N1A13-N1B13		CA-4 RMV N1A13-N1B13 RMV F2J09-F2J08 ADD F6C04-F6B04
	CA-4 RMV F6C04-F6B04 ADD F2J09-F2J08 ADD N1A13-N1B13		CA-4 RMV N1A13-N1B13 ADD F2J09-F2J08 ADD F6C04-F6B04
	CA-4 ADD F2J09-F2J08 ADD N1A13-N1B13 ADD F6C04-F6B04		CA-2 OR CA-3
CA-4 RMV F6C04 F6B04 ADD F2J09-F2J08 ADD N1A13-N1B13	CA-4 RMV F6C04-F6B04 RMV N1A13-N1B13 ADD F2J09-F2J08		CA-4 RMV N1A13-N1B13 ADD F2J09-F2J08 ADD F6C04-F6B04
	CA-4 RMV F6C04-F6B04 ADD F2J09-F2J08 ADD N1A13 N1B13	CA-4 RMV N1A13-N1B13 RMV F6C04-F6B04 ADD F2J09-F2J08	CA-4 RMV N1A13-N1B13 ADD F2J09-F2J08 ADD F6C04-F6B04
CA-4 RMV F6C04-F6B04 ADD F2J09-F2J08 ADD N1A13-N1B13	CA-4 RMV N1A13-N1B13 RMV F6C04-F6B04 ADD F2J09-F2J08	CA-4 RMV N1A13-N1B13 RMV F6C04-F6B04 ADD F2J09-F2J08	CA-4 RMV N1A13-N1B13 ADD F2J09-F2J08 ADD F6C04-F6B04

- 1** THIS CONFIGURATION IS VALID IF AN IPL SOURCE SWITCH IS INSTALLED IN PANEL SWITCH POSITION 13 OR 14. REFER TO THE FIGURE ON PA053, PG 2 OF 6, TO DETERMINE IF AN IPL SOURCE SWITCH IS INSTALLED.
- 2** FOR ROS CARD P/N'S AND B/M NUMBERS, REFERENCE SECTION 4 OF THE YZ000A PAGES IN 3705 VOLUME I. ALSO REFERENCE THE YZ000A PAGES, SECTION 4, TO DETERMINE TYPE 1 ROS AND AN IPL SOURCE SWITCH OR N-ROS REQUIREMENTS FOR A TWO CA-4 CONFIGURATION, CONFIGURATIONS 3 AND 4 ABOVE.

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<b>IBM</b>		DATE	CHANGE NO	DATE	CHANGE NO
NAME		NOV 76	316677		
		JAN 79	318589		
DESIGN	SHT   OF 6				
DETAIL					
CHECK	CLASSIFICATION	MUST CONFORM TO ENG SPEC	DEVELOPMENT NO	LOGIC PG NO	
APPRO					

620-0133-1 M102 78082203 VERTICAL ELECTRICAL FORMAT

ASTROCLOTH 48301

1755109 B

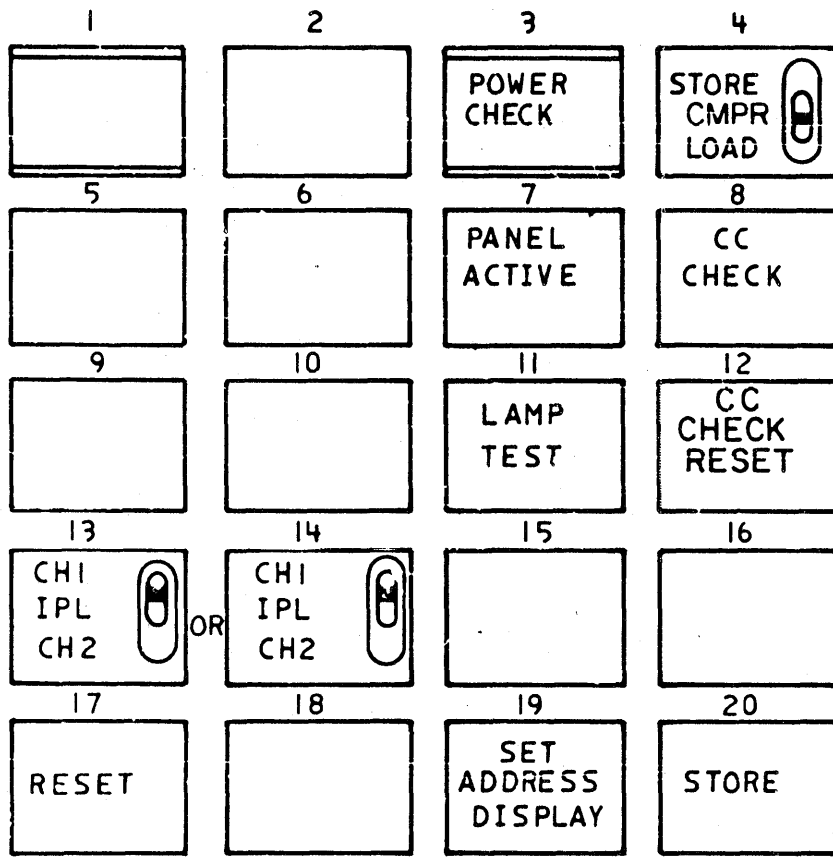
B

PART NO  
1755109

LOGIC PG NO  
PA053

PAGE 2 OF 6

OPERATORS PANEL IPL SOURCE SWITCH REFERENCE



IPL SOURCE SWITCH. POSITION 13 OR 14.

- (A) F2J09-F2J08
- (B) N1A13-N1B13
- (C) F6C04-F6B04

BOARD JUMPER PIN LOCATION REFERENCE

V	U	T	S	R	Q	P	N	M	L	K	J	H	G	F	E	D	C	B	A	
							.....													1
							(B)													2
														(A)	.....					3
																				4
																				5
																.....				6

CA-4 BOARD  
01A-B1, 01A-A4, 02A-B4 AND/OR 02A-A4  
PIN SIDE

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VERTICAL ELECTRICAL FORMAT  
MROZ 780522203  
620-0133-1

<b>IBM</b>				DATE	CHANGE NO	DATE	CHANGE NO
NAME				FEB 76	314402		
				MAY 76	314424		
DESIGN	DJR	FEB 76	SHT 2 OF 6	NOV 76	316677		
DETAIL	RB	FEB 76		JAN 79	318589		
CHECK			CLASSIFICATION	MUST CONFORM TO ENG SPEC	DEVELOPMENT NO	LOGIC PG NO	
APPRO						PA053	

1755109  
B

ASTRO/CTH NS801



1755109 B

PART NO  
1755109

LOGIC PG NO  
PA053

PG. 3 OF 6

N-CHANNEL ROS CA-4 REWORK

THE FOLLOWING BOARD DELETE AND ADD MUST BE PERFORMED ON ALL CA-4 BOARDS INSTALLED, IF N-CHANNEL ROS IS INSTALLED. THE REWORK ALTERS THE CA-4 BOARDS SUCH THAT ALL CA-4'S MAY BE SELECTED VIA AN OUTPUT AND MAY HAVE AN AUTOMATIC PI SET VIA AN OUTPUT IN THE NOT INITIALIZED STATE. WITHOUT N-ROS, THOSE TWO FUNCTIONS HAVE NO AFFECT UNLESS THE NOT INITIALIZED STATE IS RESET VIA AN OUTPUT X'77; DATA ='S 8000. THE CHANGE IS ACCOMPLISHED BY DELETING THE SIGNAL-NOT INITIALIZED DLYD (PQ104GK6) FROM THE F2 CARD AND SUBSTITUTING THE SIGNAL-POR OR RESET SW (PQ104DA2). SEE PAGE PA108 FOR THE LOGIC REPRESENTATION. CHECK FOR A RED WIRE BETWEEN F2J12 AND F2D07. IF THE WIRE IS NOT PRESENT, PERFORM THE DELETE AND ADD, USING RED WIRE, P/N 81693.

NET NAME	DELETE	PIN	IMAGE	NET NAME	ADD	TYPE	FROM	TO
-NOT INITIALIZED DLYD	I	E2M04	W]	PQ104GK6				
	I	F2D07	W]					
-POR OR RESET SW				PQ104DA2	ADD	RED	E2S08 F2J12	F2J12 F2D07

VERTICAL ELECTRICAL FORMAT

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<b>IBM</b>		DATE	CHANGE NO	DATE	CHANGE NO
NAME		NOV76	316677		
		JAN79	318589		
DESIGN	SHT 3 OF 6				
DETAIL					
CHECK	CLASSIFICATION	MUST CONFORM TO ENG SPEC	DEVELOPMENT NO	LOGIC PG NO	
APPRO				PA053	

620.0133-1 MRO# 780522203

1755109

B

IBM DATA CENTER, NEW YORK, NY 10014 U.S.A. 13700107M 04/80



1755109 B

PART NO  
**1755109**

LOGIC PG NO  
**PA053**


PG. 4 OF 6

O2A-B4 ENABLE / DISABLE REWORK (4 CA-4'S INSTALLED)

THE FOLLOWING BOARD REWORK ALTERS THE ENABLE, DISABLE AND ENABLED BOARD WIRING ON THE O2A-B4 CHANNEL ADAPTER TYPE 4 BOARD. AFTER THE ALTERATION, THE O2A-B4 BOARD WILL USE THE LOGICAL SIGNALS: ENABLE, DISABLE AND ENABLED CHAN 1 INTERFACE B SIGNALS FOR ITS A INTERFACE CONTROL. THE O2A-B4 BOARD WILL USE THE LOGICAL SIGNALS: ENABLE, DISABLE AND ENABLED CHAN 2 INTERFACE B SIGNALS FOR ITS A INTERFACE CONTROL. THE REWORK ON PA053, PG. 5 OF 6 MUST ALSO BE COMPLETED. A LOGICAL REPRESENTATION OF THE ENABLE / DISABLE SIGNAL PROPAGATION IS ON PAGE PA061, PG. 3 OF 3. CHECK FOR A RED WIRE ON THE O2A-B4 BOARD FROM C3B09 TO C5B02. IF THIS WIRE IS NOT PRESENT, PERFORM THE FOLLOWING REWORK ON THE O2A-B4 BOARD USING RED WIRE, P/N 811693. PERFORM ALL DELETES FIRST.

NET NAME	DELETE	PIN	IMAGE	NET NAME	ADD	TYPE	FROM	TO
+ CHAN 2 ENBL INTF B	1	C3B09	W	PA012DJ4	ADD	RED	C3B09	C5B02
	1	C5B08	W					
+ CHAN 2 DSBL INTF B	1	C3B12	W	PA012DK1	ADD	RED	C3B12	C5B05
	1	C5B10	W					
+ CHAN 2 INTF B ENABLED	1	C3D05	W	PA012DK5	ADD	RED	C3D05	C5B13
	1	C5D03	W					
+ CHAN 2 ENBL INTF A	1	C3B04	W	PA012DH3				
	1	C5B02	W					
+ CHAN 2 DSBL INTF A	1	C3B06	W	PA012DH7				
	1	C5B05	W					
+ CHAN 2 INTF A ENABLED	1	C3D02	W	PA012DK3				
	1	C5B13	W					
+ CHAN 1 ENBL INTF A	1	C3B02	W	PA012DH1				
	2	P2M05	W					
	1	C5B04	W					
+ CHAN 1 DSBL INTF A	1	C3B05	W	PA012DH5				
	2	P2P04	W					
	1	C5B06	W					
+ CHAN 1 INTF A ENABLED	1	C5D02	W	PB103FK2	ADD	RED	C3D03	P2U02
	2	P2U02	W					
	1	C3B13	W					
+ CHAN 1 ENBL INTF B	1	C3B08	W	PA012DJ2	ADD	RED	C3B08	P2M05
	2	R2M05	W					
	1	C5B09	W					
+ CHAN 1 DSBL INTF B	1	C3B10	W	PA012DJ6	ADD	RED	C3B10	P2P04
	2	R2S02	W					
	1	C5B12	W					
+ CHAN 1 INTF B ENABLED	1	C3D03	W	PR105GD2				
	2	R2U05	W					
	1	C5D05	W					

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		DATE	CHANGE NO	DATE	CHANGE NO
		NOV76	316677		
NAME		JAN79	318589		
DESIGN	SHT 4 OF 6				
DETAIL					
CHECK	CLASSIFICATION	MUST CONFORM TO ENG SPEC	DEVELOPMENT NO	LOGIC PG NO	
APPRO				PA053	

1755109 B

620 0133-1 MRO# 780522203 VERTICAL ELECTRICAL FORMAT

ASTROCLON N8807

1755109 B

PART NO

1755109

LOGIC PG NO

PA053

PG. 5 OF 6

02A-A4 ENABLE / DISABLE REWORK (4 CA-4'S INSTALLED)

THE FOLLOWING REWORK RESTORES THE 02A-A4 BOARD TO ITS ORIGINAL ENABLE AND DISABLE BOARD WIRING. A LOGICAL REPRESENTATION OF THE ENABLE / DISABLE SIGNAL PROPAGATION IS ON PAGE PA061. CHECK FOR A RED WIRE ON THE 02A-A4 BOARD FROM C3B09 TO P2M05. IF THIS WIRE IS PRESENT, PERFORM THE FOLLOWING REWORK ON THE 02A-A4 BOARD USING YELLOW WIRE. PERFORM ALL DELETES FIRST.

NET NAME	DELETE	PIN	IMAGE	NET NAME	ADD	TYPE	FROM	TO
+ CHAN 2 ENBL INTF B		C3B09 P2M05	W W	PA012DJ4	ADD	YEL	C3B09	C5B08
+ CHAN 2 DSBL INTF B		C3B12 P2P04	W W	PA012DK1	ADD	YEL	C3B12	C5B10
+ CHAN 2 INTF B ENABLED				PA012DK5	ADD	YEL	C3D05	C5D03
+ CHAN 1 ENBL INTF A				PA012DH1	ADD ADD	YEL YEL	C3B02 P2M05	P2M05 C5B04
+ CHAN 1 DSBL INTF A				PA012DH5	ADD ADD	YEL YEL	C3B05 P2P04	P2P04 C5B06
+ CHAN 1 INTF A ENABLED		C3D05 P2U02	W W	PB103FK2	ADD ADD	YEL YEL	C5D02 P2U02	P2UC2 C3B13

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620-0133-1 MRO# 780522203 VERTICAL ELECTRICAL FORMAT

<b>IBM</b>		DATE	CHANGE NO	DATE	CHANGE NO
NAME		NOV76	316677		
		JAN79	318589		
DESIGN	SHT 5 OF 6				
DETAIL					
CHECK	CLASSIFICATION	MUST CONFORM TO ENG SPEC		DEVELOPMENT NO	LOGIC PG NO
APPRO					PA053

1755109 B

AS110CLOTH N6501

1755109 B

PART NO  
1755109

LOGIC PG NO  
PA053

PG. 6 OF 6

02A-A4 ENABLE / DISABLE REWORK (3 CA-4'S INSTALLED)

THE FOLLOWING BOARD REWORK ALTERS THE ENABLE, DISABLE AND ENABLED BOARD WIRING ON THE 02A-A4 CHANNEL ADAPTER TYPE 4 BOARD. AFTER THE ALTERATION, THE 02A-A4 BOARD WILL USE THE LOGICAL SIGNALS: ENABLE, DISABLE AND ENABLED CHAN 2 INTERFACE B SIGNALS FOR ITS A INTERFACE CONTROL. A LOGICAL REPRESENTATION OF THE ENABLE / DISABLE SIGNAL PROPAGATION IS ON PAGE PA061, PG. 2 OF 3. CHECK FOR A RED WIRE ON THE 02A-A4 BOARD FROM C3B09 TO P2M05. IF THIS WIRE IS NOT PRESENT, PERFORM THE FOLLOWING REWORK ON THE 02A-A4 BOARD USING RED WIRE, P/N 811693. PERFORM ALL DELETES FIRST.

NET NAME	DELETE	PIN	IMAGE	NET NAME	ADD	TYPE	FROM	TO
+ CHAN 2 ENBL INTF B	1	C3B09	W	PA012DJ4	ADD	RED	C3B09	P2M05
	1	C5B08	W					
+ CHAN 2 DSBL INTF B	1	C3B12	W	PA012DK1	ADD	RED	C3B12	P2P04
	1	C5B10	W					
+ CHAN 2 INTF B ENABLED	1	C3D05	W	PA012DK5				
	1	C5D03	W					
+ CHAN 1 ENBL INTF A	1	C3B02	W	PA012DH1				
	2	P2M05	W					
	1	C5B04	W					
+ CHAN 1 DSBL INTF A	1	C3B05	W	PA012DH5				
	2	P2P04	W					
	1	C5B06	W					
+ CHAN 1 INTF A ENABLED	1	C5D02	W	PB103FK2	ADD	RED	C3D05	P2U02
	2	P2U02	W					
	1	C3B13	W					

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IBM		DATE	CHANGE NO	DATE	CHANGE NO
		NOV76	316677		
NAME		JAN79	318589		
DESIGN		SHT 6 OF 6			
DETAIL					
CHECK		CLASSIFICATION	MUST CONFORM TO ENG SPEC	DEVELOPMENT NO	LOGIC PG NO
APPRO					PA053

1755109 B

620-0133-1 MRO# 780522203 VERTICAL ELECTRICAL FORMAT

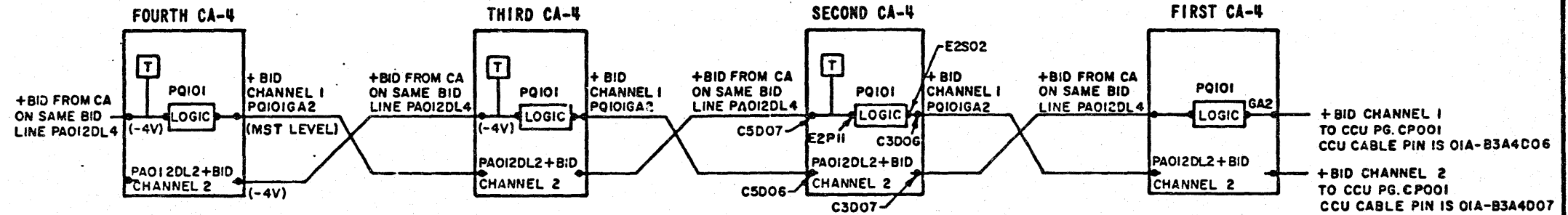
ASTROCLON NS301

APPRC	CHECK	DETAIL	DESIGN	NAME	DATE	CHANGE NO	DATE	CHANGE NO	CLASSIFICATION	MUST CONFORM TO ENG SPEC	DEVELOPMENT NO	LOGIC PG NO	1755110	B			
															SHT 1 OF 1		
															RED	MAR 76	314902
																NOV 76	316677

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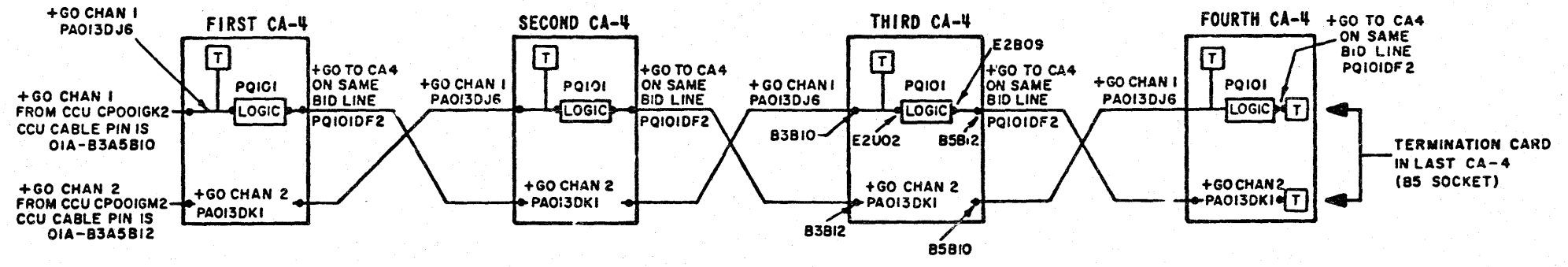
CA-4 CYCLE STEAL BID PROPAGATION (1-4 CA-4'S MAY BE INSTALLED)

1755110 B



**NOTE:** + BID FROM CA ON SAME BID LINE AND + BID CHANNEL 2 WILL BE AT MST LEVELS ONLY AFTER THEY HAVE BEEN REDRIVEN THROUGH A CA-4. PIN LOCATIONS ARE THE SAME FOR ALL CA-4'S AND ARE SHOWN ON THE 2ND CA-4. THESE SIGNALS ARE PASSED STRAIGHT THROUGH BY THE TYPE 3 SCANNER IF IT IS IN BETWEEN THE CA-4'S. THE CABLE, CABLE 4, ENTERS THE TYPE 3 SCANNER AT THE OXA-A3B3 SOCKET AND EXITS AT THE OXA-A3B3 SOCKET. IF THE REMOTE PROGRAM LOADER IS INSTALLED IN THE OIA-B1 BOARD POSITION, IT PASSES THESE SIGNALS STRAIGHT THROUGH. THE SIGNALS ENTER THE REMOTE AT THE OIA-B1C5 CABLE SOCKET AND EXIT TO THE CCU AT THE OIA-B1C3 CABLE SOCKET. THE CROSSING OF SIGNALS IS DONE ON THE CA-4 BOARD AND NOT IN THE CABLE.

CA-4 CYCLE STEAL GO PROPAGATION (1-4 CA-4'S MAY BE INSTALLED)



**NOTE:** PIN LOCATIONS ARE THE SAME FOR ALL CA-4'S AND ARE SHOWN ON THE 3RD CA-4. THESE SIGNALS ARE PASSED STRAIGHT THROUGH BY THE TYPE 3 SCANNER IF IT IS IN BETWEEN THE CA-4'S. THE CABLE, CABLE 6, ENTERS THE TYPE 3 SCANNER AT THE OXA-A3B4 SOCKET AND EXITS AT THE OXA-A3Z2 SOCKET. IF THE REMOTE PROGRAM LOADER IS INSTALLED IN THE OIA-B1 BOARD POSITION, IT PASSES THESE SIGNALS STRAIGHT THROUGH. THE SIGNALS ENTER THE REMOTE FROM THE CCU AT THE OIA-B1B3 CABLE SOCKET AND EXIT AT THE OIA-B1B5 CABLE SOCKET. THE CROSSING OF SIGNALS IS DONE ON THE CA-4 BOARD AND NOT IN THE CABLE.

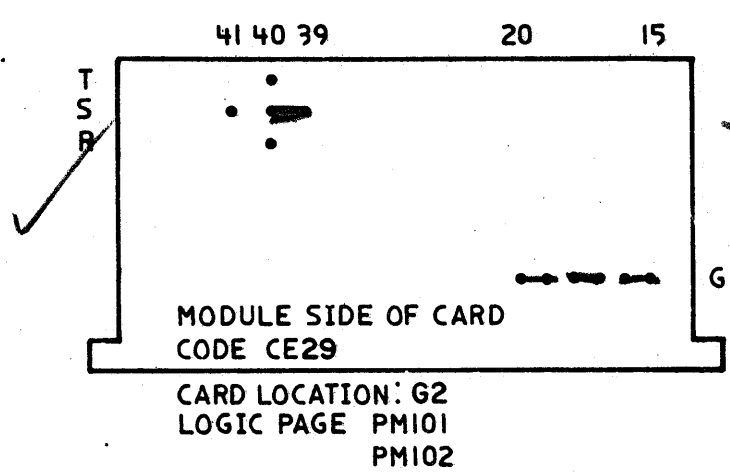
PART NO 1755110 LOGIC PG NO PA054

1755116 B

PART NO  
**1755116**

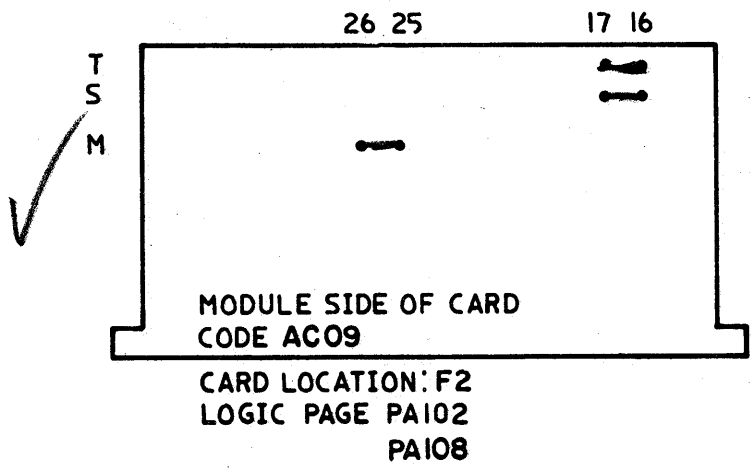
LOGIC PG NO  
PA055

TYPE 4 CHANNEL ADAPTER JUMPER INFORMATION  
(USE JUMPER P/N 816645 IN SHIPPING GROUP)



(STEP 1)  
MULTIPLE CA-4 SAMPLE TIMES (CE29)  
FIRST CA-4 JUMP S40 TO S39 ONLY  
SECOND CA-4 JUMP S40 TO R40 ONLY  
THIRD CA-4 JUMP S40 TO S41 ONLY  
FOURTH CA-4 JUMP S40 TO T40 ONLY

(STEP 2)  
LAST CA-4 IN 3705 (CE29)  
JUMP G15-G16, G17-G18, AND G19-G20 IF THIS IS THE LAST CA4 IN THE 3705



(STEP 3)  
CA-4 POSITION IDENTIFIER (AC09)  
FIRST CA-4 - JUMP T17-T16  
                  JUMP S17-S16  
                  JUMP M25-M26  
SECOND CA-4 - JUMP S17-S16 ONLY  
THIRD CA-4 - JUMP T17-T16 ONLY  
FOURTH CA-4 - NO JUMPERS

NOTE: REFER TO CONFIGURATION CHARTS ON PA048, PAGE 1, TO DETERMINE THE FIRST, SECOND, THIRD, FOURTH OR LAST CA-4

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620-0133-1 MRO# 78052203 VERTICAL ELECTRICAL FORMAT

<b>IBM</b>				DATE	CHANGE NO	DATE	CHANGE NO	1755116 B
NAME	TYPE 4 CA REFERENCE PAGE			MAY 76	314424			
				NOV76	316677			
DESIGN	DJR	FEB76	SHT 1 OF 1					
DETAIL	TS	JUN76						
CHECK			CLASSIFICATION	MUST CONFORM TO ENG SPEC	DEVELOPMENT NO	LOGIC PG NO		
APPRO			DJR FEB76			PA055		

ASTROCLON N5507

1750419 B

PART NO  
1750419

LOGIC PG NO  
PA056

(BYTE MULTIPLEX CHANNEL ONLY)

CA-4 BURST LENGTH AND DURATION OF DELAY PLUGGING OPTIONS

IN ORDER TO PREVENT THE OCCURENCE OF CPU CONDITICH CODE 2 SITUATIONS (MULTIPLEX CHANNEL IN BURST MODE AND A SIO IS ISSUED) THE 3705 CANNOT BE PLUGGED FOR BURSTING A GREATER NUMBER OF BYTES THAN THE CHART AT THE BOTTOM OF PAGE PA049 SPECIFIES. THE PLUGGING SPECIFIED IS TO GIVE THE 3705 MAXIMUM THRUPUT CAPABILITY. THIS PLUGGING MAY BE CHANGED IF THE 3705 IS CAUSING MULTIPLEX CHANNEL INTERFERENCE FOR OTHER DEVICES, E.G. 250I OVERRUNS, EXCEPT THAT ANY CHANGE MUST BE A DECREASE IN THE NUMBER OF BYTES IN THE BURST SPECIFIED OR AN INCREASE IN THE DURATION OF DELAY BETWEEN BURSTS.

EXAMPLE: THE CHART ON PG. PA049 SPECIFIES THAT THE 3705 ATTACHED TO A 370-3145 BE ALLOWED TO BURST THE FULL BYTE COUNT. IF IT IS NECESSARY TO CHANGE THIS PLUGGING OPTION THE 3705 CAN BE PLUGGED FOR 16, 8, OR 4 BYTE BURSTS WITH A DURATION OF DELAY OF APPROXIMATELY 0.5 US OR 32 US.

NOTE 1: IF THE 3705 IS PLUGGED FOR A BURST LENGTH IT MUST ALSO BE PLUGGED FOR A DURATION OF DELAY.

NOTE 2: THE PLUGGING OPTIONS ARE INDEPENDENT OF THE EP SYSGEN OPTION THAT SPECIFIES THE NUMBER OF BYTES EP WILL REQUEST TO HAVE TRANSFERRED OVER THE CHANNEL ADAPTER TYPE 4.

NOTE 3: THE EB OR CS BURST LENGTH AND DURATION OF DELAY MUST BE PLUGGED TO BURST THE FULL BYTE COUNT (NO JUMPERS), IF THE CA-4 IS ATTACHED TO A BLOCK MULTIPLEX OR SELECTOR CHANNEL. INTERFACE CONTROL CHECKS WILL RESULT IF ANY JUMPERS ARE PRESENT.

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<b>IBM</b>		DATE	CHANGE NO	DATE	CHANGE NO	1750419 B
NAME		SEPT76	315620			
		NOV76	316677			
DESIGN	SHT   OF					
DETAIL						
CHECK	CLASSIFICATION	MUST CONFORM TO ENG SPEC	DEVELOPMENT NO	LOGIC PG NO		
APPRO				PA056		

VERTICAL ELECTRICAL FORMAT

MRO# 780522203

620-0133-1

ASTROCLON N6501

1755111 B

CA-4 CABLE #10

DEPICTED BELOW IS THE SIGNAL PROPAGATION FOR CABLE #10. THE SIGNAL TITLES ARE GIVEN WITH THE 1ST CA-4 AND PIN LOCATIONS WITH THE 2ND CA-4. THESE SIGNAL TITLES AND PIN LOCATIONS ARE THE SAME FOR ALL CA-4'S. THE JUMPER LOCATIONS ARE DEPENDENT UPON THE NUMBER OF CA-4'S INSTALLED. REFER TO PA048, PG. 1 OF 3 TO DETERMINE YOUR CHANNEL CONFIGURATION.

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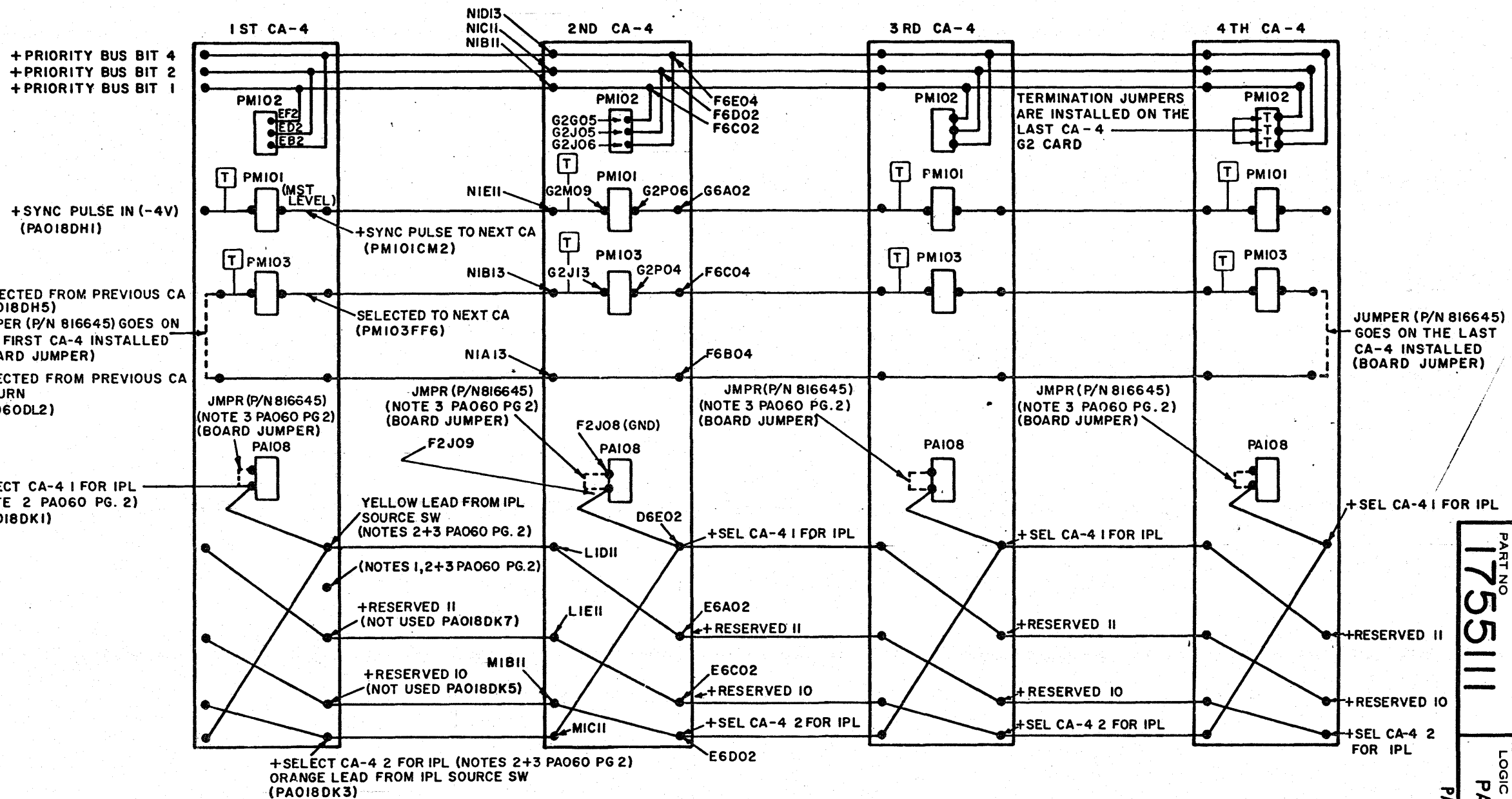
IBM

APPRO	
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DETAIL	
DESIGN	SHT 1 OF 2
NAME	

CLASSIFICATION	
MUST CONFORM TO ENG SPEC	
DATE	FEB 76
CHANGE NO	314402
DATE	NOV 76
CHANGE NO	316677

DEVELOPMENT NO	
LOGIC PG NO	PA060

1755111 B



PART NO 1755111 LOGIC PG NO PA060 PAGE 1 OF 2

1755111 B

PART NO  
1755111

LOGIC PG NO  
PA060

PG 2 OF 2

- NOTE 1: THE BLACK LEAD FROM THE IPL SOURCE SWITCH SLIPS ON PIN E6B02 AND SUPPLIES GND TO EITHER PIN D6E02 OR PIN E6D02 DEPENDENT UPON WHETHER THE SWITCH IS SELECTING CH 1 OR CH 2 FOR IPL.
- NOTE 2: SEE PA053 PG. 2 FOR THE LOCATION OF THE IPL SOURCE SWITCH. IF AN IPL SOURCE SWITCH IS INSTALLED, TWO AND ONLY 2 CA-4'S MAY BE INSTALLED. NO JUMPERS ARE INSTALLED BETWEEN F2J09 AND F2J08.
- NOTE 3: THE SELECT CA-4 1 FOR IPL SIGNAL IS A GROUND SOURCE USED TO SELECT ONE OR MORE CA-4'S AND ALLOW THEIR NSC ADDRESSES TO BECOME OPERATIONAL AFTER A POWER ON RESET OR RESET SWITCH RESET SEQUENCE. IF A SINGLE CA-4 IS INSTALLED, A JUMPER IS INSTALLED BETWEEN F2J08 AND F2J09 TO SUPPLY THE GND.

IF AN IPL SOURCE SWITCH IS INSTALLED, ONLY THE FIRST OR SECOND CA-4 WILL SELECT AND HAVE IT'S NSC OPERATIONAL DEPENDENT UPON WHICH CA-4 THE IPL SOURCE SWITCH IS SET TO. AFTER THE 3705 BECOMES INITIALIZED, THE OPPOSITE CA-4 MAY BE SELECTED BUT IT'S NSC ADDRESS IS INOPERATIVE.

IF N-CHANNEL ROS IS INSTALLED, F2J09 IS JUMPERED TO F2J08 ON ALL CA-4'S AND ALL BECOME SELECTED WITH THEIR NATIVE SUBCHANNELS OPERATIONAL WITH EACH POR OR RESET SWITCH RESET. N-CHANNEL ROS CONTROLS THE CHANNELS SUCH THAT ONLY 1 WILL END UP SELECTED.

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620-0133-1 MRO# 78082203 VERTICAL ELECTRICAL FORMAT

<b>IBM</b>		DATE	CHANGE NO	DATE	CHANGE NO
NAME		NOV76	316677		
DESIGN	SHT 2 OF 2				
DETAIL					
CHECK	CLASSIFICATION	MUST CONFORM TO ENG SPEC	DEVELOPMENT NO	LOGIC PG NO	
APPRO				PA060	

1755111 B

ASTROCLOT# N690T

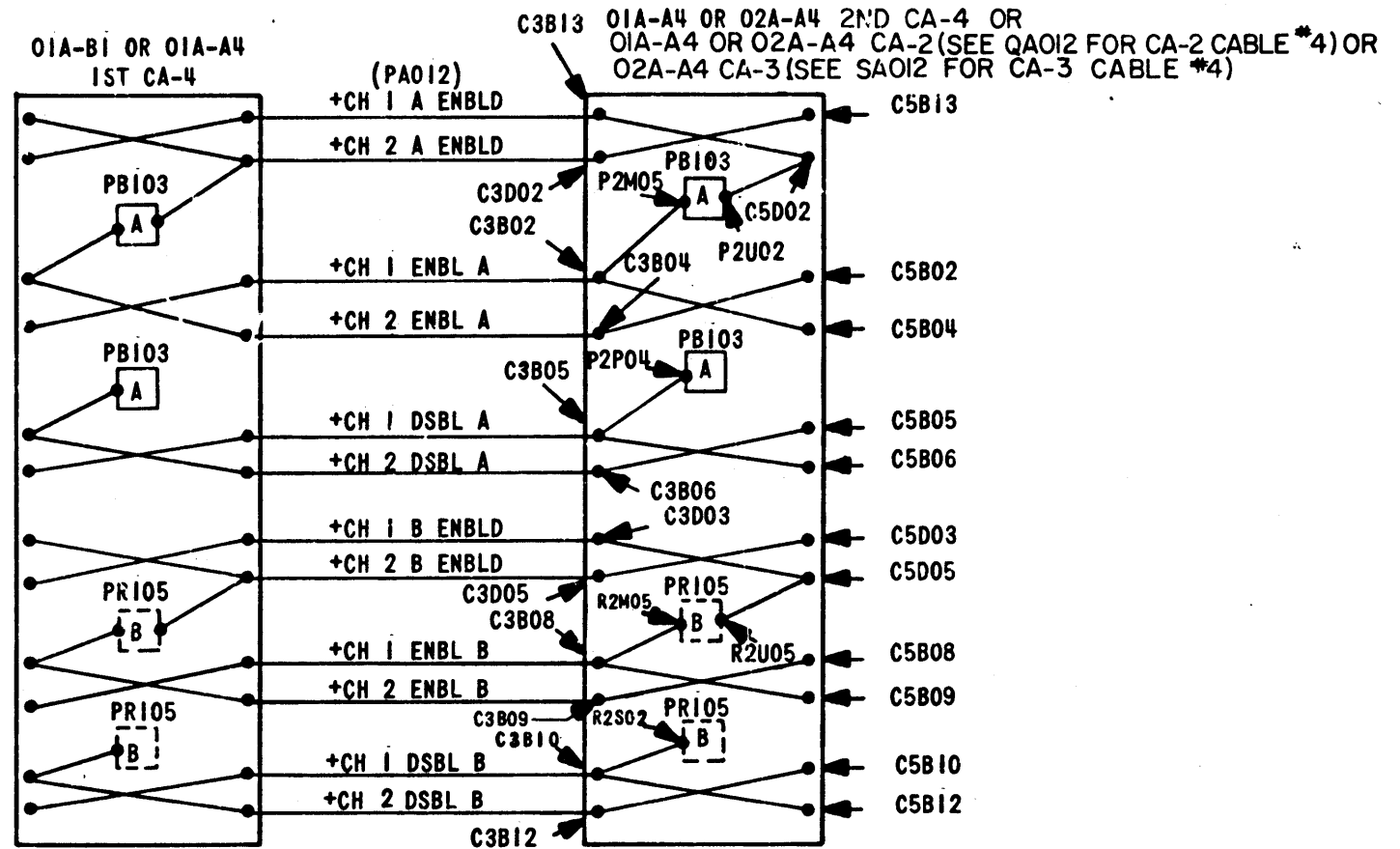


ENABLE / DISABLE SIGNAL PROPAGATION (1 OR 2 CA-4'S INSTALLED)  
(CABLE #4)

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APPRO									
CHECK									
DETAIL									
DESIGN			SHT 1 OF 3						
NAME				DATE	CHANGE NO	DATE	CHANGE NO	PA061	
				NOV76	31677				1749376 B
		CLASSIFICATION				MUST CONFORM TO ENG SPEC	DEVELOPMENT NO	LOGIC PG NO	

CCU PAGE TO CU001 TO CU001	CCU CABLE PIN 01A-B3A4B13 01A-B3A4D02	(PA012) ABBREVIATED NAME +CH 1 A ENBLD +CH 2 A ENBLD		01A-B1 OR 01A-A4 1ST CA-4		(PA012) +CH 1 A ENBLD +CH 2 A ENBLD			
FROM AP007EG4	01A-B3A4B02	+CH 1 ENBL A							
FROM AP007EL4	01A-B3A4B04	+CH 2 ENBL A							
FROM AP007EH4	01A-B3A4B05	+CH 1 DSBL A							
FROM AP007EM4	01A-B3A4B06	+CH 2 DSBL A							
TC CU001 TO CU001	01A-B3A4D03 01A-B3A4D05	+CH 1 B ENBLD +CH 2 B ENBLD							
FROM AP007EK4	01A-B3A4B08	+CH 1 ENBL B							
FROM AP007FN4	01A-B3A4B09	+CH 2 ENBL B							
FROM AP007EJ4	01A-B3A4B10	+CH 1 DSBL B							
FROM AP007FM4	01A-B3A4B12	+CH 2 DSBL B							



[B] A B INTERFACE MAY ONLY BE PRESENT IF THE CHANNEL ADAPTER INSTALLED IS THE ONLY CHANNEL ADAPTER IN THAT FRAME. THE BOARD WIRING REMAINS EVEN IF A B INTERFACE IS NOT PRESENT.

NOTE: WITH 1 OR 2 CA'S INSTALLED THERE ARE NO ALTERATIONS TO THE ENABLE / DISABLE BOARD WIRING. THE SIGNAL NAMES AND PINS ARE IDENTICAL. THE PIN LOCATIONS ARE GIVEN WITH THE 2ND CA-4. REFER TO PA048 PG. 1 OF 3 TO DETERMINE YOUR CHANNEL CONFIGURATION. IF A TYPE 3 SCANNER IS INSTALLED IN BETWEEN CA-4'S, THESE SIGNALS ARE PASSED STRAIGHT THROUGH. THE CABLE, CABLE 4, ENTERS THE TYPE 3 SCANNER AT OXA-A3B3 AND EXITS AT OXA-A3Z3. IF THE REMOTE PROGRAM LOADER IS INSTALLED IN THE O1A-B1 BOARD POSITION, THESE SIGNALS ARE PASSED STRAIGHT THROUGH, ALTHOUGH THE REMOTE USES THE ENABLE AND ENABLED SIGNALS FOR ITS INTERNAL LOGIC. THE CABLE ENTERS THE REMOTE AT THE O1A-B1C3 CABLE SOCKET AND EXITS AT THE O1A-B1C5 CABLE SOCKET.

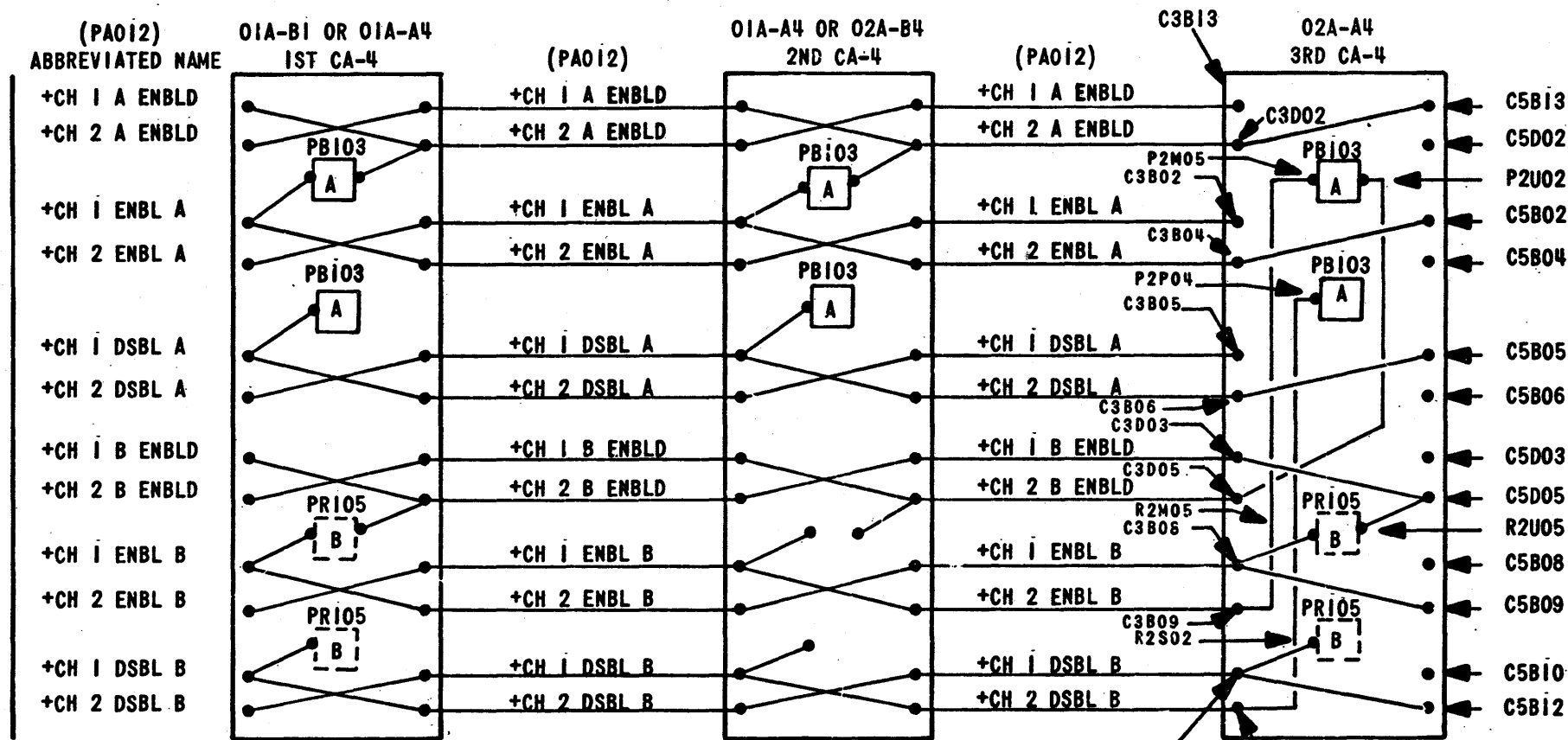
1749376 B

ENABLE / DISABLE SIGNAL PROPAGATION (3 CA-4'S INSTALLED)  
(CABLE #4)

APPRO	CHECK	DETAIL	DESIGN	NAME	DATE	CHANGE NO
				IBM	NOV76	316677
CLASSIFICATION				MUST CONFORM TO ENG SPEC		
DEVELOPMENT NO			PA061			
LOGIC PG NO			PA061			
DATE			CHANGE NO			
DATE			CHANGE NO			
1749376						B

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CCU PAGE	CCU CABLE PIN	ABBREVIATED NAME
TO CU001	01A-B3A4B13	+CH 1 A ENBLD
TO CU001	01A-B3A4D02	+CH 2 A ENBLD
FROM AP007EG4	01A-B3A4B02	+CH 1 ENBL A
FROM AP007EL4	01A-B3A4B04	+CH 2 ENBL A
FROM AP007EH4	01A-B3A4B05	+CH 1 DSBL A
FROM AP007EM4	01A-B3A4B06	+CH 2 DSBL A
TO CU001	01A-B3A4D03	+CH 1 B ENBLD
TO CU001	01A-B3A4D05	+CH 2 B ENBLD
FROM AP007EK4	01A-B3A4B08	+CH 1 ENBL B
FROM AP007FN4	01A-B3A4B09	+CH 2 ENBL B
FROM AP007EJ4	01A-B3A4B10	+CH 1 DSBL B
FROM AP007FM4	01A-B3A4B12	+CH 2 DSBL B



[B] A B INTERFACE MAY ONLY BE PRESENT IF THE CA-4 INSTALLED IS THE ONLY CHANNEL ADAPTER IN THAT FRAME. THE 2ND CA-4 CAN NEVER HAVE A B INTERFACE.

NOTE: WITH 3 CA-4'S INSTALLED THE THIRD CA-4 (ALWAYS 02A-A4) IS ALTERED SUCH THAT IT USES THE LOGICAL SIGNALS: ENABLE, DISABLE, AND ENABLED CHAN 1 INTERFACE B FOR ITS B INTERFACE (ONLY IF IT IS THE ONLY CA-4 IN THE 1ST EXPANSION FRAME) AND ENABLE, DISABLE, AND ENABLED CHAN 2 INTERFACE B FOR ITS A INTERFACE CONTROL. THE SIGNAL NAMES AND PINS ARE IDENTICAL ON ALL BOARDS. THE PIN LOCATIONS ARE GIVEN WITH THE 3RD CA-4. REFER TO PA053, PG. 6 OF 6 FOR THE 02A-A4 BOARD WIRING ALTERATIONS AND PA048 PG. 1 OF 3 TO DETERMINE YOUR CHANNEL CONFIGURATION. IF A TYPE 3 SCANNER IS INSTALLED IN BETWEEN CA-4'S, THESE SIGNALS ARE PASSED STRAIGHT THROUGH. THE CABLE, CABLE 4, ENTERS THE TYPE 3 SCANNER AT OXA-A3B3 AND EXITS AT OXA-A3Z3. IF THE REMOTE PROGRAM LOADER IS INSTALLED IN THE 01A-B1 BOARD POSITION, THESE SIGNALS ARE PASSED STRAIGHT THROUGH, ALTHOUGH THE REMOTE USES THE ENABLE AND ENABLED SIGNALS FOR ITS INTERNAL LOGIC. THE CABLE ENTERS THE REMOTE AT THE 01A-B1C3 CABLE SOCKET AND EXITS AT THE 01A-B1C5 CABLE SOCKET.

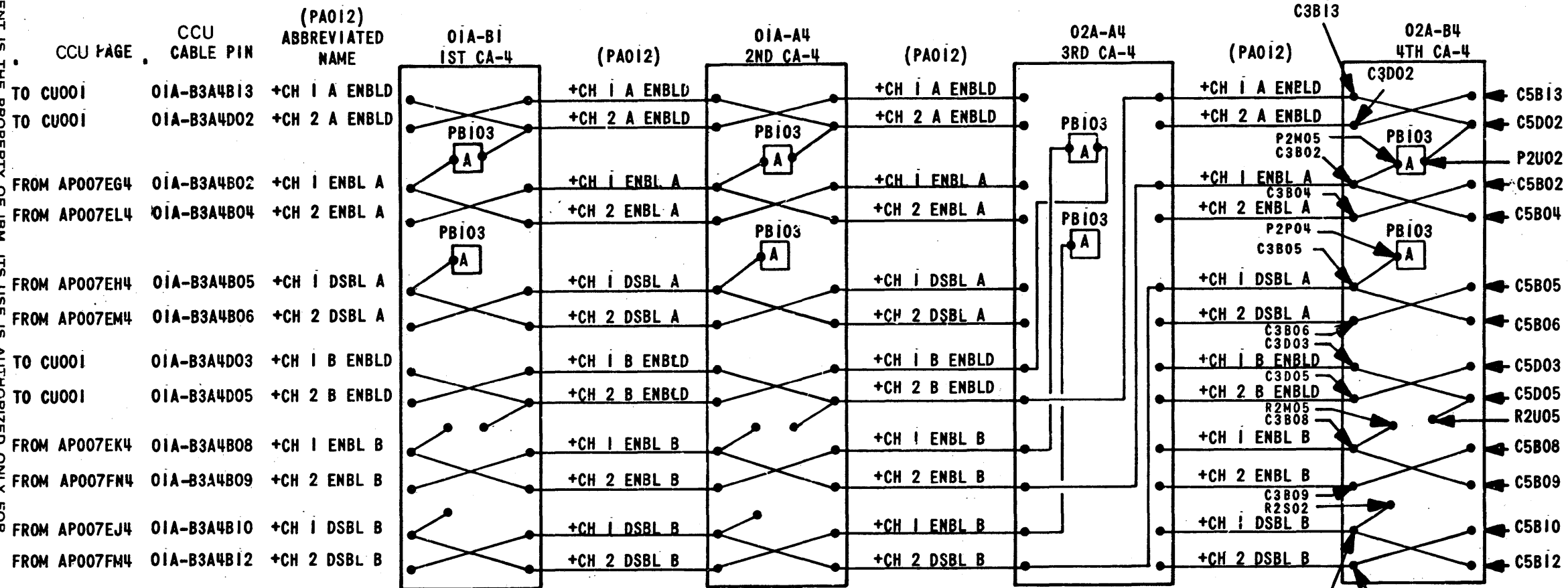
PART NO 1749376  
LOGIC PG NO PA061  
PAGE 2 OF 3

ENABLE / DISABLE SIGNAL PROPAGATION (4 CA-4'S INSTALLED)

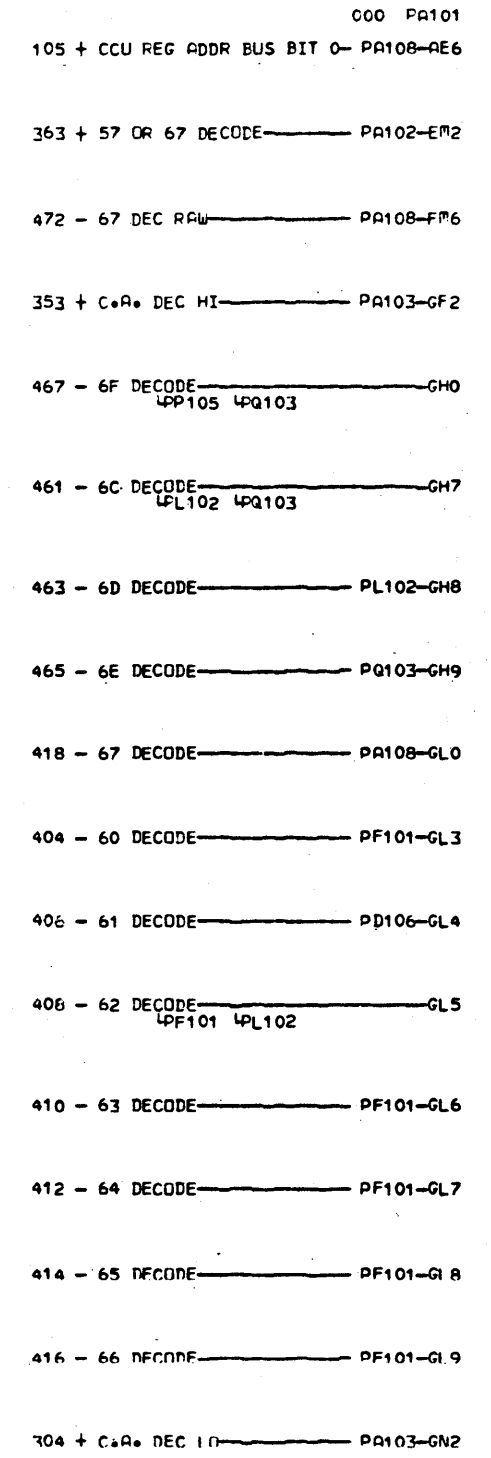
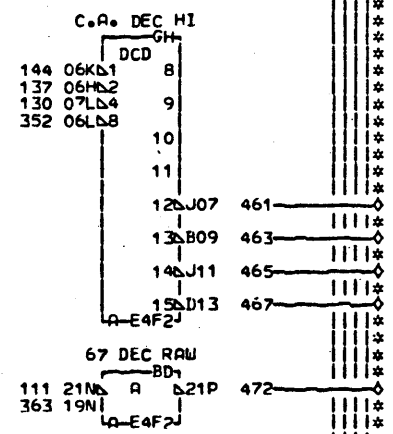
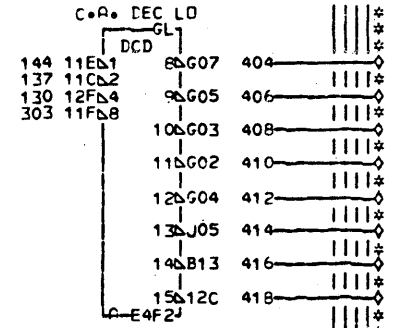
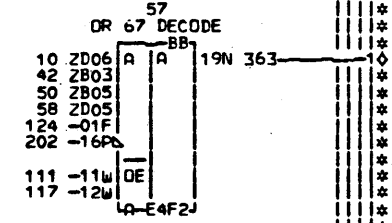
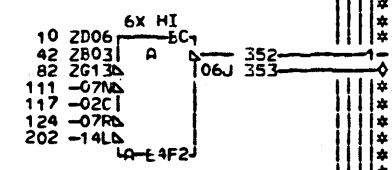
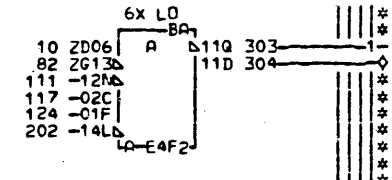
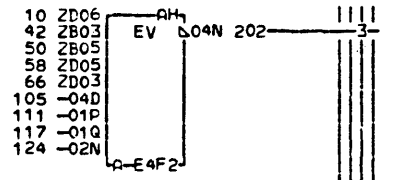
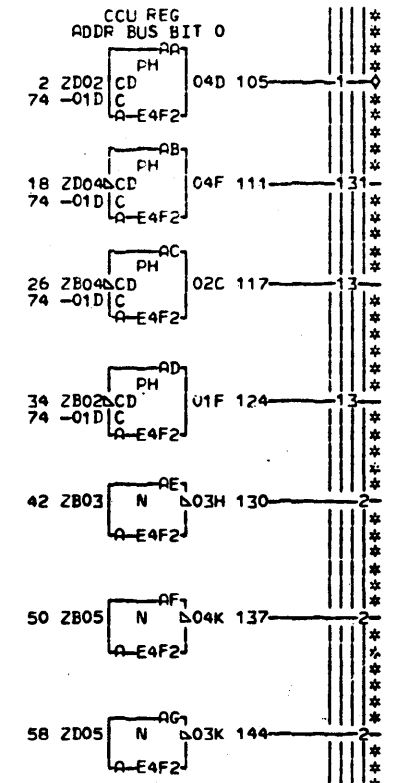
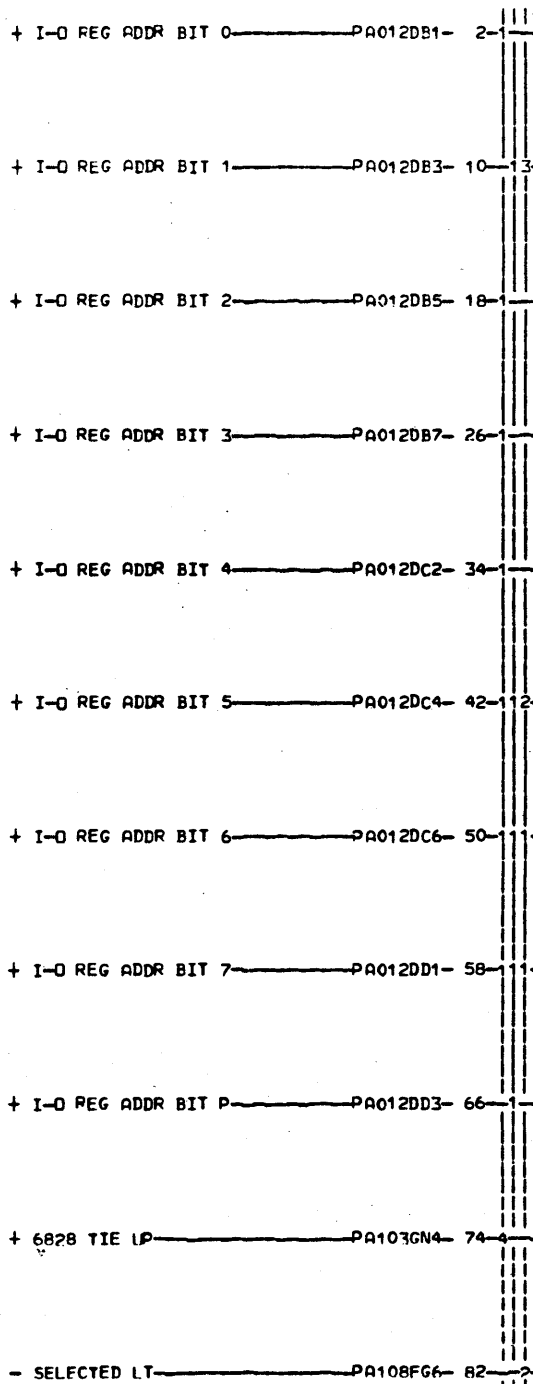
(CABLE #4)

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APPRO				NAME			
CHECK				DESIGN			
				DETAIL			
					SHT 3 OF 3		
				CLASSIFICATION			
				MUST CONFORM TO ENG SPEC			
				DEVELOPMENT NO			
				LOGIC PG NO	PA061		
				DATE		DATE	
				CHANGE NO		CHANGE NO	
					NOV76		
					31677		
				1749376 B			



NOTE: WITH 4 CA-4'S INSTALLED THE THIRD CA-4 (ALWAYS O2A-B4) IS ALTERED SUCH THAT IT USES THE LOGICAL SIGNALS: ENABLE, DISABLE AND ENABLED CHAN 1 INTERFACE B FOR ITS A INTERFACE. THE BOARD IS ALSO ALTERED SUCH THAT THE LOGICAL SIGNALS: ENABLE, DISABLE AND ENABLED CHAN 2 INTERFACE B ARE CROSSED SO THAT THEY CONTROL INTERFACE A OF THE 4TH CA-4 (O2A-A4). THE SIGNAL NAMES AND PINS ARE IDENTICAL ON ALL BOARDS. THE PIN LOCATIONS ARE GIVEN WITH THE 4TH CA-4. REFER TO PA053 PG. 4 OF 6 FOR THE O2A-B4 BOARD WIRING ALTERATIONS AND PA048 PG. 1 OF 3 TO DETERMINE YOUR CHANNEL CONFIGURATION. IF A TYPE 3 SCANNER IS INSTALLED IN BETWEEN CA-4'S, THESE SIGNALS ARE PASSED STRAIGHT THROUGH. THE CABLE, CABLE 4, ENTERS THE TYPE 3 SCANNER AT OXA-A3B3 AND EXITS AT OXA-A3Z3. IF THE REMOTE PROGRAM LOADER IS INSTALLED IN THE O1A-B1 BOARD POSITION, THESE SIGNALS ARE PASSED STRAIGHT THROUGH, ALTHOUGH THE REMOTE USES THE ENABLE AND ENABLED SIGNALS FOR ITS INTERNAL LOGIC. THE CABLE ENTERS THE REMOTE AT THE O1A-B1C3 CABLE SOCKET AND EXITS AT THE O1A-B1C5 CABLE SOCKET.



LOC. TYPE  
A-E4F2 AC09

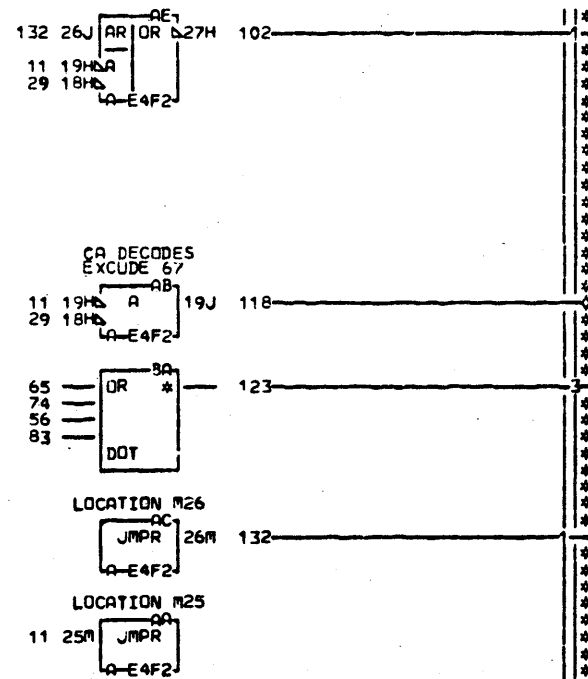
PA101  
000

TYPE 4 CA	
I-O DECODES	
F.C. HISTORY	MACH. 27RNB
314402	FRAME 01
DATE LAST FC	IBM CORP. SDD PA101
11-19-76 316677	P.N. 1755009 000

- GATE INPUT 76 — PA012DE2- 2-2  
 + 57 OR 67 DECODE — PA101EM2- 11-3  
 + ADAPTER I-O DECODED ENTER — PA102034\* 20  
 - C.A DECODES — PA103AL2- 29-2  
 + 6828 TIE UP — PA103GN4- 38-2  
 - SELECTED LT — PA108EG6- 47  
 + CA L1 INTERRUPT REQ — PF105GM6- 56  
 + INTF A BUS IN ERR — PH107CC2- 65  
 + BID L1 EB CHECK — PL103FH6- 74  
 + BID L1 INTERRUPTS CS — PQ105EA6- 83

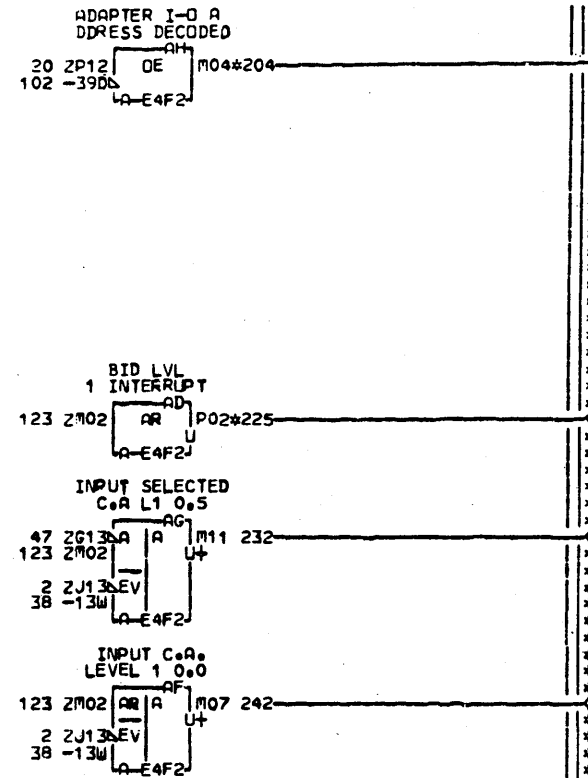
NOTE 1 SEE PAGE PROSS NOTES FOR CA-4 POSITION IDENTIFIER JUMPING

PA102  
000



EDGE CONN.  
 20 A-E4C4D09  
 204 A-E4C2D09  
 225 A-E4C2D10  
 01A-E4C4D10

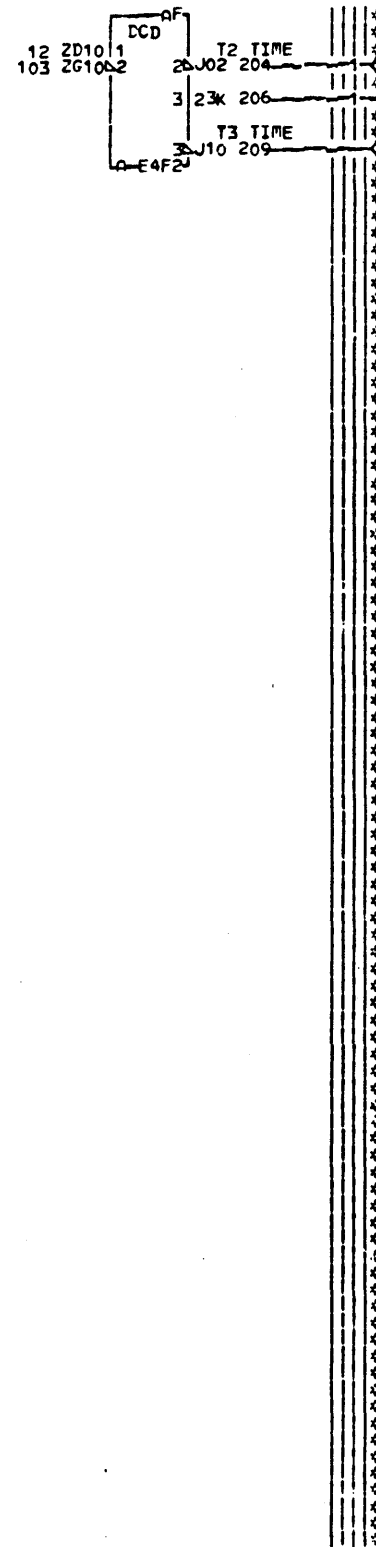
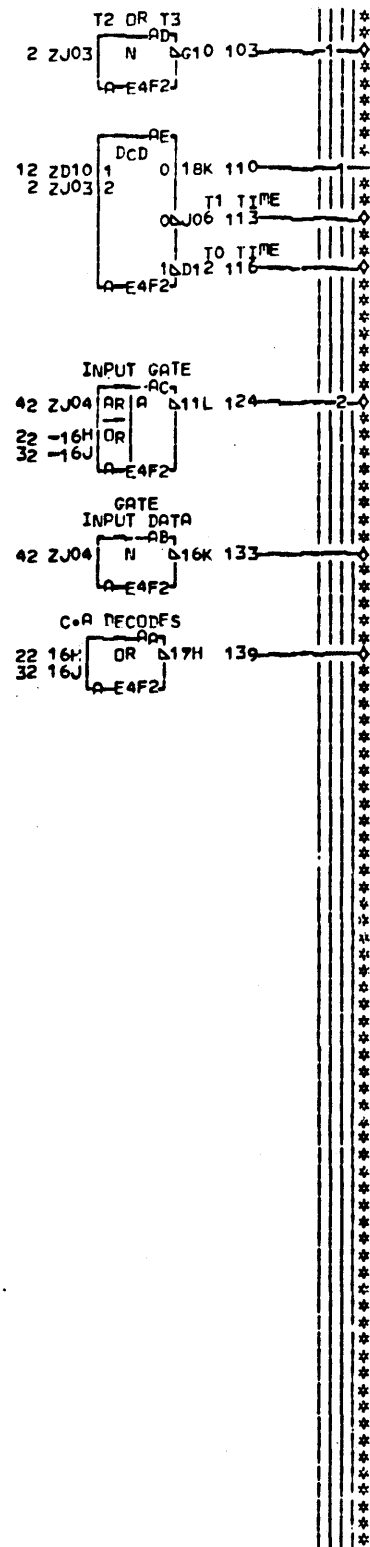
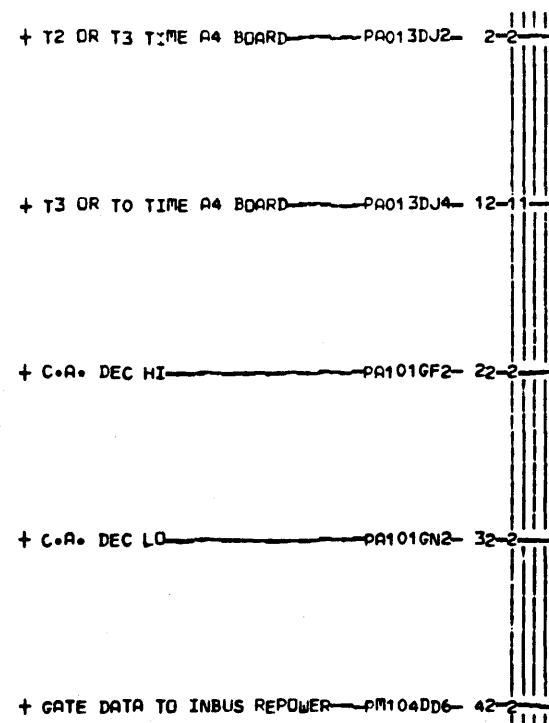
LOC. TYPE  
 A-E4F2 AC09



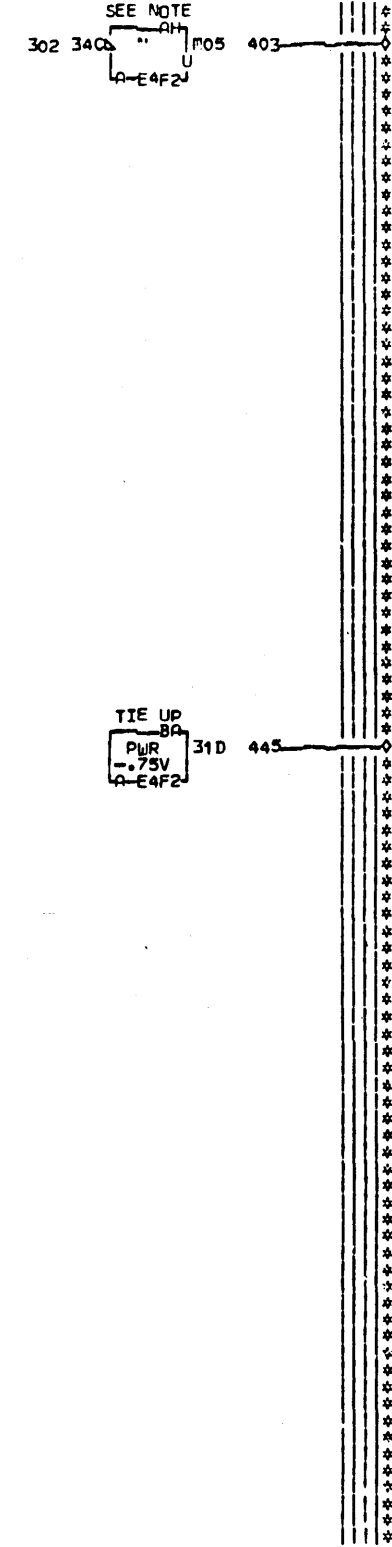
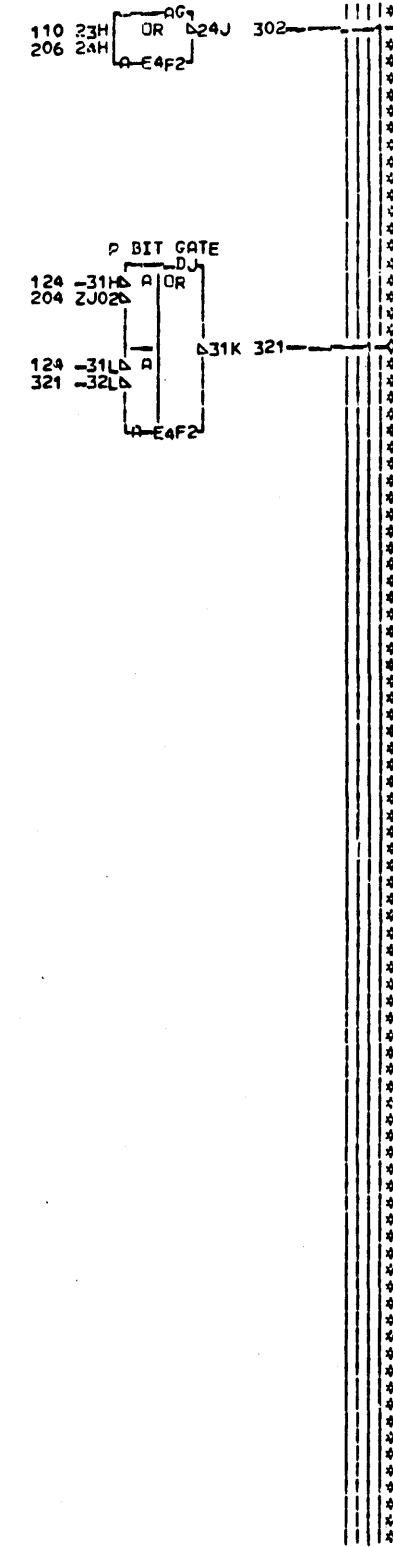
000 PA102

118 + CA DECODES EXCLUDE 67 — PA108-BD2  
 242 + INPUT C.A. LEVEL 1 0.0 — PA104-CJ2  
 232 + INPUT SELECTED C.A L1 0.5 — CK2  
 LPA104  
 225 + BID LVL 1 INTERRUPT — PA012-CL6  
 204 + ADAPTER I-O ADDRESS DECODED — ED6  
 LPA012

TYPE 4 CA		I-O FEEDBACK - LEVEL 1 BID	
E.C. HISTORY — D. MACH#27RNB		FRAME 01	
314402		IBM CORP. SDD PA102	
DATE	LAST EC	P.N.	1755010
07-14-76	315620		



LOC. TYPE  
A-E4F2 AC09



- 000 PA103
- 139 - C.A. DECODES PA102-AL2
- 133 - GATE INPUT DATA PA108-AM2
- 124 - INPUT GATE PA108-BL6
- 103 - T2 OR T3 PL103-CG2
- 116 - T0 TIME DB6  
PA108 LPE103 LPE105 LPE102  
LPM103
- 113 - T1 TIME DC6  
OPC102 LC104 LPM101 LQ101  
LQ104
- 204 - T2 TIME DD6  
LPE102 LPM104 LQ102 LQ106
- 209 - T3 TIME DE6  
OPC102 LPE104 LPE105 LQ102  
LQ104
- 321 - PARITY TO INBUS GATE DELAYED DJ6  
LPA106 LPA107
- 403 + CA 50 OR 62.5 NS CLOCK FG2  
LPC101 LPE105 LPM106 LPS106
- 445 + 6828 TIE UP GN4  
LPA101 LPA102 LPA108

NOTE. MOD 1 CLOCK 50NS  
MOD 2 CLOCK 62.5NS

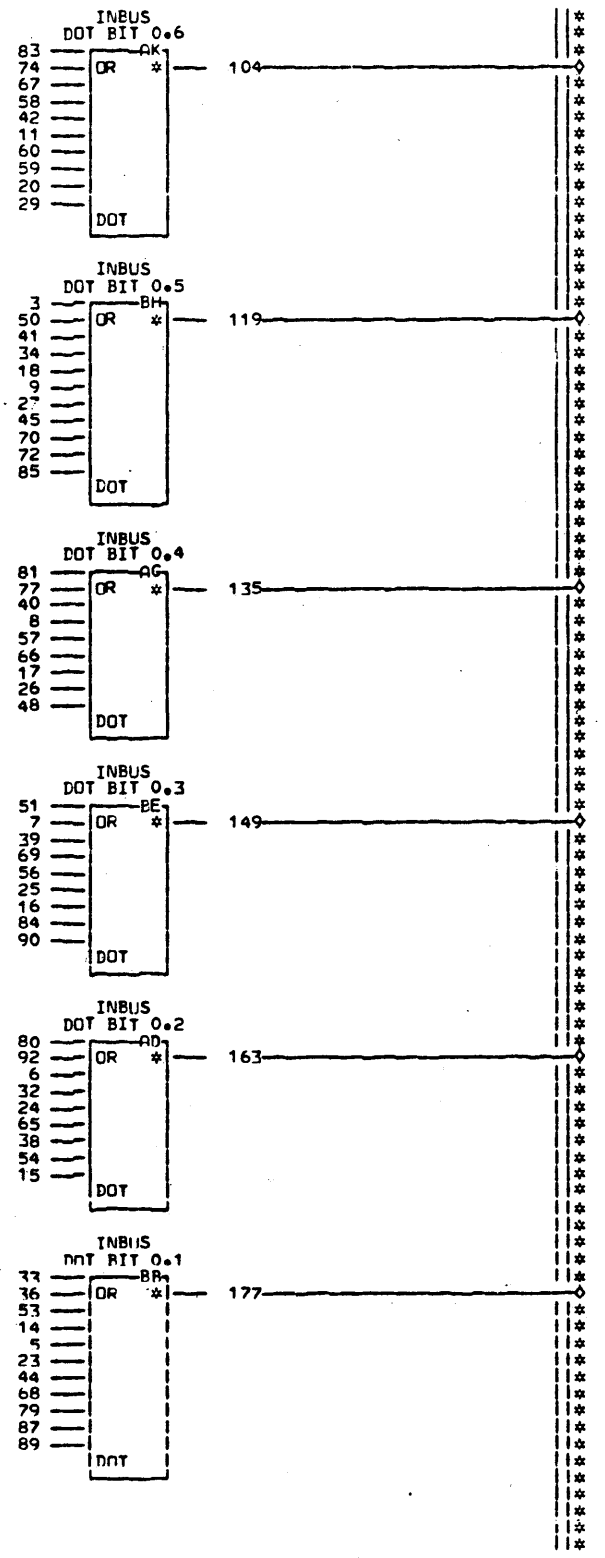
PA103  
000

TYPE 4 CA	
BASIC CLOCKING	
E.C. HISTORY	E. MACH. 27RNB
314402	FRAME 01
316677	IPM COMP. SDN PA103
DATE LAST FC	P.N. 1755011 000
08-09-79 321749	

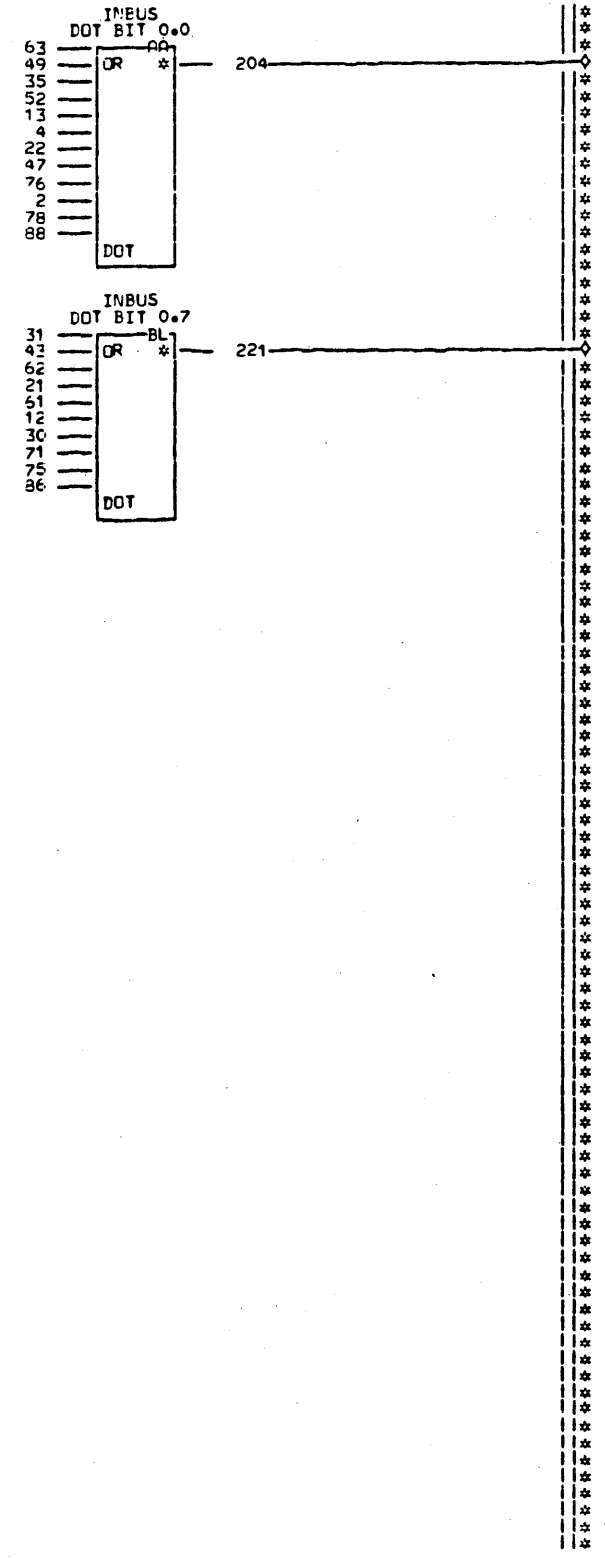
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+ INPUT C.A. LEVEL 1 0.0-----D102CJ2- 2-1
+ INPUT SELECTED C.A. I:1 0.5-----D102CK2- 3-1
+ INTF A ADDR INPUT BIT 0.0-----B104GB2- 4-1
+ INTF A ADDR INPUT BIT 0.1-----B104GC2- 5-1
+ INTF A ADDR INPUT BIT 0.2-----B104GD2- 6-1
+ INTF A ADDR INPUT BIT 0.3-----B104GE2- 7-1
+ INTF A ADDR INPUT BIT 0.4-----B104GF2- 8-1
+ INTF A ADDR INPUT BIT 0.5-----B104GG2- 9-1
+ INTF A ADDR INPUT BIT 0.6-----B104GH2- 11-1
+ INTF A ADDR INPUT BIT 0.7-----B104GJ2- 12-1
+ LOCAL STORE BIT 0.0-----D106GC2- 13-1
+ LOCAL STORE BIT 0.1-----D106GD2- 14-1
+ LOCAL STORE BIT 0.2-----D106GE2- 15-1
+ LOCAL STORE BIT 0.3-----D106GF2- 16-1
+ LOCAL STORE BIT 0.4-----D106GG2- 17-1
+ LOCAL STORE BIT 0.5-----D106GH2- 18-1
+ LOCAL STORE BIT 0.6-----D106GJ2- 20-1
+ LOCAL STORE BIT 0.7-----D106GK2- 21-1
+ INTF B ADDR INPUT BIT 0.0-----B107EB2- 22-1
+ INTF B ADDR INPUT BIT 0.1-----B107EC2- 23-1
+ INTF B ADDR INPUT BIT 0.2-----B107ED2- 24-1
+ INTF B ADDR INPUT BIT 0.3-----B107EE2- 25-1
+ INTF B ADDR INPUT BIT 0.4-----B107EF2- 26-1
+ INTF B ADDR INPUT BIT 0.5-----B107EG2- 27-1
+ INTF B ADDR INPUT BIT 0.6-----B107EH2- 29-1
+ INTF B ADDR INPUT BIT 0.7-----B107EJ2- 30-1
+ INPUT SYSTEM RESET 0.7-----D105AD2- 31-1
+ INPUT INIT SEL TV RST BIT 0.2-----D105FD2- 32-1
+ INPUT INIT INTF DISC BIT 0.1-----D105FH2- 33-1
+ INPUT SVC STP OR DISC BIT 0.5-----D105FK2- 34-1
+ SID ADR BIT 0.0-----D106CC2- 35-1
+ SID ADR BIT 0.1-----D106CD2- 36-1
+ SID ADR BIT 0.2-----D106CF2- 38-1
+ SID ADR BIT 0.3-----D106CG2- 39-1
+ SID ADR BIT 0.4-----D106CH2- 40-1
+ SID ADR BIT 0.5-----D106CK2- 41-1
+ SID ADR BIT 0.6-----D106CL2- 42-1
+ SID ADR BIT 0.7-----D106CM2- 43-1
+ STAT MOD INPUT 0.1-----D108DB2- 44-1
+ DEV END INPUT 0.5-----D108DF2- 45-1
+ ATTN STAT INPUT 0.0-----D108EA2- 47-1
+ CHAN END INPUT 0.4-----D108EE2- 48-1
+ INPUT INIT SEL ST 0.0-----D102GB2- 49-1
+ INPUT STK INITIAL 0.5-----D102GD2- 50-1
+ INPUT INIT B.C. CHK 0.3-----D102GK2- 51-1
+ INPUT OUTBND XFER 0.0-----D103GC2- 52-1
+ INPUT INBND XFER 0.1-----D103GD2- 53-1
+ INPUT ESC STAT XFER 0.2-----D103GE2- 54-1
+ INPUT NSC CE STAT XFER 0.3-----D103GF2- 56-1
+ INPUT NSC FIN STAT XFER 0.4-----D103GG2- 57-1
+ INPUT NSC STAT CLRD 0.6-----D103FK2- 58-1
+ INPUT UNIT CHK STAT 0.6-----D102FB2- 59-1
+ INPUT SLPP OUT INT 0.6-----D102FC2- 60-1
+ INPUT UNIT EXCPT STAT 0.7-----D102FE2- 61-1
+ INPUT PGM INT REQ 0.7-----D102FF2- 62-1
+ EB L.S. TO INBUS 0.0-----D104DA2- 63-1
+ EB L.S. TO INBUS 0.2-----D104DC2- 65-1
+ EB L.S. TO INBUS 0.4-----D104DK2- 66-1
+ EB L.S. TO INBUS 0.6-----D104DM2- 67-1
+ EB L.S. TO INBUS 0.1-----D104EB2- 68-1
+ EB L.S. TO INBUS 0.3-----D104ED2- 69-1
+ EB L.S. TO INBUS 0.5-----D104EL2- 70-1
+ EB L.S. TO INBUS 0.7-----D104EN2- 71-1
+ DLE TO INBUS 0.5-----D101GG2- 72-1
+ LISASCTI TO INBUS 0.6-----D101GK2- 74-1
+ EBCDIC TO INBUS 0.7-----D101GM2- 75-1
+ FR MODF TO INBUS 0.0-----D102FG2- 76-1
+ SYN TO INBUS 0.4-----D105FL2- 77-1
+ CSAR TO INBUS 0.0-----D104CN2- 78-1
+ CSAR TO INBUS 0.1-----D104CM2- 79-1
+ CSAR TO INBUS 0.2-----D104FN2- 80-1
+ CSAR TO INBUS 0.4-----D104FK2- 81-1
+ CSAR TO INBUS 0.6-----D104FM2- 83-1
+ CSAR TO INBUS 0.7-----D104FJ2- 84-1
+ CSAR TO INBUS 0.5-----D104CL2- 85-1
+ CSAR TO INBUS 0.7-----D104FN2- 86-1
+ CS MODF TO INBUS 0.1-----D104FD2- 87-1
+ CS OUTRHS CK TO INBUS 0.0-----D105GC2- 88-1
+ CS INBUS CK TO INBUS 0.1-----D105GE2- 89-1
+ CS ADDR EXCEP CK TO IN 0.3-----D105GG2- 90-1
+ CS ADRS CK TO INBUS 0.2-----D105GJ2- 92-1

```



LOC. TYPE



000 PA104

204 + INBUS DOT BIT 0.0-----PA106-AA4

163 + INBUS DOT BIT 0.2-----PA106-AD4

135 + INBUS DOT BIT 0.4-----PA106-AG4

104 + INBUS DOT BIT 0.6-----PA106-AK4

177 + INBUS DOT BIT 0.1-----PA106-BB4

149 + INBUS DOT BIT 0.3-----PA106-BE4

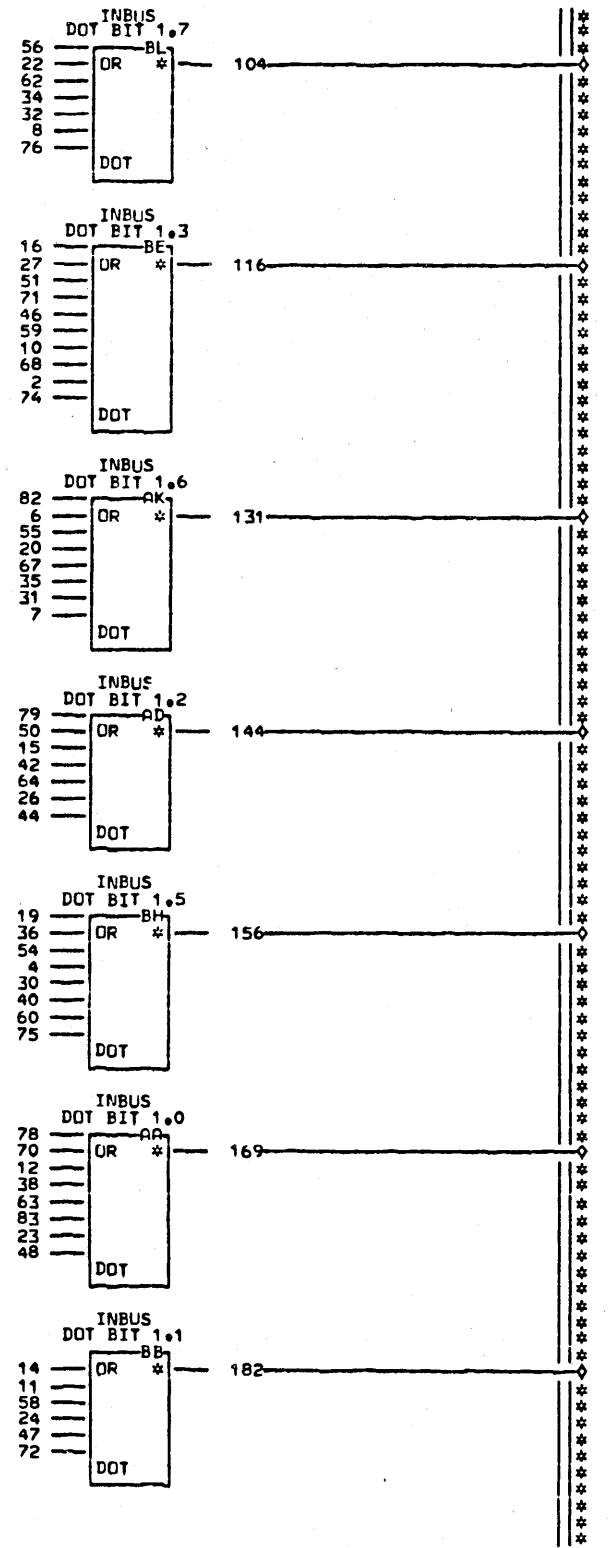
119 + INBUS DOT BIT 0.5-----PA106-BH4

221 + INBUS DOT BIT 0.7-----PA106-BL4

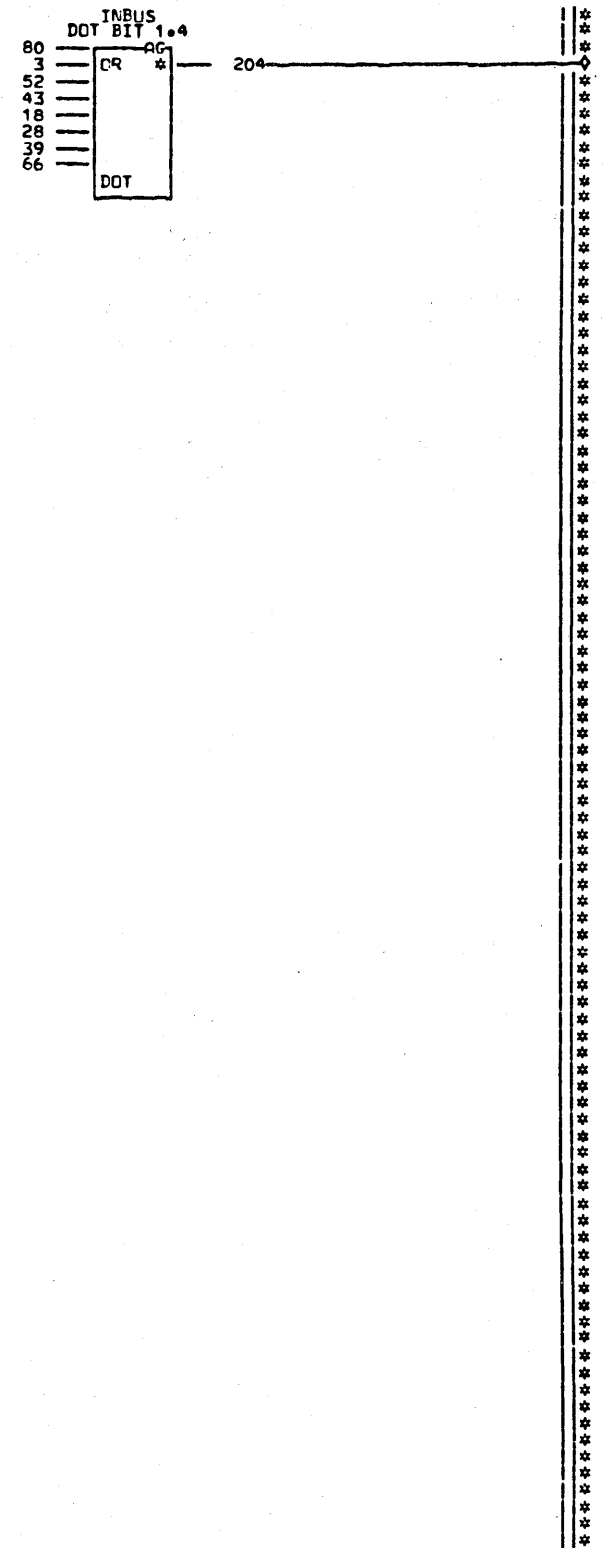
PA104  
000

TYPE 4 CH		INBUS DOT BYTE C	
F.C. HISTORY		D.MACH. 27RNR	
314402	FRAME	01	
DATE	LAST EC	IBM CORP. SDD	PA104
11-19-76	316677	P.N. 1755012	000

+ INPUT D.S. L3 1.3 PA108GF2 2-1  
 + INPUT 1.5 L3 1.4 PA108GG2 3-1  
 + INPUT 77 CA4 ID 1.5 PA108GH2 4-1  
 + INPUT 77 CA4 ID 1.6 PA108GJ2 6-1  
 + INPUT 67 CA4 ID 1.6 PA108GL2 7-1  
 + INPUT 67 CA4 ID 1.7 PA108GM2 8-1  
 + IN SUPPRESSIBLE STAT STK 1.3 PC104GM2 10-1  
 + INPUT SVC SELTV RST BIT 1.1 PC105FX2 11-1  
 + INPUT SID CMMD BIT 1.0 PD106GD2 12-1  
 + INPUT SID CMMD BIT 1.1 PD106GE2 14-1  
 + INPUT SID CMMD BIT 1.2 PD106GG2 15-1  
 + INPUT SID CMMD BIT 1.3 PD106GH2 16-1  
 + INPUT SID CMMD BIT 1.4 PD106GJ2 18-1  
 + INPUT SID CMMD BIT 1.5 PD106GL2 19-1  
 + INPUT SID CMMD BIT 1.6 PD106GM2 20-1  
 + INPUT SID CMMD BIT 1.7 PD106GN2 22-1  
 + LOCAL STORE BIT 1.0 PD107GC2 23-1  
 + LOCAL STORE BIT 1.1 PD107GD2 24-1  
 + LOCAL STORE BIT 1.2 PD107GF2 26-1  
 + LOCAL STORE BIT 1.3 PD107GG2 27-1  
 + LOCAL STORE BIT 1.4 PD107GH2 28-1  
 + LOCAL STORE BIT 1.5 PD107GK2 30-1  
 + LOCAL STORE BIT 1.6 PD107GL2 31-1  
 + LOCAL STORE BIT 1.7 PD107GM2 32-1  
 + INPUT HDWR COUNT 1 1.7 PE104GB2 34-1  
 + INPUT HDWR COUNT 2 1.6 PE104GC2 35-1  
 + INPUT HDWR COUNT 4 1.5 PE104GD2 36-1  
 + INPUT SVC CH BD CHK 1.0 PE105GF2 38-1  
 + INPUT CA ENABLED 1.4 PF104DC2 39-1  
 + INPUT RN ACTIVE 1.5 PF104DE2 40-1  
 + INPUT 1.2 SUPPRESS OUT ON PF104DG2 42-1  
 + INPUT CMMD CHAINING 1.3 PF104FF2 43-1  
 + CCU BUS OUT CK BYTE 1.2 PF105GD2 44-1  
 + LOCAL STORE PAR CK BYTE 1.3 PF105GG2 46-1  
 + INVALID ID OP BYTE 1.1 PF105GK2 47-1  
 + CHNL BUS IN ERR TO BIT 1.0 PH107DD2 48-1  
 + INPUT EB COUNT 32 1.2 PK103ED2 50-1  
 + INPUT EB COUNT 16 1.3 PK103EE2 51-1  
 + INPUT EB COUNT 8 1.4 PK103EF2 52-1  
 + INPUT EB COUNT 4 1.5 PK103EG2 54-1  
 + INPUT EB COUNT 2 1.6 PK103EH2 55-1  
 + INPUT EB COUNT 1 1.7 PK103EJ2 56-1  
 + EB L.S. TO INBUS 1.1 PK104FE2 58-1  
 + EB L.S. TO INBUS 1.3 PK104FG2 59-1  
 + EB L.S. TO INBUS 1.5 PK104FJ2 60-1  
 + EB L.S. TO INBUS 1.7 PK104FM2 62-1  
 + EB L.S. TO INBUS 1.0 PK104GC2 63-1  
 + EB L.S. TO INBUS 1.2 PK104GF2 64-1  
 + EB L.S. TO INBUS 1.4 PK104GH2 66-1  
 + EB L.S. TO INBUS 1.6 PK104GK2 67-1  
 + EB L.S. PARITY CHECK 1.3 PL105FG2 68-1  
 + L3 INT TO 1.0 PM103GA2 70-1  
 + INPUT SVC STAT STK 1.3 PM104FH2 71-1  
 + CS INPUTS TO INBUS 1.1 PP104FB2 72-1  
 + CS INPUTS TO INBUS 1.3 PP104FD2 74-1  
 + CS INPUTS TO INBUS 1.5 PP104FF2 75-1  
 + CS INPUTS TO INBUS 1.7 PP104FH2 76-1  
 + CS INPUTS TO INBUS 1.0 PP104GA2 78-1  
 + CS INPUTS TO INBUS 1.2 PP104GC2 79-1  
 + CS INPUTS TO INBUS 1.4 PP104GE2 80-1  
 + CS INPUTS TO INBUS 1.6 PP104GG2 82-1  
 + CHNL BUS IN ERR TO BIT 1.0 PS107DD2 83-1



LOC. TYPE



000 PA105  
 169 + INBUS DOT BIT 1.0 PA107-AA4  
 144 + INBUS DOT BIT 1.2 PA107-AD4  
 204 + INBUS DOT BIT 1.4 PA107-AG4  
 131 + INBUS DOT BIT 1.6 PA107-AK4  
 182 + INBUS DOT BIT 1.1 PA107-BB4  
 116 + INBUS DOT BIT 1.3 PA107-BE4  
 156 + INBUS DOT BIT 1.5 PA107-BH4  
 104 + INBUS DOT BIT 1.7 PA107-BL4

PA105  
000

TYPE 4 CA  
 INBUS DOT BYTE 1  
 -E.C.-HISTORY-C MACH.27RNB  
 FRAME 01  
 IBM CORP.SDD PA105  
 DATE LAST EC P.No. 1755013 000  
 02-23-76 314402



- PARITY TO INBUS GATE DELAYED-PA103DJ6- 2-1

+ INBUS DOT BIT 0.0 - PA104AA4\* 11-2

+ INBUS DOT BIT 0.2 - PA104AD4\* 20-2

+ INBUS DOT BIT 0.4 - PA104AC4\* 29-2

+ INBUS DOT BIT 0.6 - PA104AK4\* 38-2

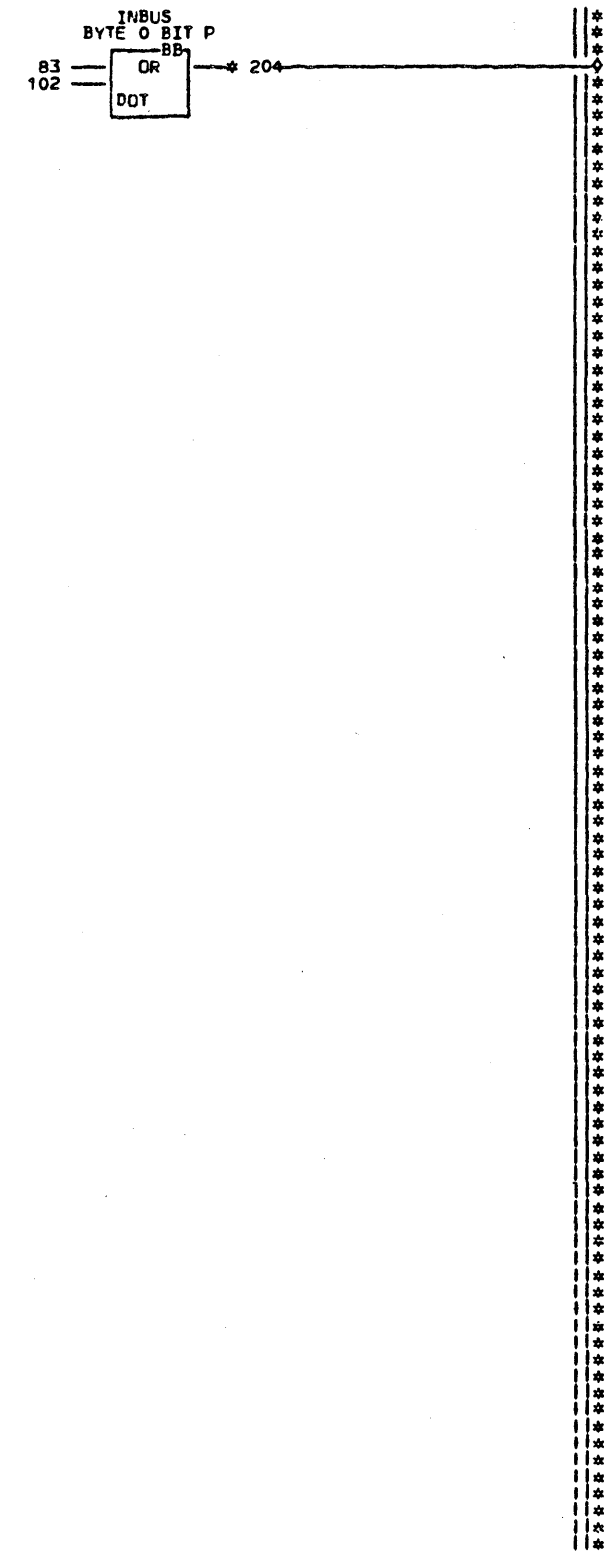
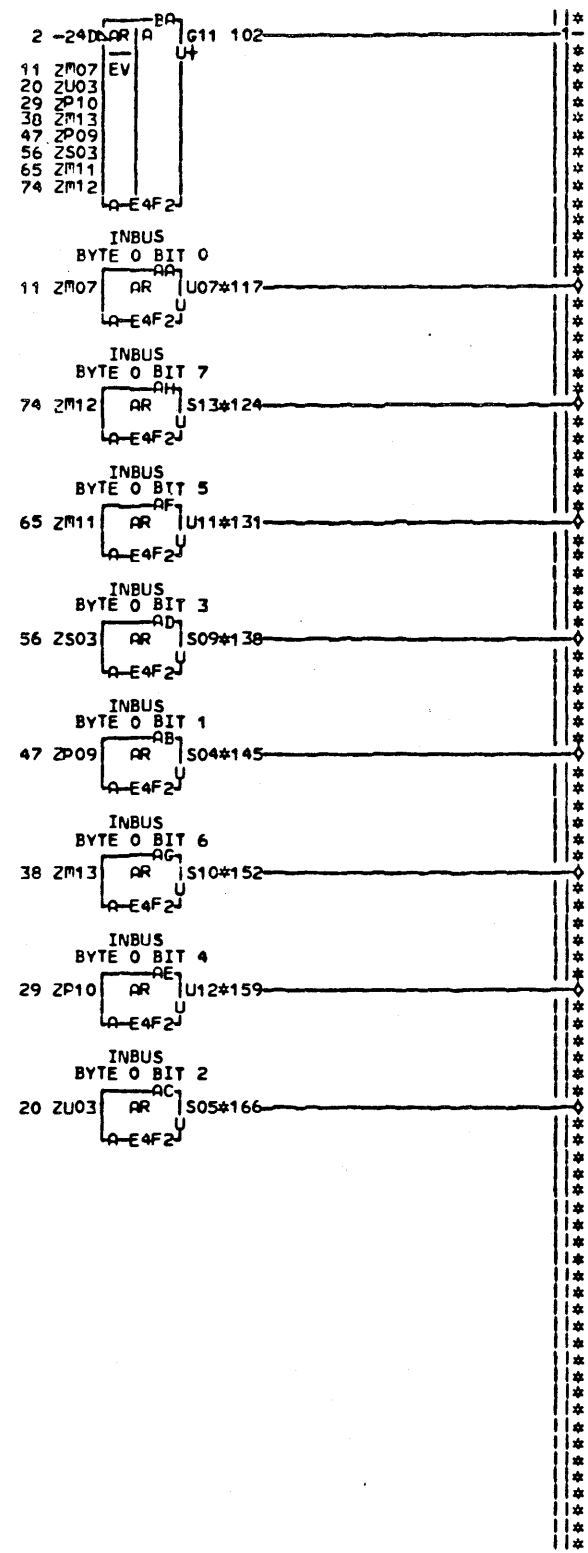
+ INBUS DOT BIT 0.1 - PA104BB4\* 47-2

+ INBUS DOT BIT 0.3 - PA104BE4\* 56-2

+ INBUS DOT BIT 0.5 - PA104BH4\* 65-2

+ INBUS DOT BIT 0.7 - PA104BL4\* 74-2

+ INBOUND CS DATA TO INBUS 0.P-PA104GK2- 83



117 + INBUS BYTE 0 BIT 0 - PA011-FB6

145 + INBUS BYTE 0 BIT 1 - PA011-FC6

166 + INBUS BYTE 0 BIT 2 - PA011-FD6

138 + INBUS BYTE 0 BIT 3 - PA011-FE6

159 + INBUS BYTE 0 BIT 4 - PA011-FF6

131 + INBUS BYTE 0 BIT 5 - PA011-FG6

152 + INBUS BYTE 0 BIT 6 - PA011-FH6

124 + INBUS BYTE 0 BIT 7 - PA011-FJ6

204 + INBUS BYTE 0 BIT P - PA011-GL4

EDGE CONN.	Q-E4F2D09	01Q-E4A4B13	01Q-E4A4B09
11 RESISTOR	56 RESISTOR	131 Q-E4A2B10	166 Q-E4A2B06
Q-E4F2M07	Q-E4F2S03	01Q-E4A4B10	01Q-E4A4B06
20 RESISTOR	65 RESISTOR	138 Q-E4A2B08	204 Q-E4A2B02
Q-E4F2U03	Q-E4F2M11	01Q-E4A4B08	01Q-E4A4B02
29 RESISTOR	74 RESISTOR	145 Q-E4A2B05	
Q-E4F2P10	Q-E4F2M12	01Q-E4A4B05	
38 RESISTOR	117 Q-E4A2B04	152 Q-E4A2B12	
Q-E4F2M13	01Q-E4A4B04	01Q-E4A4B12	
47 RESISTOR	124 Q-E4A2B13	159 Q-E4A2B09	

PA106  
000

LOC. TYPE  
Q-E4F2 AC09

TYPE 4 CA	
INBUS BYTE 0	
-F-C-HISTORY-D-MACH.27RNR	
314402	FRAME 01
314424	
DATE	LAST EC
11-19-76	316677
P.N.	1755014
000	

- PARITY TO INBUS GATE DELAYED-PA103DJ6- 2-1

+ INBUS DOT BIT 1.0 PA105AA4\* 11-2

+ INBUS DOT BIT 1.2 PA105AD4\* 20-2

+ INBUS DOT BIT 1.4 PA105AG4\* 29-2

+ INBUS DOT BIT 1.6 PA105AK4\* 38-2

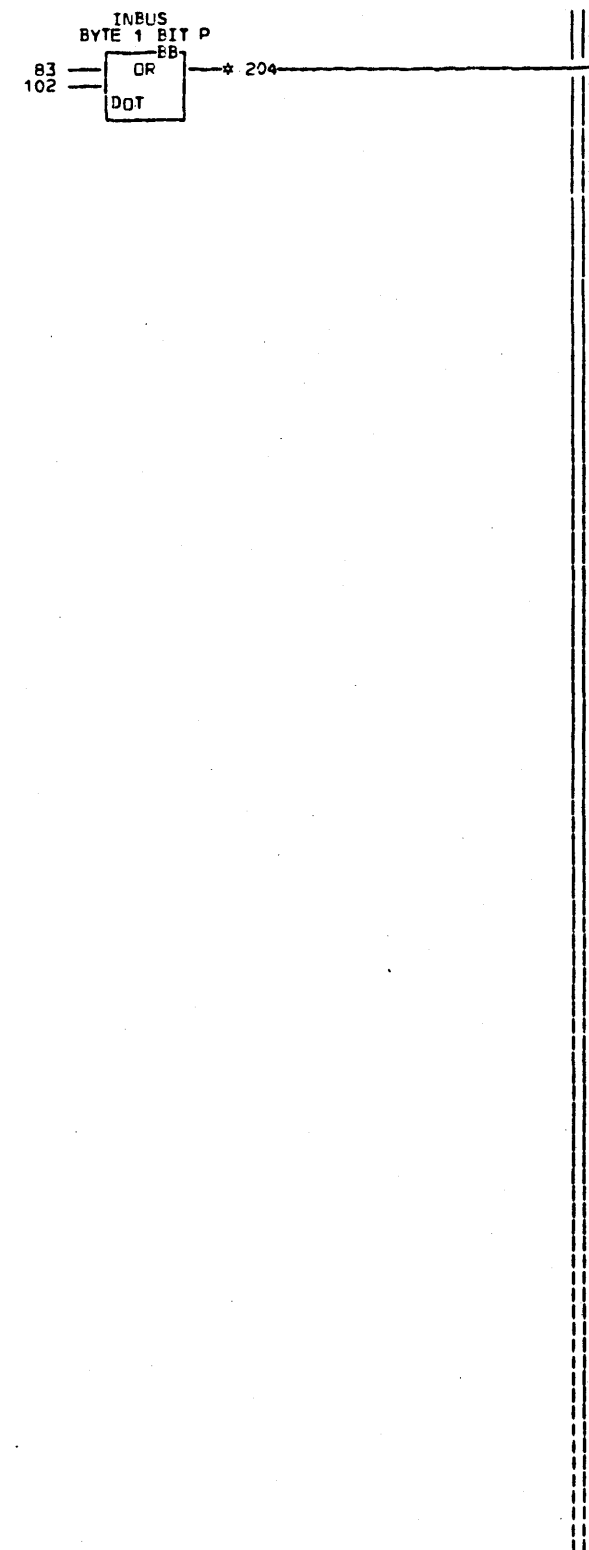
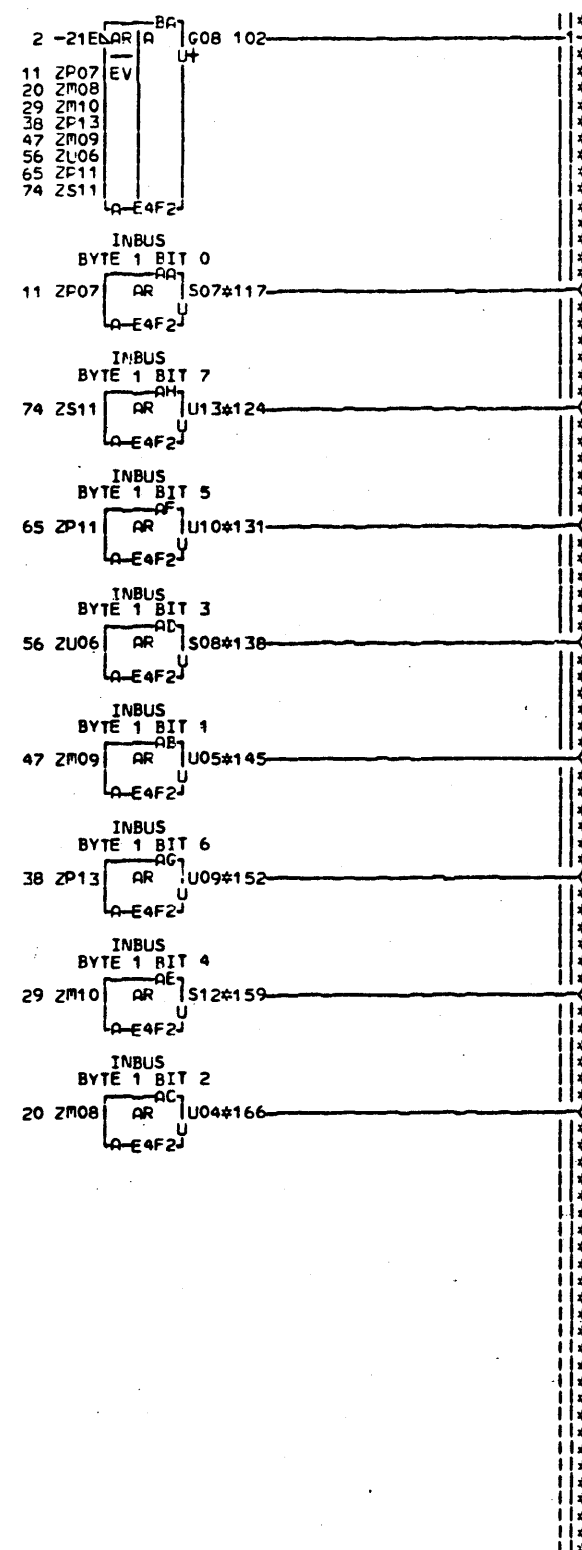
+ INBUS DOT BIT 1.1 PA105BB4\* 47-2

+ INBUS DOT BIT 1.3 PA105BE4\* 56-2

+ INBUS DOT BIT 1.5 PA105BH4\* 65-2

+ INBUS DOT BIT 1.7 PA105BL4\* 74-2

+ INBOUND CS DATA TO INBUS 1.P-PM104GN2- 83-1



117 + INBUS BYTE 1 BIT 0 PA011-FB5

145 + INBUS BYTE 1 BIT 1 PA011-FC5

166 + INBUS BYTE 1 BIT 2 PA011-FD6

138 + INBUS BYTE 1 BIT 3 PA011-FE6

159 + INBUS BYTE 1 BIT 4 PA011-FF6

131 + INBUS BYTE 1 BIT 5 PA011-FG6

152 + INBUS BYTE 1 BIT 6 PA011-FH6

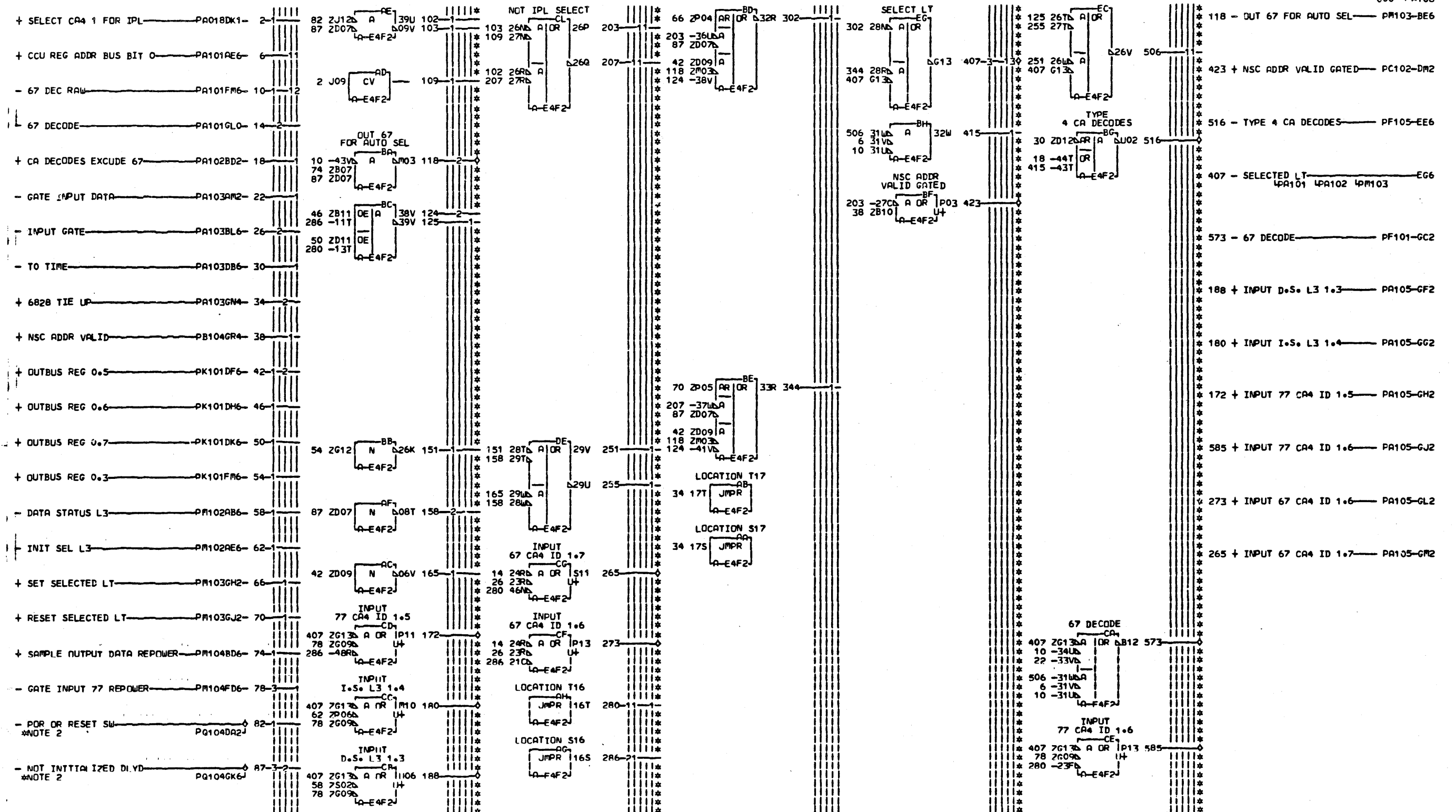
124 + INBUS BYTE 1 BIT 7 PA011-FJ6

204 + INBUS BYTE 1 BIT P PA011-GL4

EDGE CONN.	A-E4F2M09	01A-E4A4N13	01A-E4A4D09
11 RESISTOR	56 A-E4F2P07	131 A-E4A2U10	166 A-E4A2D06
20 RESISTOR	65 A-E4F2M08	01A-E4A4N10	01A-E4A4D06
29 RESISTOR	74 A-E4F2P11	138 A-E4A2H07	204 A-E4A2H02
38 RESISTOR	117 A-E4A2D03	01A-E4A4D07	01A-E4A4D07
47 RESISTOR	124 A-E4A2D13	145 A-E4A2D05	
		159 A-E4A2D09	

LOC. TYPE  
A-E4F2 AC09

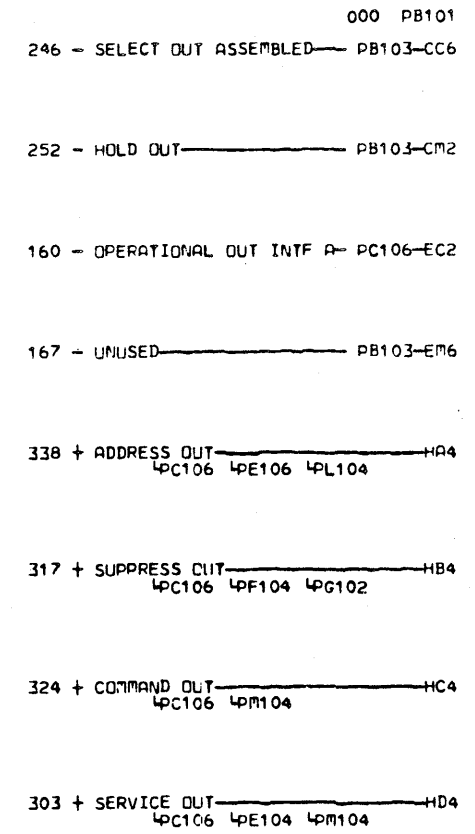
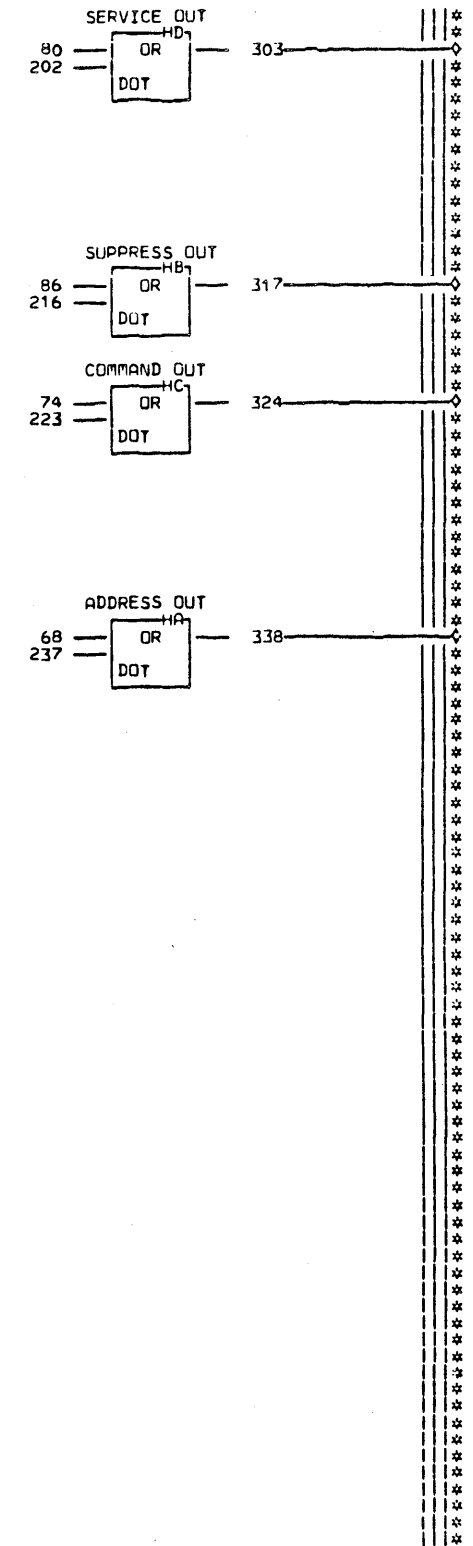
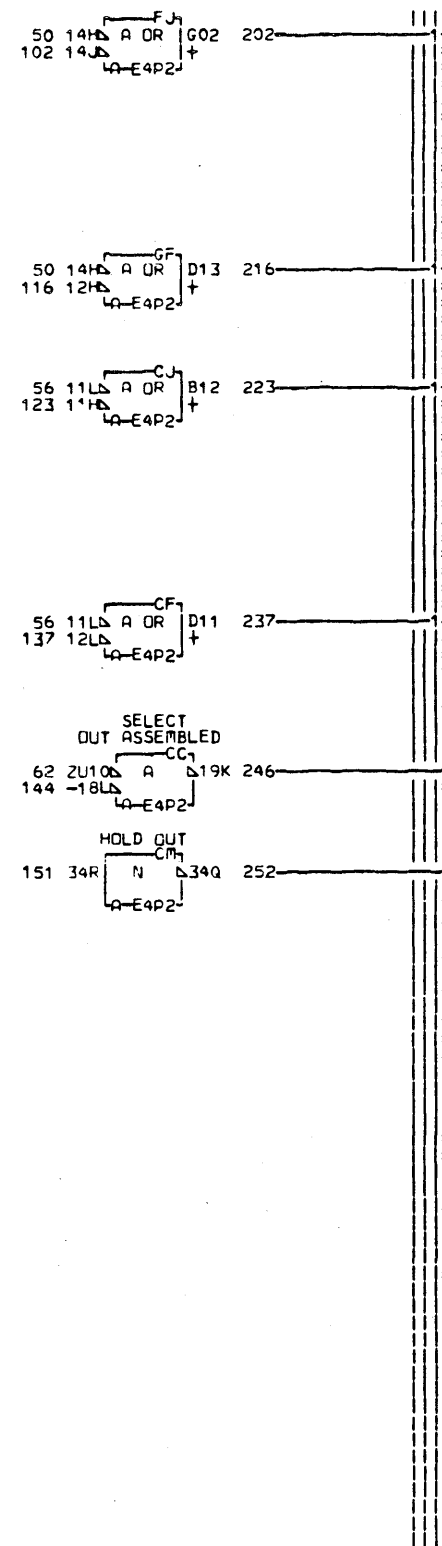
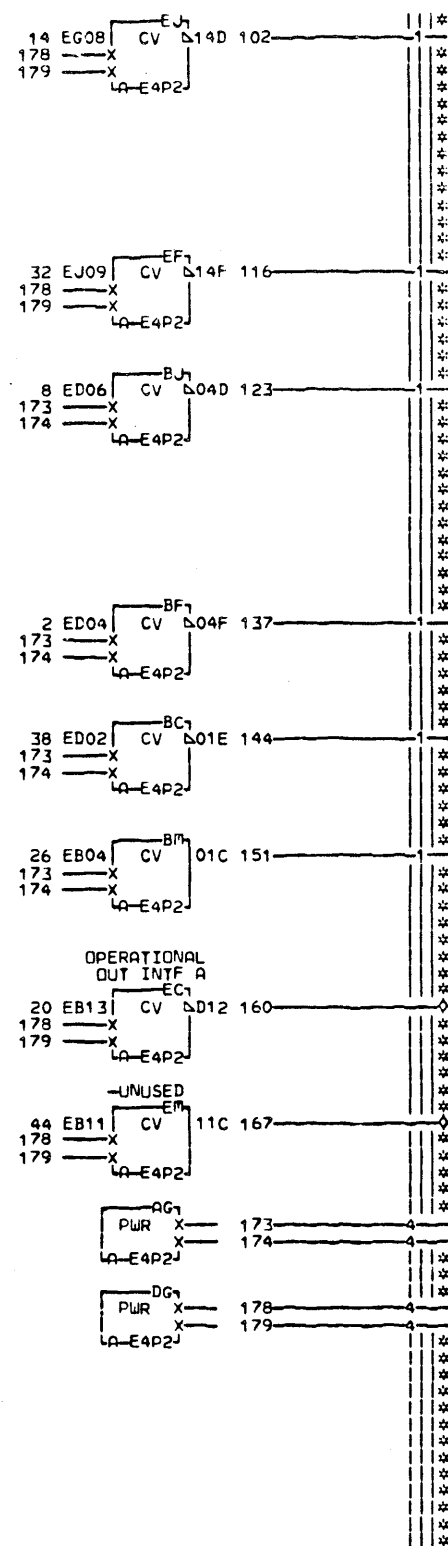
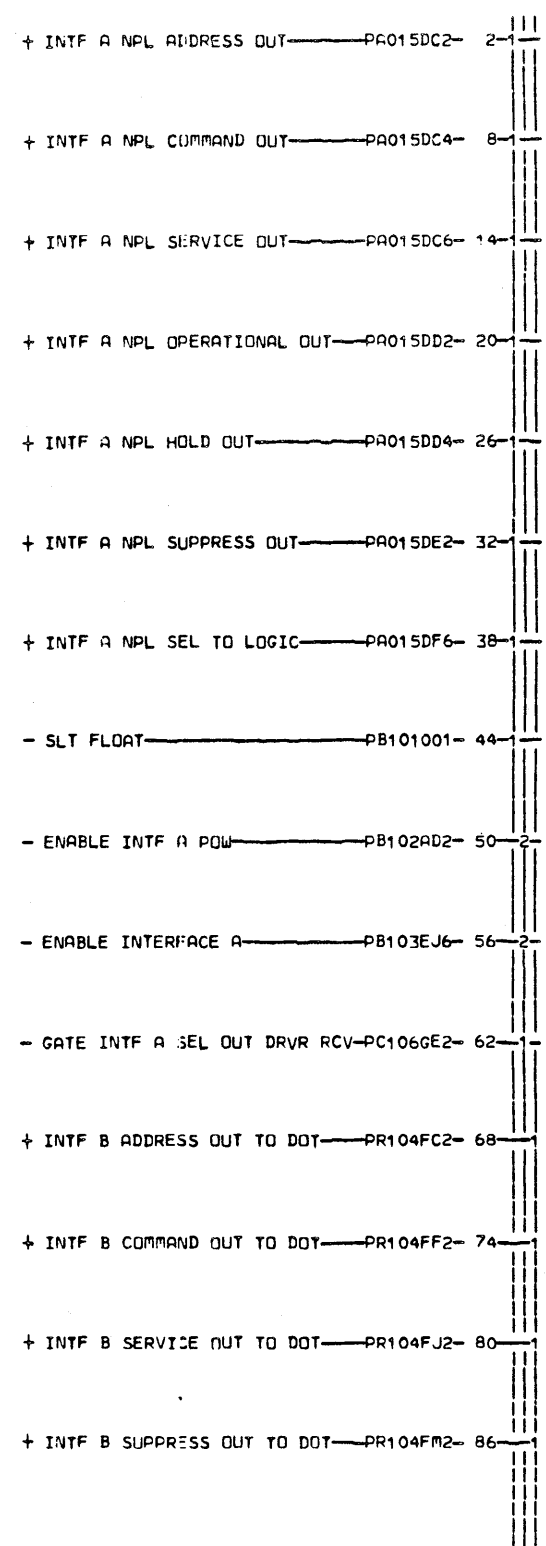
TYPE 4 CA	
INBUS BYTE 1	
F.C. HISTORY	D. MACH. 27RNB
314402	
314424	FRAME 01
DATE LAST FC	IBM CORP. SDD PA107
11-19-76 316677	P.No. 1755015 000



NOTE 1 SEE PAGE PA055 NOTES FOR CA-4 POSITION IDENTIFIER JUMPING  
 NOTE 2 IF N-CANAL ROS IS INSTALLED, PIN D07 IS DELETED FROM NET P0104GK6 AND ADDED TO NET P0104DA2 IN RED WIRE  
 PA108 PIN F2J12 TO F2D07  
 000

LOC. TYPE  
 A-E4F2 AC09

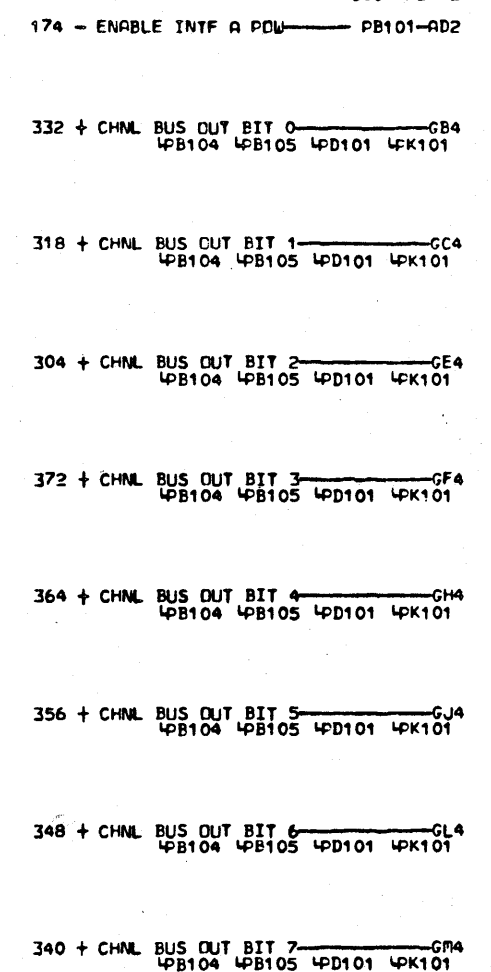
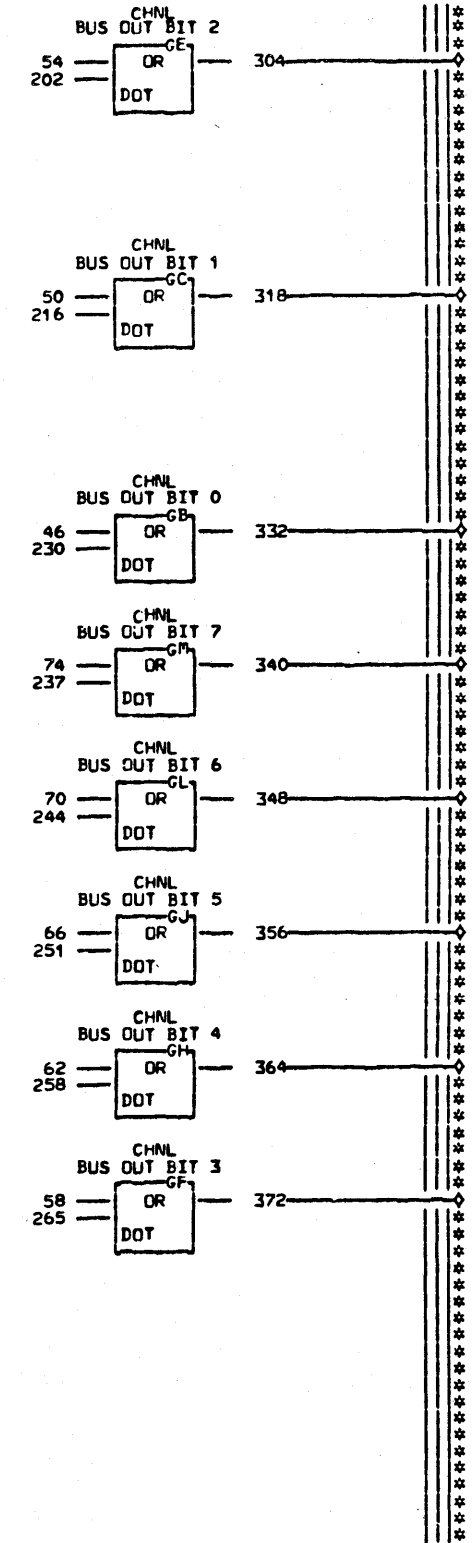
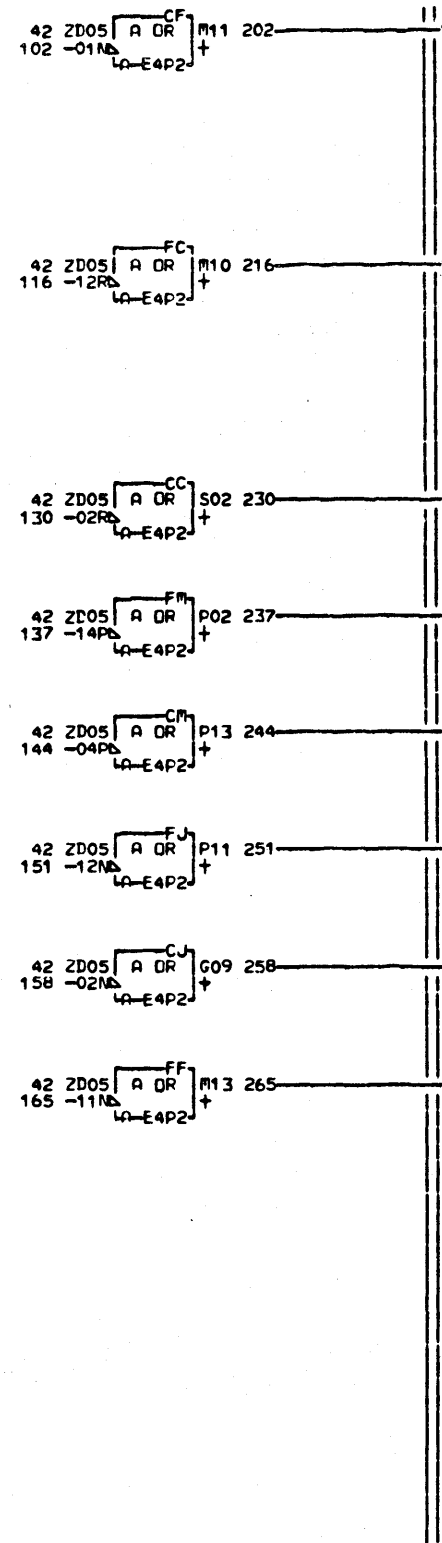
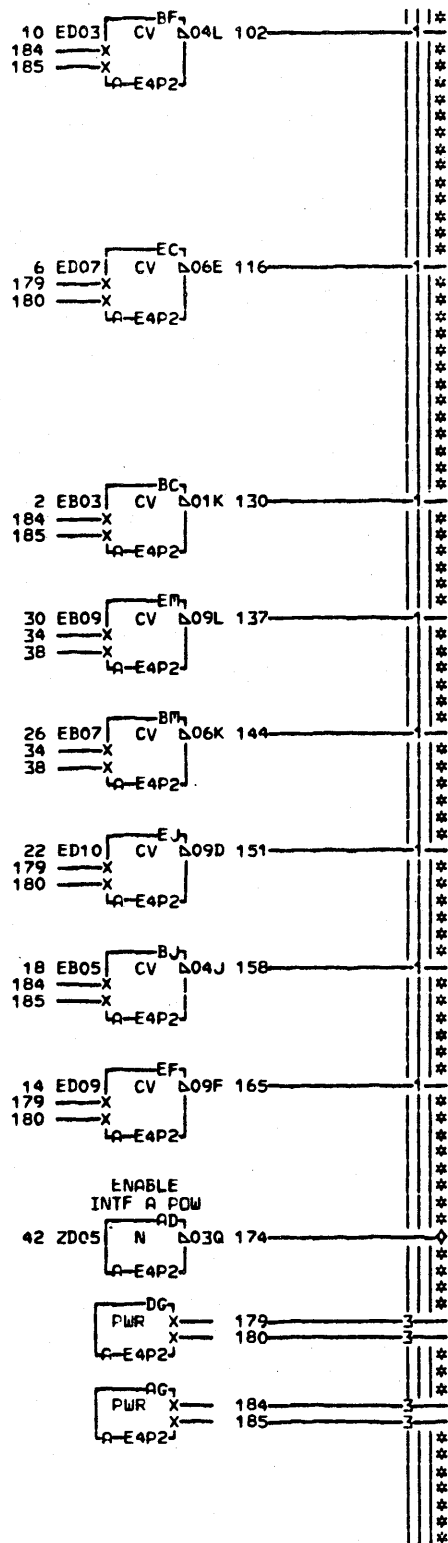
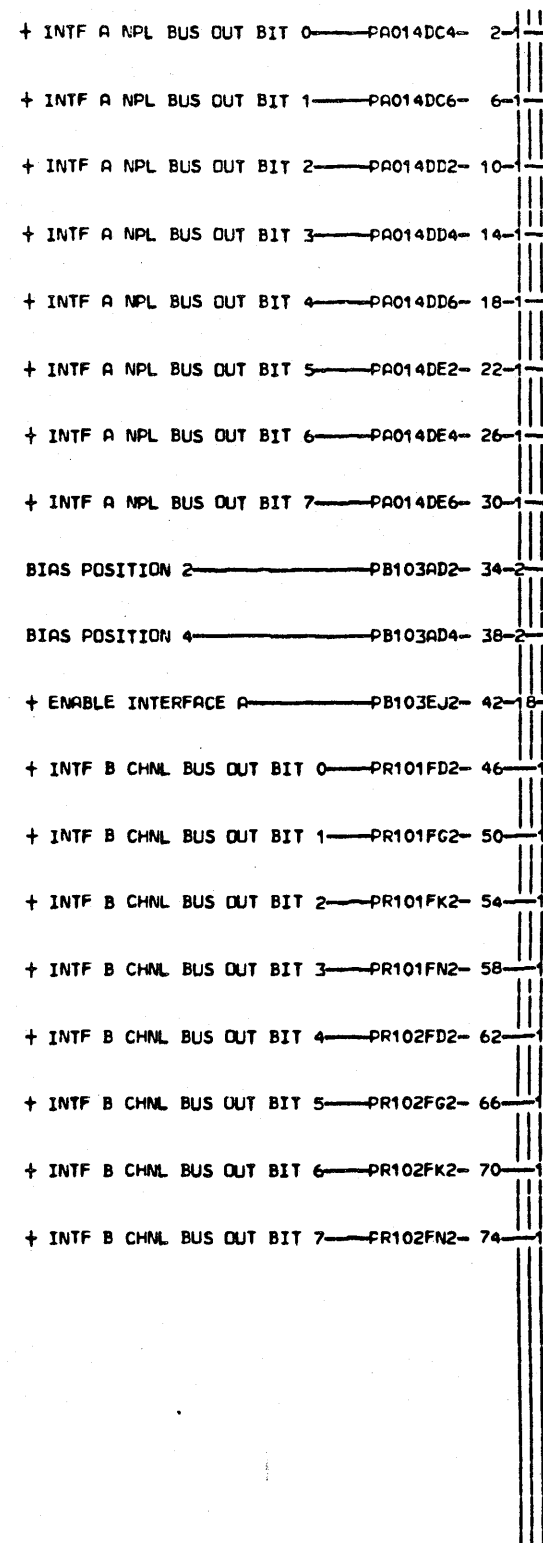
CHANNEL ADAPTER TYPE 4	
SELECTED LATCH AND L3 BITS	
E.C. HISTORY	D. MACH. 27RNB
314402	
314424	FRAME 01
315620	
DATE	LAST EC
11-19-76	316677
P.N. 1755016	000



PB101  
000

LDC TYPE  
A-E4P2 7601

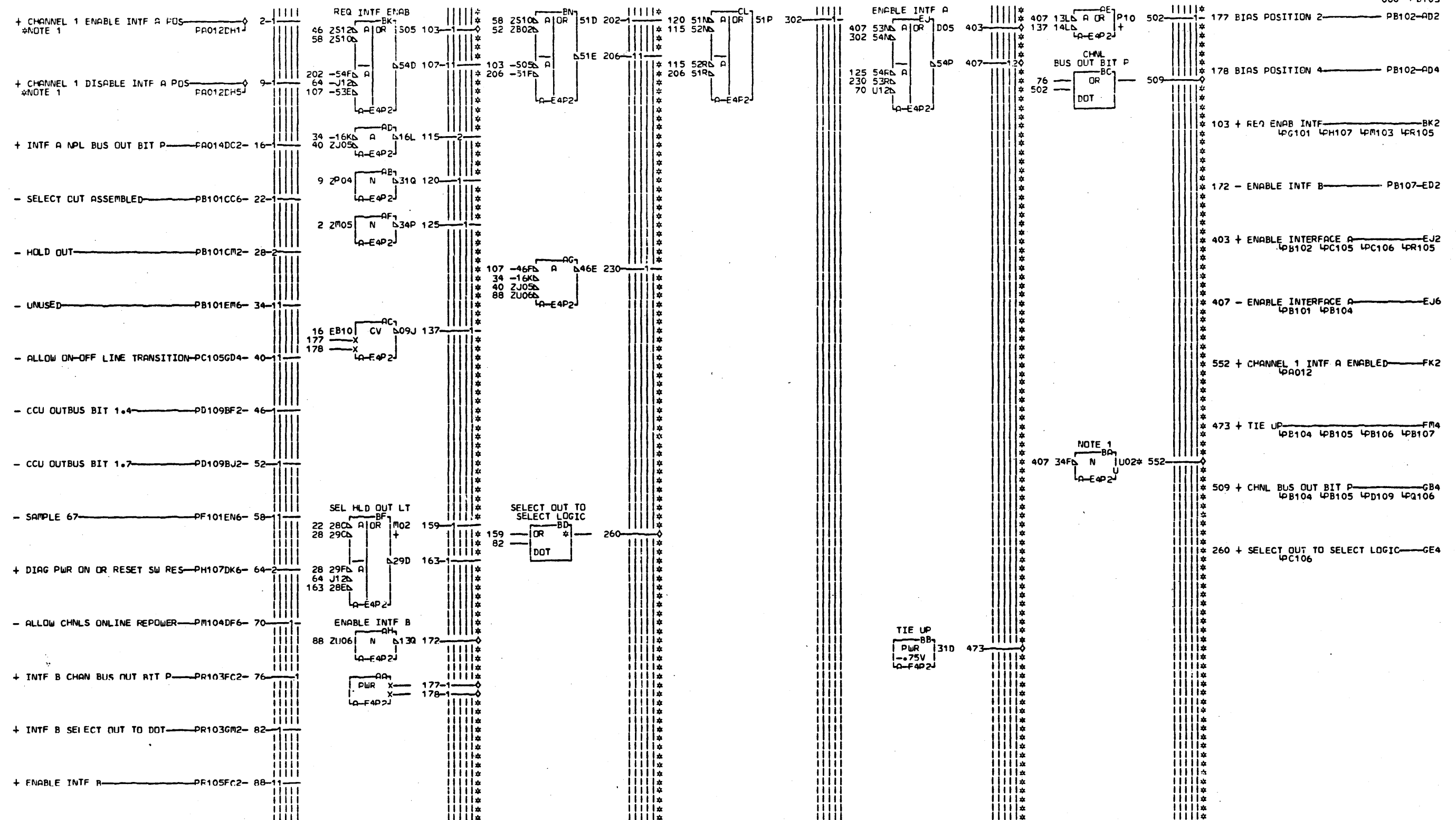
CHANNEL INTF TAGS + CONTROLS	
E-C-HISTORY 314402	C-MACH-27RNB FRAME 01
DATE LAST EC 05-17-76 314424	IBM CORP-SDD P.N. 1755017
	PB101 000



LOC. TYPE  
A-E4P2 7601

PB102  
000

CHANNEL INTF A RCVRS			
E.C.	HISTORY	C <sub>1</sub> MACH	27RNB
		FRAME	01
DATE	LAST EC	IBM CORP.	SDD PB102
02-23-76	314402	P.N.	1755018 000



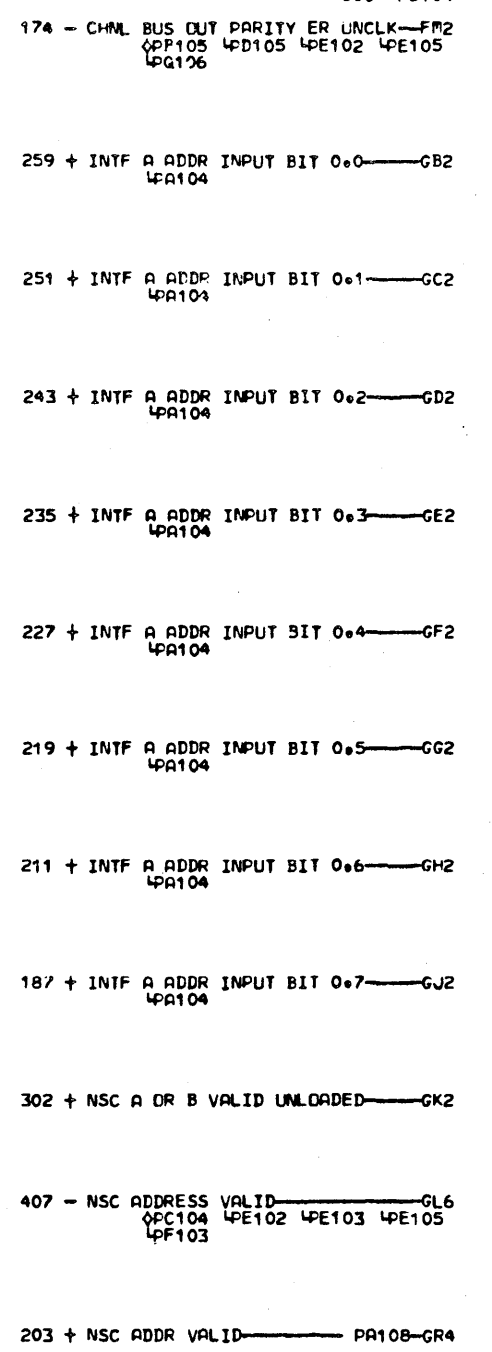
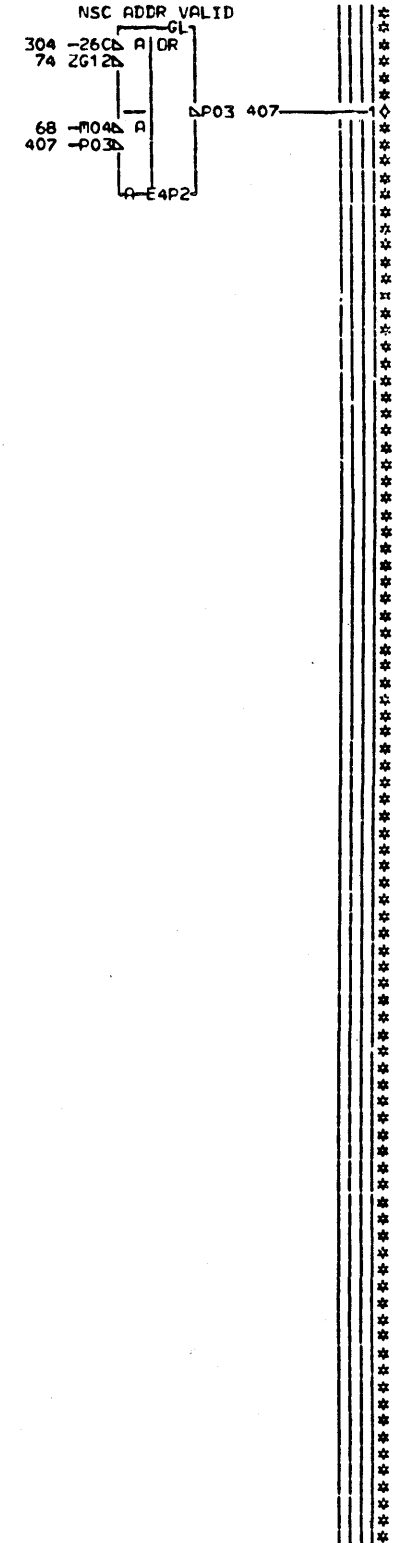
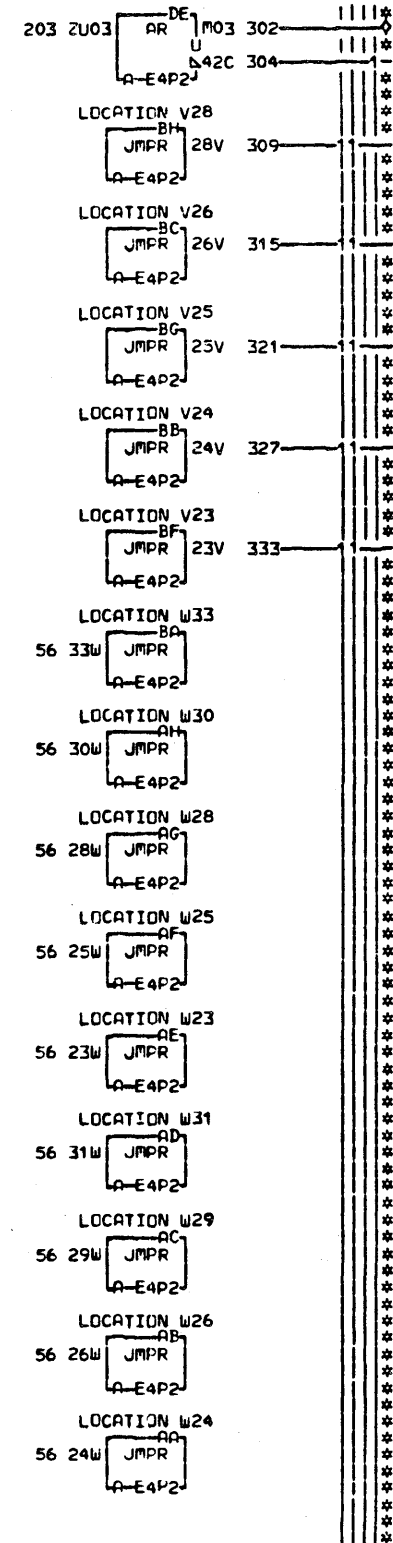
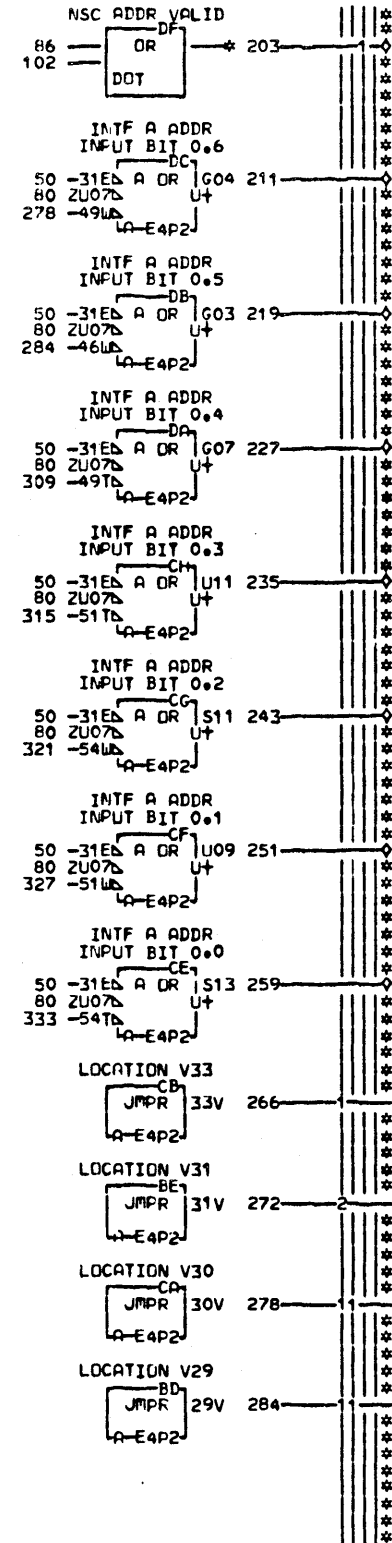
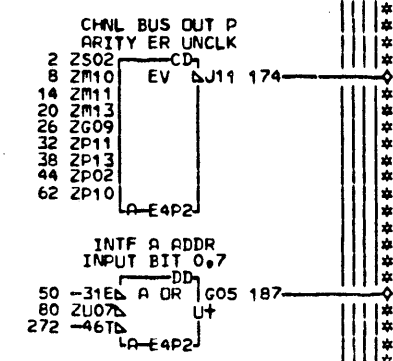
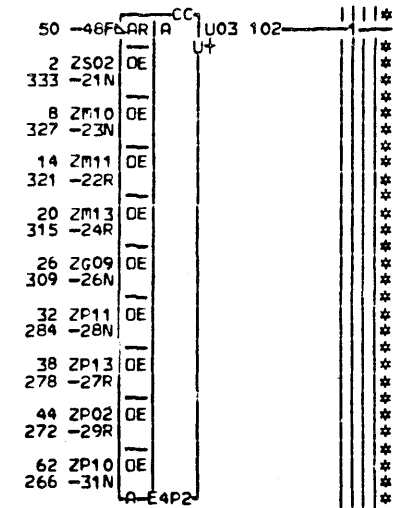
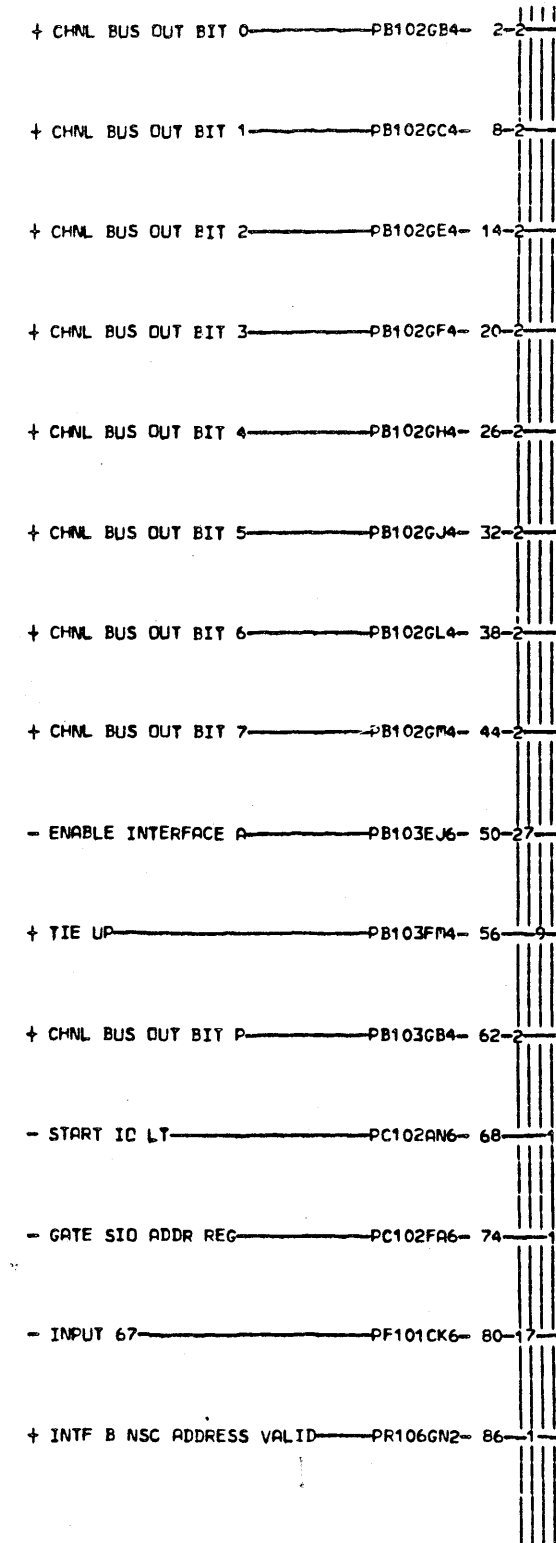
NOTE 1 SEE PA061 PGS 102 AND 3 FOR ENABLE/DISABLE WIRING CHANGES IF MORE THAN 2 CA-4S ARE INSTALLED

PB103  
000

FDGF CONN.  
552 A-E4C3B13  
01A-E4C5D02

LOC. TYPE  
A-E4P2 7601

INTERFACE A CONTRA.	
F.C. HISTORY	MACH. 27RNB
314402	FRAME 01
314424	IBM CORP. SDD
DATE LAST EC	P.N. 1755019
11-19-76 316677	000

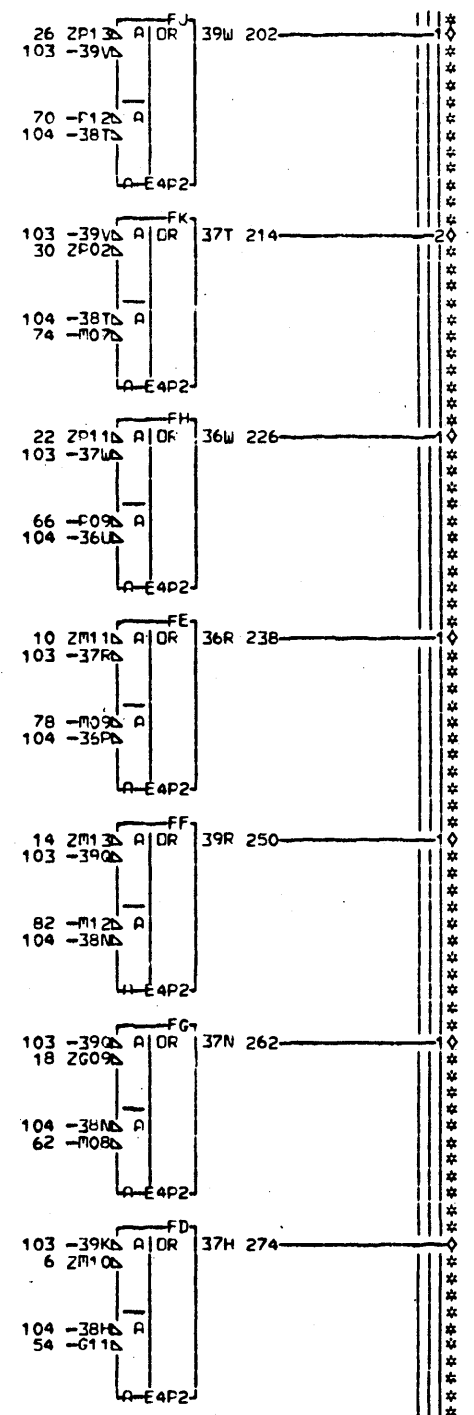
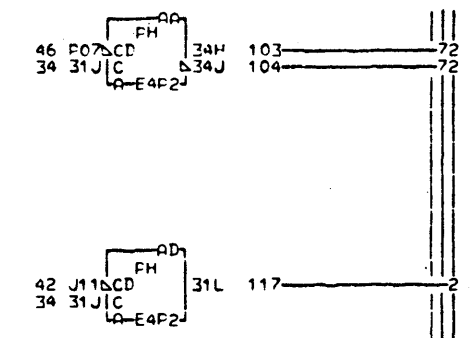
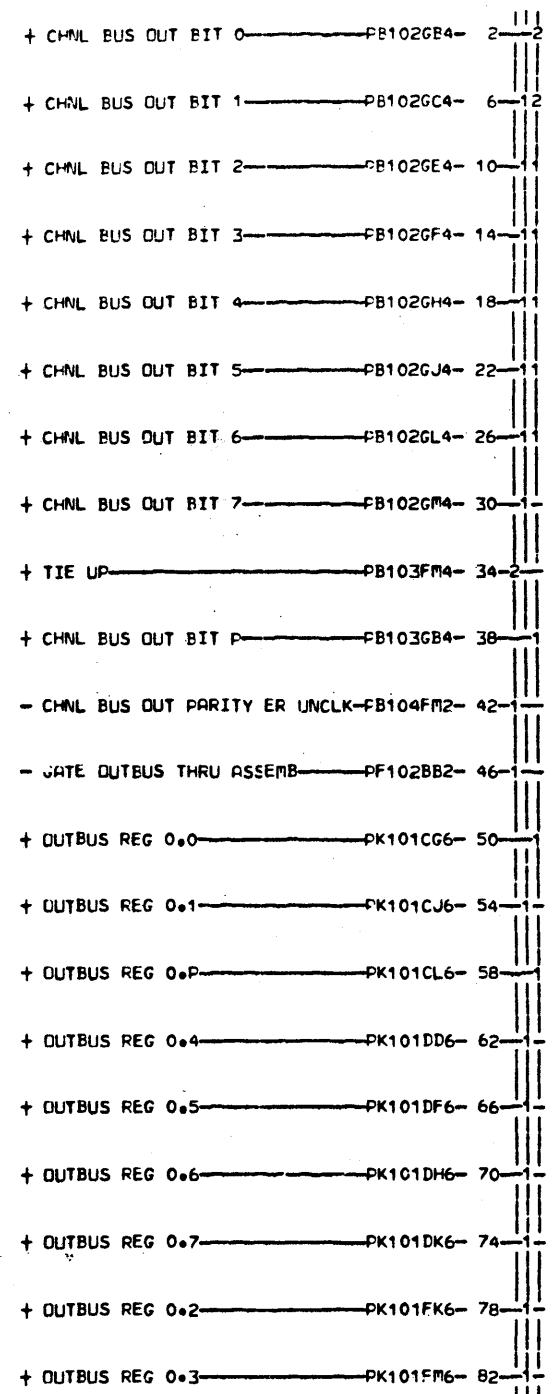


NOTE SEE PAGE PA050 NOTES FOR INTF A NSC CHANNEL ADDRESS JUMPERING

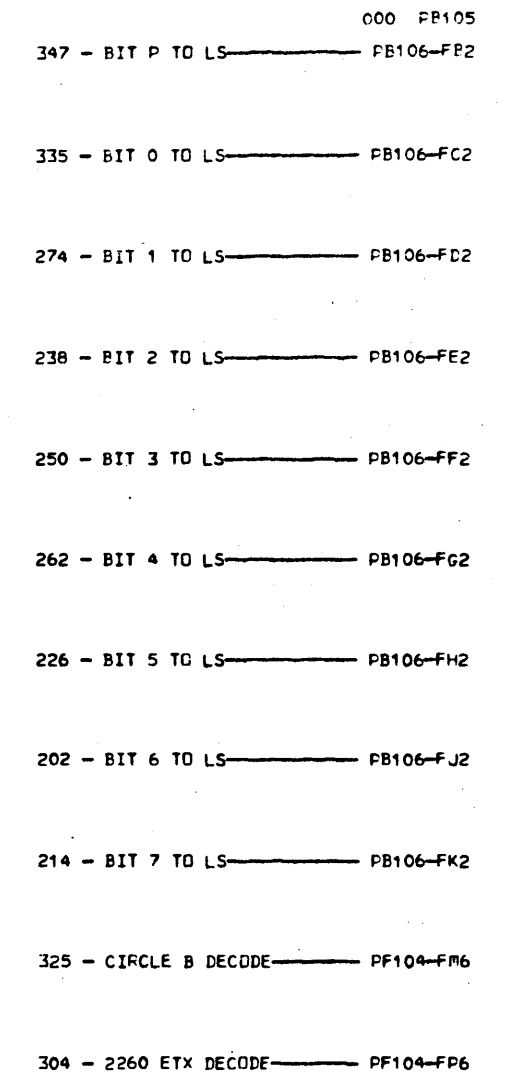
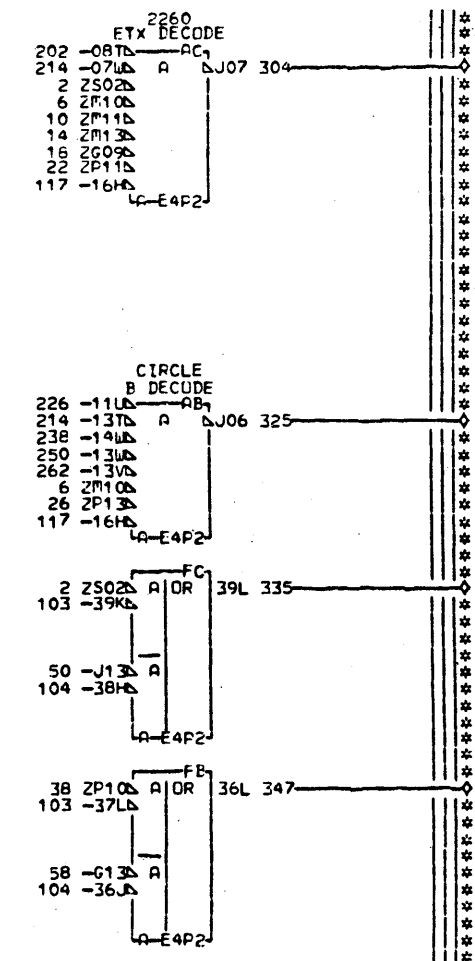
EDGE CONN. 203 RESISTOR A-E4P2U03

LOC. TYPE A-E4P2 7601

CHANNEL ADDRESS JUMPERING	
CHANNEL PARITY CHECK	
-E-C-HISTORY-	C-MACH-27RN8
FRAME	01
DATE	LAST EC
02-23-76	314402
IBM CORP. SDD	PB104
P.No. 1755020	000



LOC. TYPE  
A-E4F2 76C1

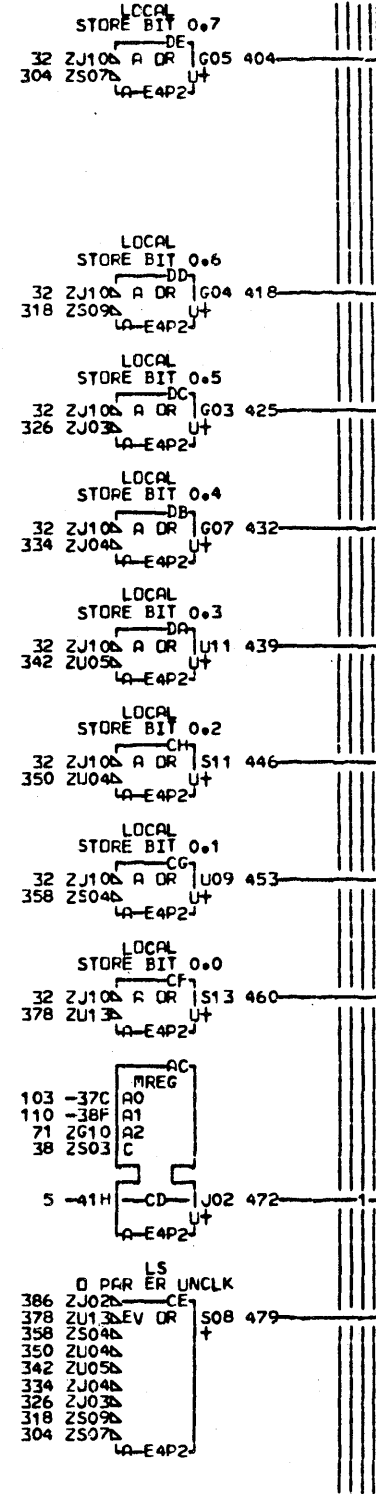
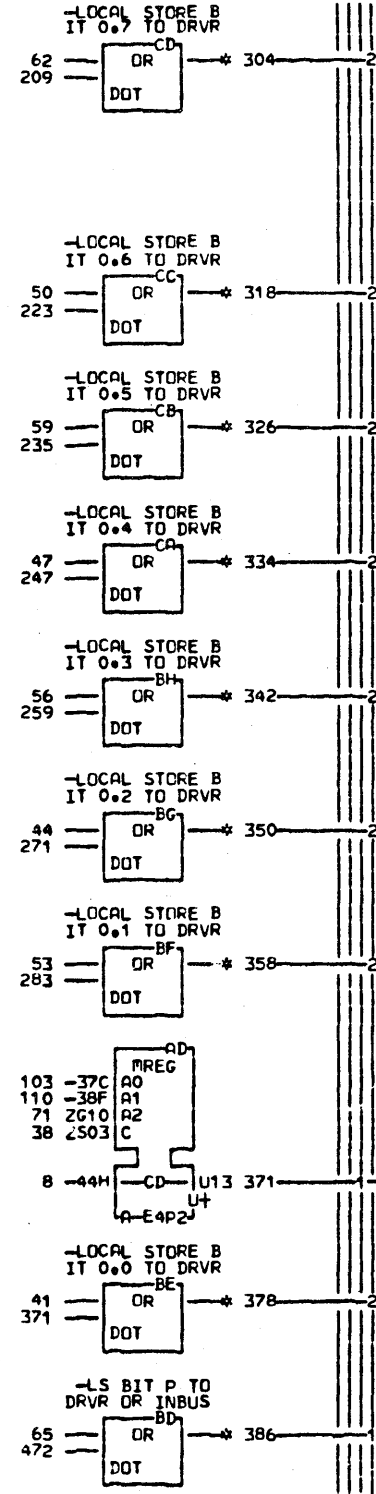
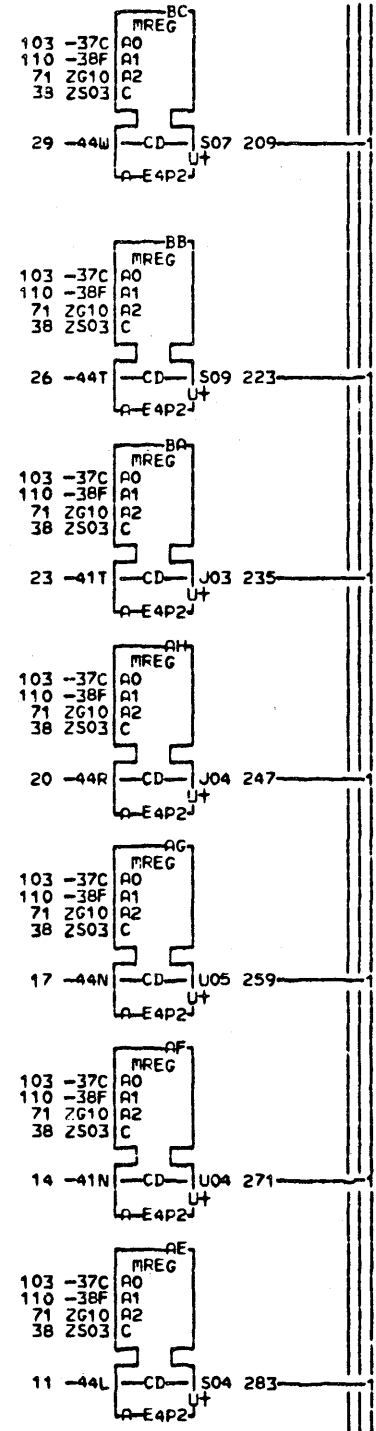
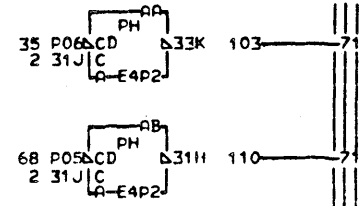


FB105  
000

NON EB LOCAL STUFF BYTE 0  
 ASSEMBLER  
 -E.C.-HISTORY-C, MACH.27RMB  
 FRAME 01  
 IBM CORP.SDD FB105  
 DATE LAST EC P.No. 1755021 000  
 02-23-76 314402



- + TIE UP PB103FM4- 2-2
- BIT P TO LS PB105FB2- 5
- BIT 0 TO LS PB103FC2- 8
- BIT 1 TO LS PB105FD2- 11
- BIT 2 TO LS PB105FE2- 14
- BIT 3 TO LS PB105FF2- 17
- BIT 4 TO LS PB105FG2- 20
- BIT 5 TO LS PB105FH2- 23
- BIT 6 TO LS PB105FJ2- 26
- BIT 7 TO LS PB105FK2- 29
- GATE LOCAL STORE ON INBUS PF102BF2- 32
- + SEL ADDR AND STATUS PF102GE6- 35
- + WRITE INTO LS 0 PF102GL6- 38
- L.S. BIT 0 EB TO DRV PK107BE6- 41
- L.S. BIT 2 EB TO DRV PK107BG6- 44
- L.S. BIT 4 EB TO DRV PK107BJ6- 47
- L.S. BIT 6 EB TO DRV PK107BL6- 50
- L.S. BIT 1 EB TO DRV PK107CE6- 53
- L.S. BIT 3 EB TO DRV PK107CG6- 56
- L.S. BIT 5 EB TO DRV PK107CJ6- 59
- L.S. BIT 7 EB TO DRV PK107CL6- 62
- L.S. BIT P EB TO DRV PK107CM6- 65
- + GATE SELECT DATA 1 AND 2 PL105DE2- 68
- + GATE SELECT DATA 3 AND 4 PL105DF2- 71



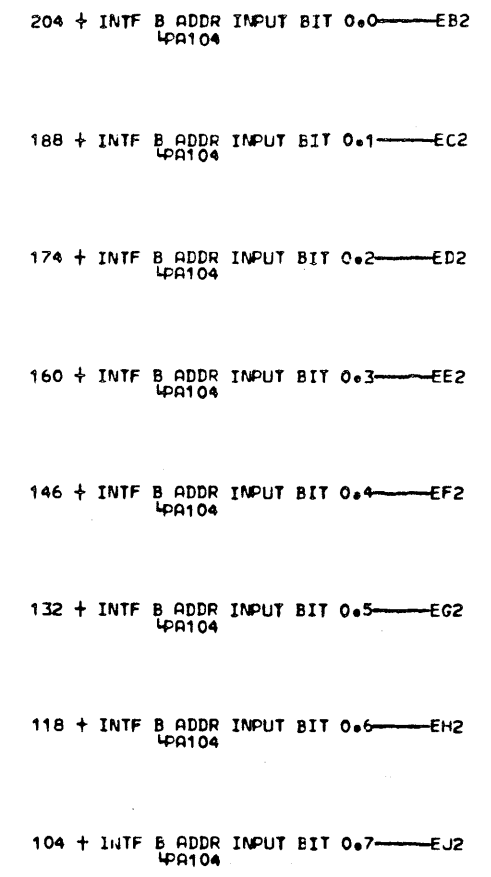
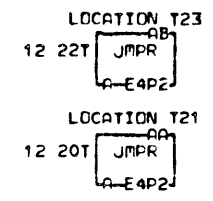
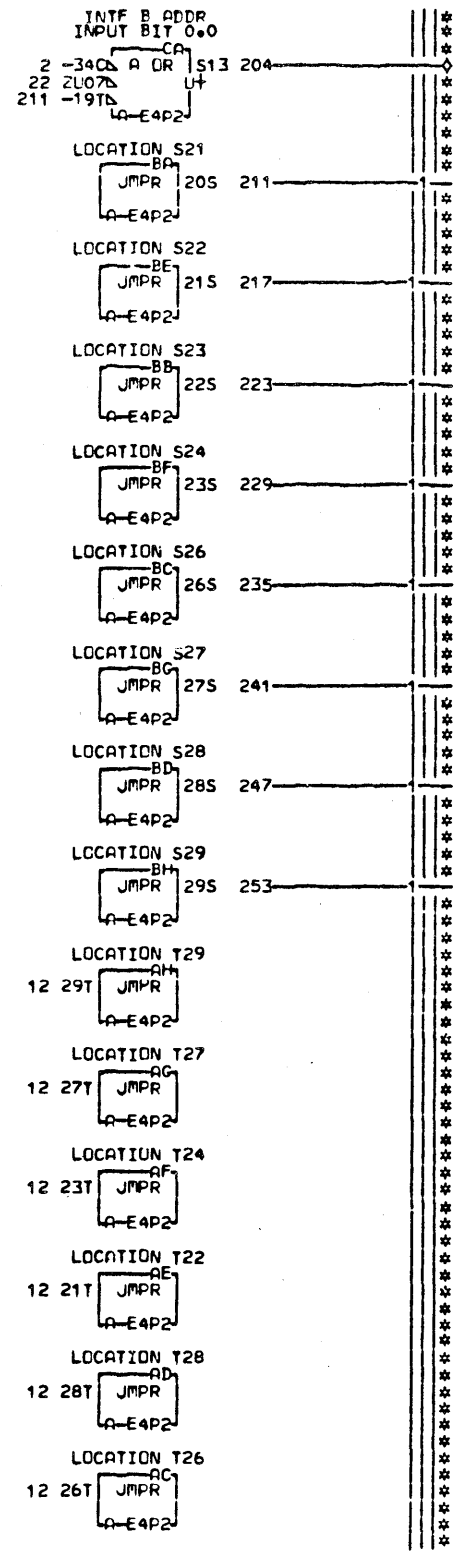
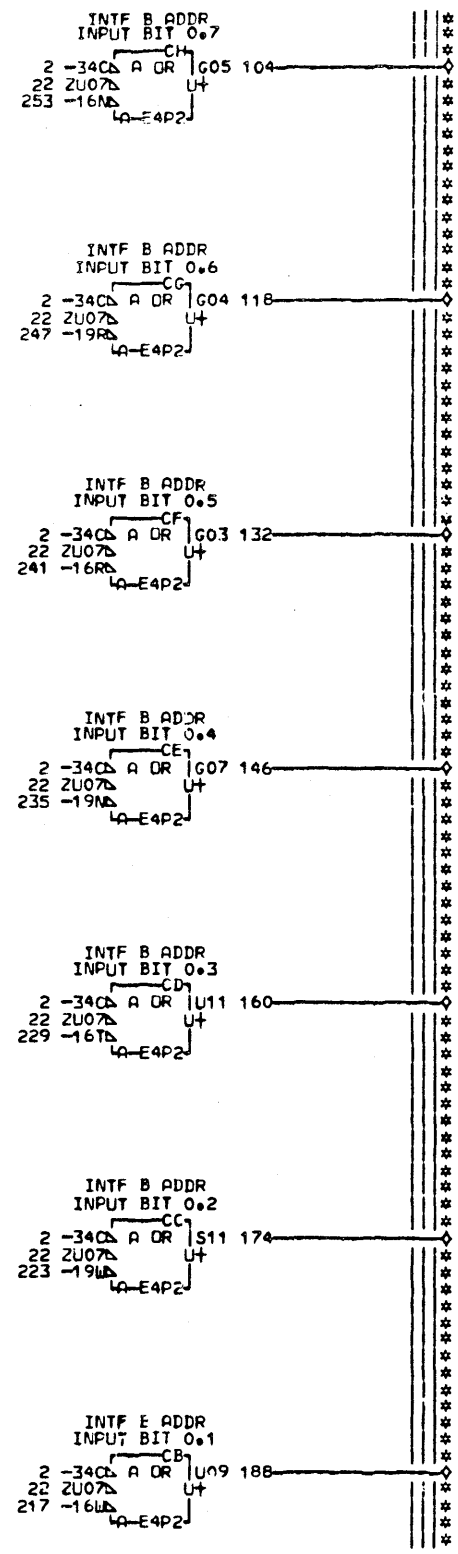
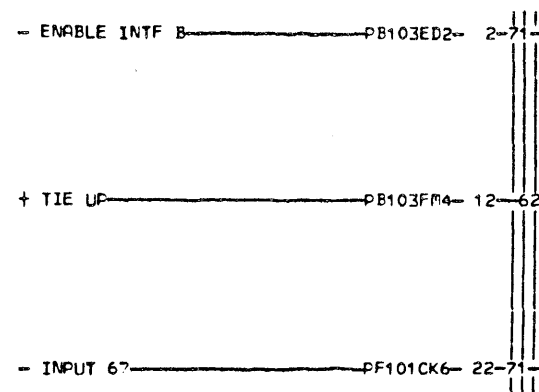
- 386 - LS BIT P TO DRV OR INBUS DB4  
LPH103 LPM104 LPS103
- 378 - LOCAL STORE BIT 0.0 TO DRV DC4  
LPH101 LPS101
- 358 - LOCAL STORE BIT 0.1 TO DRV DD4  
LPH101 LPS101
- 350 - LOCAL STORE BIT 0.2 TO DRV DE4  
LPH101 LPS101
- 342 - LOCAL STORE BIT 0.3 TO DRV DF4  
LPH102 LPS102
- 334 - LOCAL STORE BIT 0.4 TO DRV DG4  
LPH102 LPS102
- 326 - LOCAL STORE BIT 0.5 TO DRV DH4  
LPH102 LPS102
- 318 - LOCAL STORE BIT 0.6 TO DRV DJ4  
LPH103 LPS103
- 304 - LOCAL STORE BIT 0.7 TO DRV DK4  
LPH103 LPS103
- 479 + LS 0 PAR ER UNCLK PF105-FM2
- 460 + LOCAL STORE BIT 0.0 PA104-GC2
- 453 + LOCAL STORE BIT 0.1 PA104-GD2
- 446 + LOCAL STORE BIT 0.2 PA104-GE2
- 439 + LOCAL STORE BIT 0.3 PA104-GF2
- 432 + LOCAL STORE BIT 0.4 PA104-GG2
- 425 + LOCAL STORE BIT 0.5 PA104-GH2
- 418 + LOCAL STORE BIT 0.6 PA104-GJ2
- 404 + LOCAL STORE BIT 0.7 PA104-GK2

EDGE CONN.	A-E4P2U05
304 RESISTOR	350 RESISTOR
A-E4P2S07	A-E4P2U04
318 RESISTOR	358 RESISTOR
A-E4P2S09	A-E4P2S04
326 RESISTOR	378 RESISTOR
A-E4P2J03	A-E4P2U13
334 RESISTOR	386 RESISTOR
A-E4P2J04	A-E4P2J02
342 RESISTOR	

PB106  
000

LOC. TYPE  
A-E4P2 7601

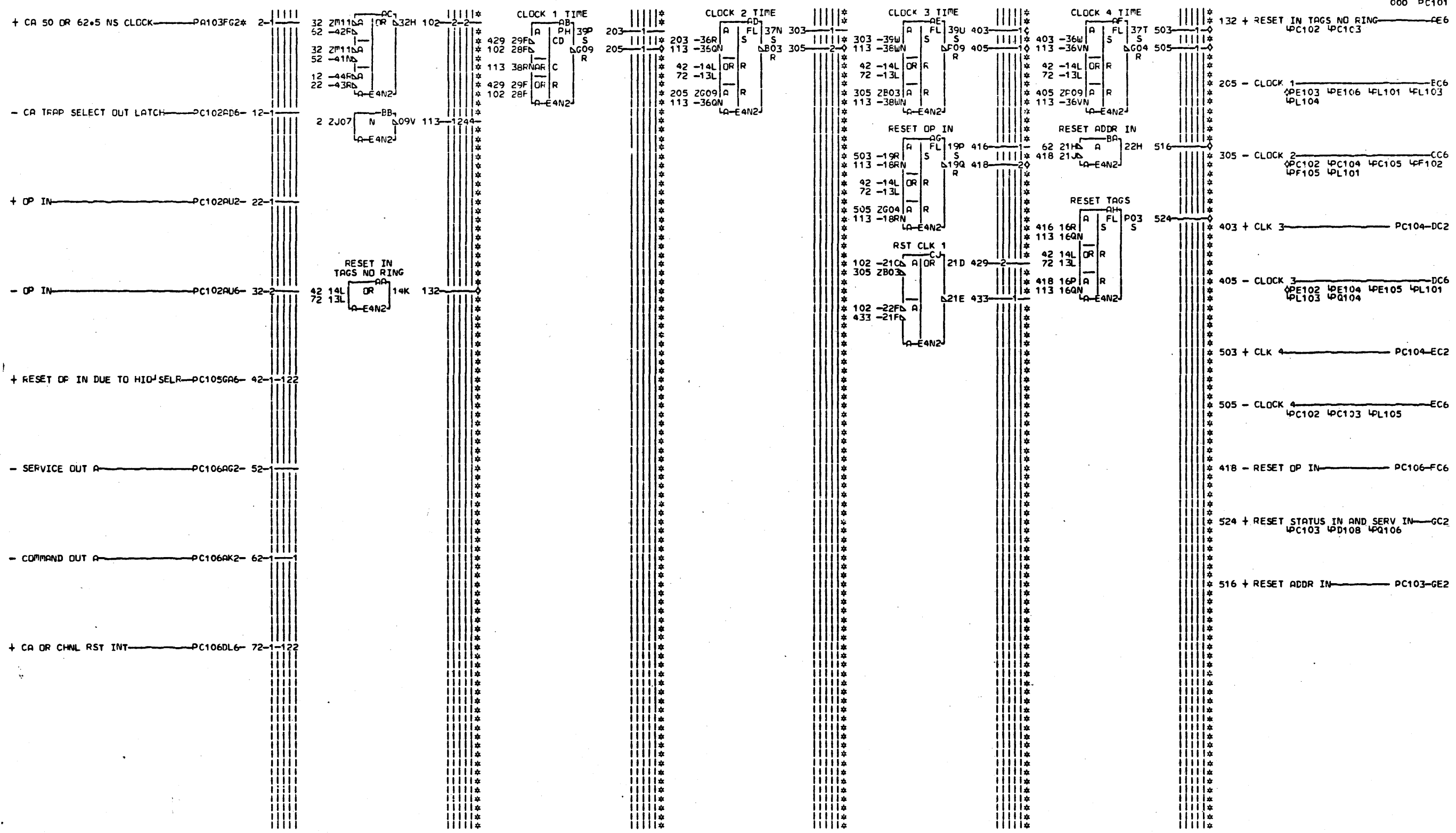
NON EB LOCAL STORE BYTE 0	
E.C.-HISTORY	C-MACH.27RNB
DATE	FRAME 01
LAST EC	IBM CORP.SDD
02-23-76 314402	P.No. 1755022
	000



NOTE 1 SEE PAGE PA050 NOTES FOR INTF B INPUT 67 NSC ADDRESS JUMPING

LOC. TYPE A-E4P2 7601

INTERFACE B ADDRESS	
E-C-HISTORY	C-MACH#27RNB
DATE	LAST EC
02-23-76	314402
FRAME	01
IBM CORP.SDD	PB107
P.N. 1755023	000

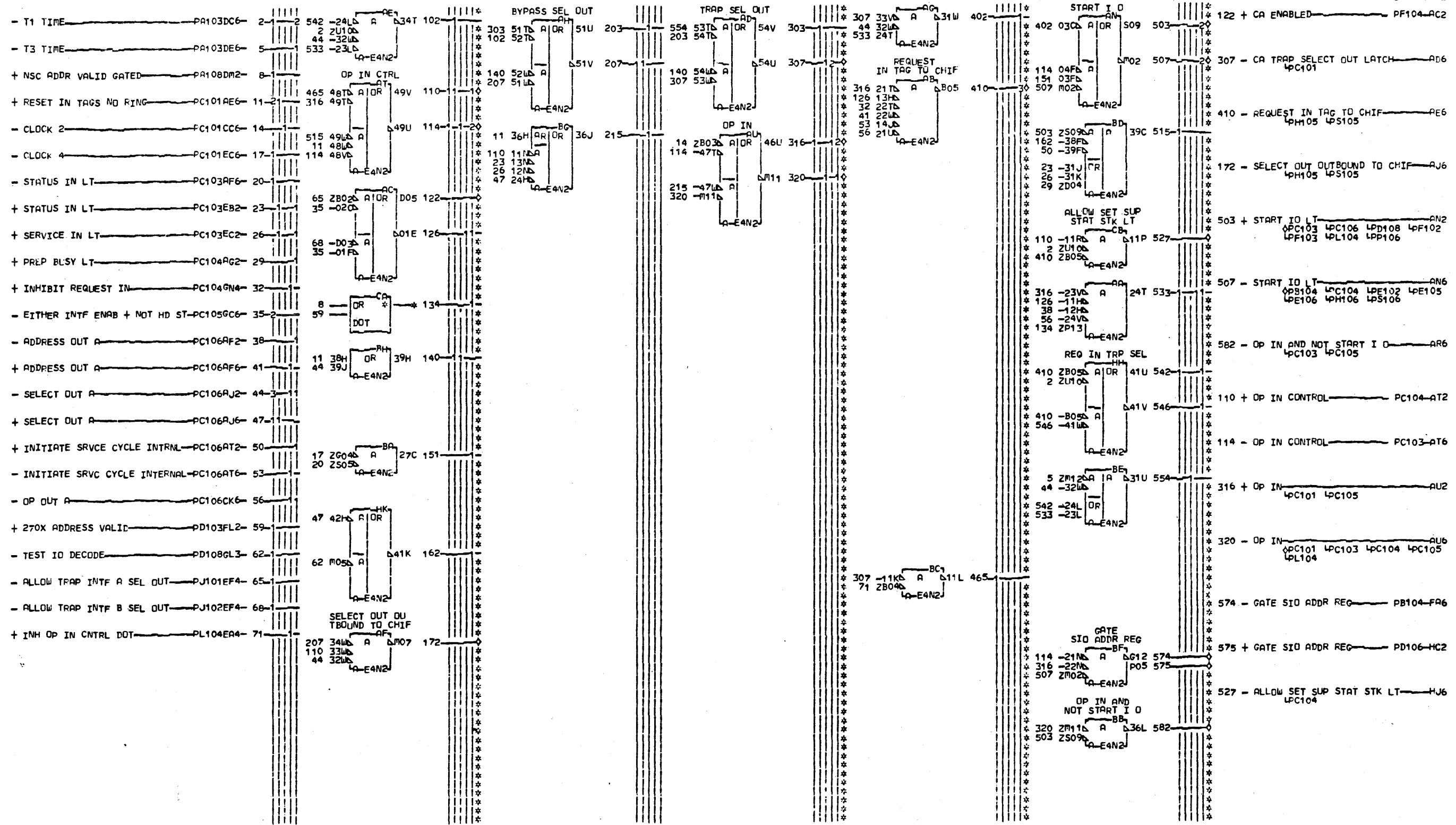


EDGE CONN.  
2 RESISTOR  
A-E4N2J07

LOC. TYPF  
A-E4N2 CE26

PC101  
000

CHANNEL TAG CONTROL	
TAG CLOCK	
F.C. HISTORY	D. MACH. 27RNB
314402	
FRAME	01
DATE LAST FC	IBM CORP. SDD
11-19-76 316677	P.N. 1755024
	PC101
	000

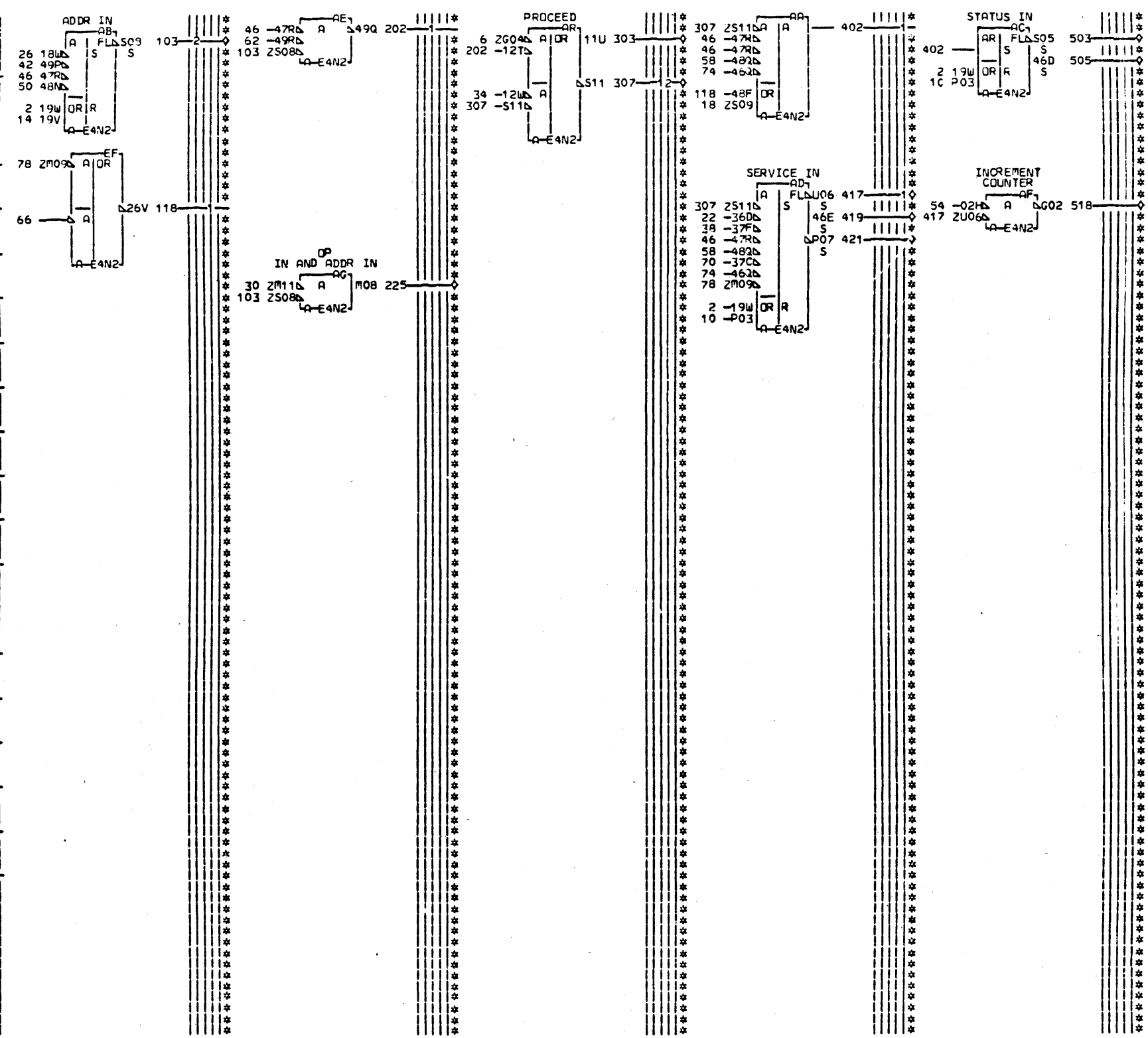


EDGE CONN.  
134 RESISTOR  
A-E4N2P13

LOC. TYPE  
A-E4N2 CE26

CHANNEL TAGS CONTROL	
START IO AND OPERATIONAL IN	
E-C-HISTORY	E-MACH-27RNB
314402	FRAME 01
314424	
315620	
DATE LAST EC	IBM CORP. SDD PC102
08-09-79 321749	P.N. 1755025 000

+ RESET IN TAGS NO RING — PC101AE6 — 2-1-1  
 - CLOCK 4 — PC101EC6 — 6-1-1  
 + RESET STATUS IN AND SERV IN — PC101GC2 — 10-1-1  
 + RESET ADDR IN — PC101GE2 — 14-1-1  
 + START IO LT — PC102AN2 — 18-1-1  
 - OP IN AND NOT START I O — PC102AR6 — 22-1-1  
 - OP IN CONTROL — PC102AT6 — 26-1-1  
 - OP IN — PC102AU6 — 30-1-1  
 + NOT OP IN OR STACK PULSE — PC105AJ6 — 34-1-1  
 + CHANNEL STOP — PC105AY2 — 38-1-1  
 + PROCEED OR STACK STATUS PULS — PC105CM6 — 42-1-1  
 + RESET OP IN DUE TO HIGH SELR — PC105GA6 — 46-1-3  
 + ADDRESS OUT A — PC106AF6 — 50-1-1  
 - SERVICE OUT A — PC106AG2 — 54-1-1  
 + SERVICE OUT A — PC106AG6 — 58-1-2  
 - COMMAND OUT A — PC106AK2 — 62-1-1  
 + INITIATE SRVCE CYCLE INTRNL — PC106AT2 — 66-1-1  
 - INITIATE SRVC CYCLE INTERNAL — PC106AT6 — 70-1-1  
 + COMMAND OUT A — PC106FK2 — 74-1-2  
 + STATUS AVAILABLE — PE103GJ4 — 78-1-1



000 PC103

103 - ADDRESS IN LT — AR6  
 PC104 WP102 WP104 WH104  
 WP106 WP5104 WP5106  
 503 - STATUS IN LT — AF6  
 PC102 WP104 WP105 WP102  
 WPE103 WPE106 WPF102 WPF103  
 417 - SERVICE IN LT — FL6  
 PC105 WPE103 WPE105 WPF102  
 WPL102 WPL105  
 303 + PROCEED LT — PC105-AR2  
 307 - PROCEED LT — AR6  
 WPH107 WP5107  
 518 - INCREMENT COUNTER — AX6  
 WPE104 WPF102 WPL101 WPL103  
 WPL104 WQ104  
 505 + STATUS IN LT — PC102-EB2  
 419 + SERVICE IN LT — PC102-EC2  
 421 - SERVICE IN LT DRIVEN — P2102-FF2  
 225 + OP IN AND ADDR IN — FG2  
 WPH106 WP5106

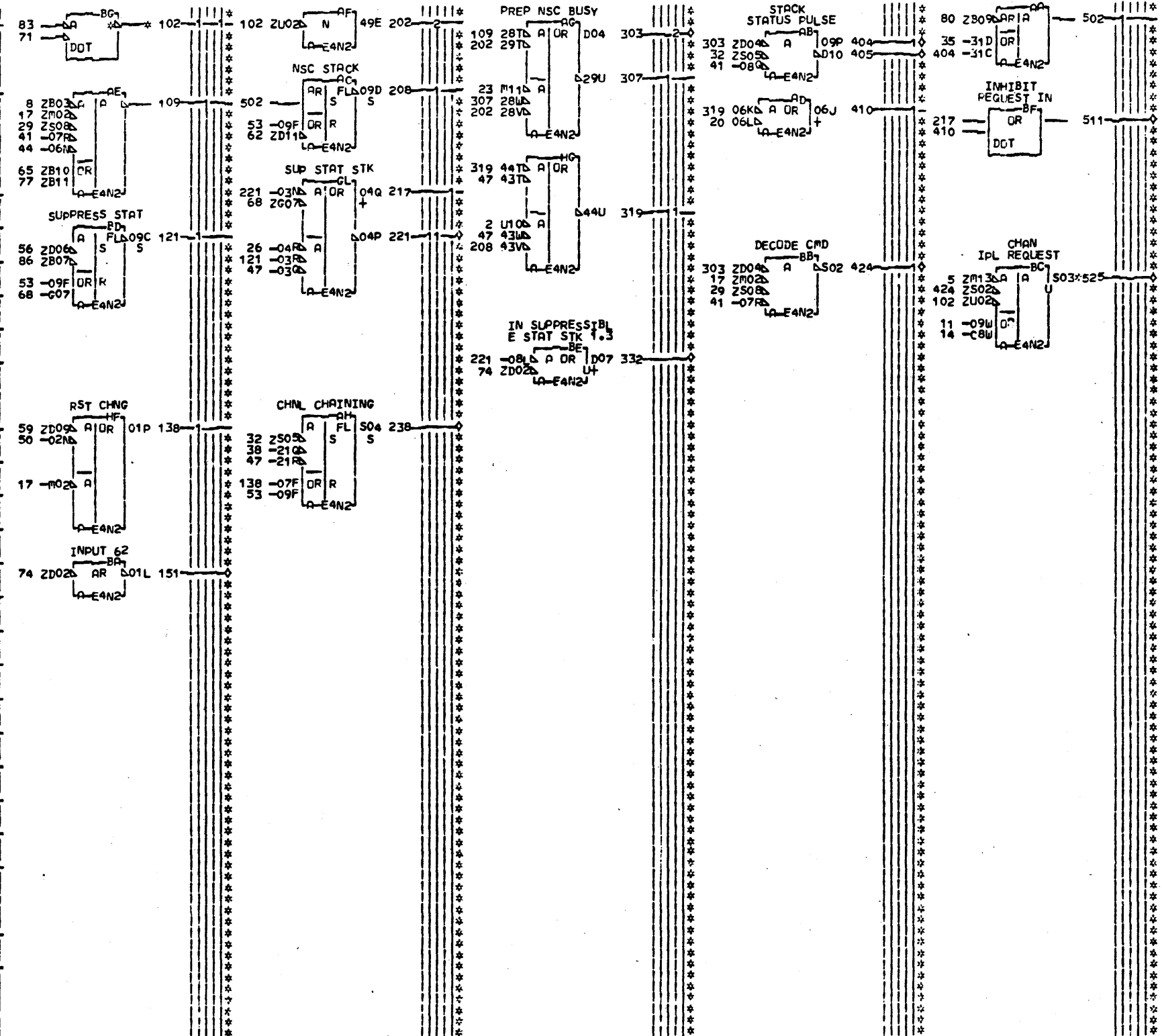
LOC. TYPE  
A-E4N2 CE26

PC103  
000

CHANNEL TAGS CONTROL	
TAG IN LATCHES	
E.C. HISTORY — 314402	D. MACH. 27RN8
314424	FRAME 01
DATE LAST EC	IBM CORP. SDD
07-14-76 315620	P. No. 1755026

PC103  
000

- T1 TIME - PA103DC6 2  
 - NSC ADDRESS VALID - PB104GL6 5  
 - CLOCK 2 - PC101CC6 8  
 + CLK 3 - PC101DC2 11  
 + CLK 4 - PC101EC2 14  
 - START IO LT - PC102AN6 17-2  
 + OP IN CONTROL - PC102AT2 20  
 - OP IN - PC102AU6 23  
 - ALLOW SET SUP STAT STK LT - PC102HJ6 26  
 - ADDRESS IN LT - PC103AA6 29-1  
 - STATUS IN LT - PC103AF6 32-1  
 + HALT IO RESET - PC105BA2 35  
 - SERVICE OUT A - PC106AG2 38-1  
 - COMMAND OUT A - PC106AK2 41-2  
 - CA ACTIVE A - PC106AU6 44-1  
 - SUPPR OUT A - PC106AV2 47-22  
 + SUPPR OUT A - PC106AV6 50-1  
 + CA OR CHNL RST INT - PC106DL6 53-2  
 - CCU OUTBUS BIT 1.3 - PD109BE2 56  
 + NSC CE STATUS - PE103FF2 59  
 - NSC XFER ACPTED RESET - PE103GN6 62-1  
 + NSC CE SVC INT - PE103GR2 65  
 + DATA SVC RESET - PE105BK6 68-1  
 - ALLOW IPL DEC - PE105DM6 71  
 - INPUT 62 - PF101CE6 74  
 + ALLOW PREP BUSY - PF103FE2 77  
 + ESC MODE ENAB - PG102CH2 80  
 - ALLOW IPL FLAG TO CCU - PG102HA2 83  
 - SAMPLE OUT 62 REPOWER - PM102AK6 86-1



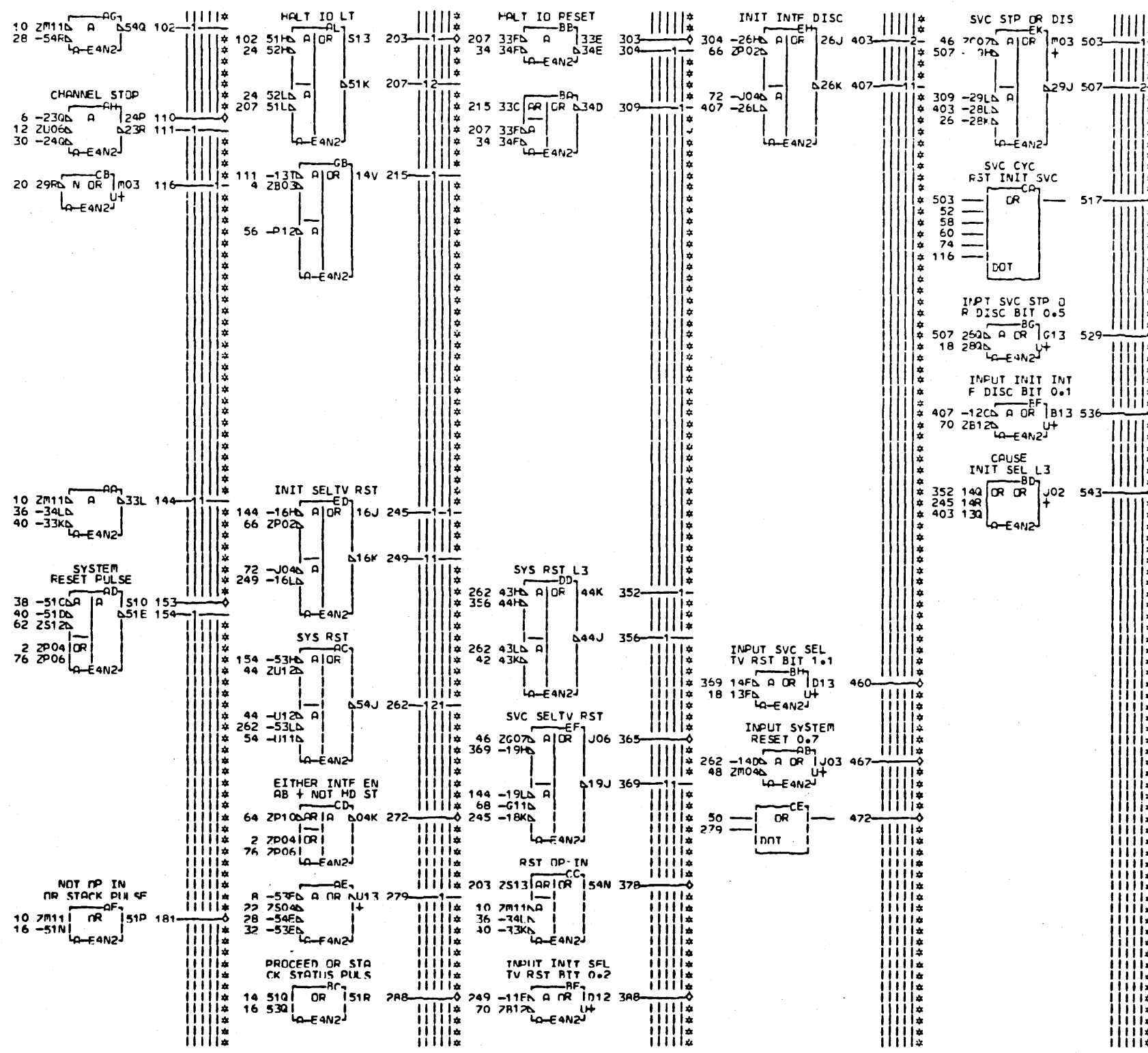
000 PC104  
 404 + STACK STATUS PULSE - PC105-AA2  
 405 - STK STATUS PULSE - AA6  
 303 + PREP BUSY LT - AG2  
 151 - INPUT 62 - PC105-CL6  
 424 - DECODE CMD - FG6  
 525 + CHAN IPL REQUEST - PR013-GH2  
 221 - SUPPRESSIBLE STAT STK - PC105-GL6  
 332 + IN SUPPRESSIBLE STAT STK 1.3 - GM2  
 511 + INHIBIT REQUEST IN - PC102-GM4  
 238 + CHAINING IND - H2

EDGE COMN.  
 102 RESISTOR  
 A-E4N2U02  
 525 A-E4B3D10  
 01A-E4B5D10

LOC. TYPE  
 A-E4N2 CE26

CHANNEL TAGS CONTROL	
STACK CHAINING STOP OR HALT IO	
E.C. HISTORY	MACH. 27RNB
314402	
314424	FRAME 01
316677	
DATE LAST EC	TBM CORR. STD PC104
08-09-79 321749	P.N. 1755027 000

+ ENABLE INTERFACE A PB103EJ2- 2-1  
- CLOCK 2 PC101CC6- 4-1  
- OP IN AND NOT START I O PC102AR6- 6-1  
+ OP IN PC102AU2- 8-1  
- OP IN PC102AU6- 10-3-1  
- SERVICE IN LT PC103AL6- 12-1  
+ PROCEED LT PC103AR2- 14-1  
+ STACK STATUS PULSE PC104AA2- 16-1  
- INPUT 62 PC104CL6- 18-1  
- SUPPRESSIBLE STAT STK PC104GL6- 20-1  
+ CHAINING IND PC104HA2- 22-1  
- ADDRESS OUT A PC106AF2- 24-2  
+ ADDRESS OUT A PC106AF6- 26-1  
+ SELECT OUT A PC106AJ6- 28-1  
- COMMAND OUT A PC106AK2- 30-1  
+ CA ACTIVE PC106AU2- 32-1  
- CA ACTIVE A PC106AU6- 34-2  
- SUPPR OUT A PC106AV2- 36-1  
+ SUPPR OUT A PC106AV6- 38-1  
+ OP OUT A PC106CK2- 40-2-1  
- OP OUT A PC106CK6- 42-1  
+ RESET SYS RST OR NSC PC106FC2- 44-1  
+ DATA SVC RESET PE105BK6- 46-1  
- INPUT 60 PF101CC6- 48-1  
+ INPUT 67 TO DOT PF104CF2- 50-1  
+ CS HARDSTOP RST INIT SVC CYC-PH107DF2- 52-1  
+ DIAG PWR ON OR RESET SW RES-PH107DK6- 54-1  
- CHN STOP FROM CHAR DCD EB-PL101GC2- 56-1  
+ RST INIT SVC CYCLE EB-PL105BH2- 58-1  
+ RESET INIT SERV CYCLE-PM103DH2- 60-1  
+ GATE DATA TO INBUS REPOWER-PM104DD6- 62-1  
- ALLOW CHNLS ONLINE REPOWER-PM104DF6- 64-1  
+ GATE SET INIT SEL LATCHES-PP106DB2- 66-1  
- BLK SET OF SVC SEL RST LT-PP106DB6- 68-1  
- INPUT 60 TO TAG CNTRL-PP106FF6- 70-1  
+ RST INIT SEL TO TAG CNTRL-PP106FG2- 72-1  
+ CS RST INTT SVC CY LT-PA105GR2- 74-1  
+ ENABE INTF R-PR105FC2- 76-1



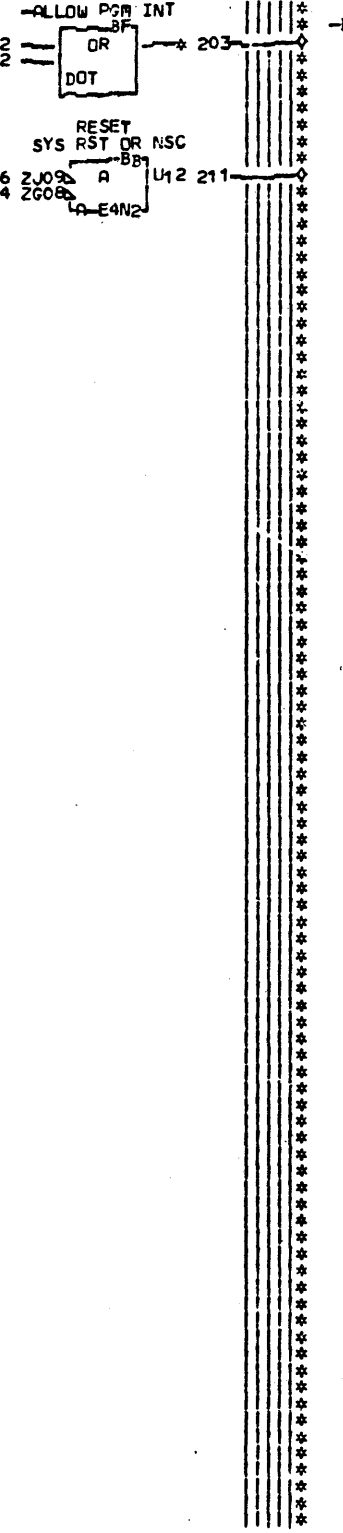
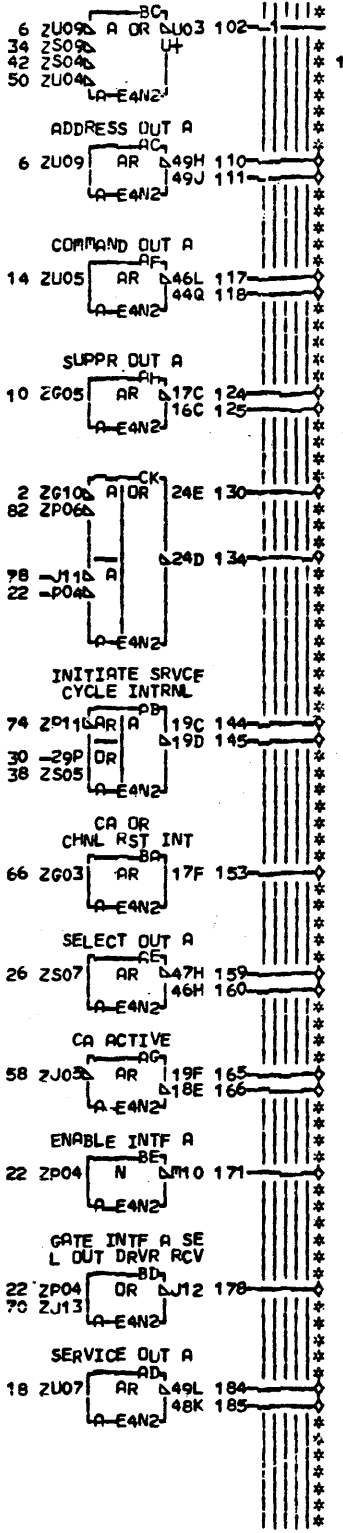
000 PC105  
\* 467 + INPUT SYSTEM RESET 0.7- PA104-AD2  
\* 153 + SYSTEM RESET PULSE- PH107-AF2  
\* 181 + NOT OP IN OR STACK PULSE- AJ6  
\* 203 + HALT IO LT- PL2  
\* 110 + CHANNEL STOP- PC103-AY2  
\* 303 + HALT IO RESET- PC104-BR2  
\* 288 + PROCEED OR STACK STATUS PULS- CM6  
\* 365 + SVC SELTV RST- EF2  
\* 543 + CAUSE INIT SEL L3- PE102-FC6  
\* 388 + INPUT INIT SELTV RST BIT 0.2- FD2  
\* 536 + INPUT INIT INTF DISC BIT 0.1- FH2  
\* 529 + INPT SVC STP OR DISC BIT 0.5- FK2  
\* 460 + INPUT SVC SELTV RST BIT 1.1- FX2  
\* 517 + SVC CYC RST INIT SVC- PE103-FY4  
\* 378 + RESET OP IN DUE TO HIW SELR- GA6  
\* 272 - EITHER INTF ENAB + NOT HD ST- GC6  
\* 472 - ALIOW ON-OFF LINE TRANSITION- GD4

LOC. TYPE  
A-E4N2 CE26

CHANNEL TAGS CONTROL	
ENABLE AND SEL SYS RESET	
-E.C.-HISTORY- D-MACH-27RNB	
314402	
314424	IFRAME 01
315620	IBM CORP.SDD PC105
DATE	LAST FC
11-19-76	316677
P.N.	1755028
000	



- OPERATIONS OUT INTF A PB101EC2- 2  
 + ADDRESS OUT PB101HA4- 6  
 + SUPPRESS OUT PB101HB4- 10  
 + COMMAND OUT PB101HC4- 14  
 + SERVICE OUT PB101HD4- 18  
 + ENABLE INTERFACE A PB103EJ2- 22  
 + SELECT OUT TO SELECT LOGIC PB103GE4- 26  
 - RESET OP IN PC101FC6- 30  
 + START ID LT PC102AN2- 34  
 - STATUS IN LT PC103AF6- 38  
 + CHAINING IND PC104HA2- 42  
 - CCU OUTBUS BIT 1-3 PD109BE2- 46  
 + SVC SEQ IN PRDQ PE103GS2- 50  
 - SAMPLE 67 FF101EN6- 54  
 - CA ACTIVE PF103FA2- 58  
 + INPUT 62 UNLD PG102HE2- 62  
 + DIAG CCU OR CHNL RESET PH107EM6- 66  
 + GTE INTF A SEL OUT DRV RCV PJ101EH2- 70  
 - INIT SVC CYC EB OR CS GATED PQ103BK6- 74  
 - OPERATION OUT INTF B PR103EF2- 78  
 + ENABLE INTF B FR105FC2- 82



-BLANK COLUMN-

-BLANK COLUMN-

26P 502  
 PUR  
 75V  
 A-E4N2

000 PC106  
 \* 502 + CE26 TIC LP AC4  
 \* 110 - ADDRESS OUT A AF2  
 \* LPC102 LPC105  
 \* 111 + ADDRESS OUT A AF6  
 \* LPC102 LPC103 LPC105  
 \* 184 - SERVICE OUT A AG2  
 \* LPC101 LPC103 LPC104  
 \* 185 + SERVICE OUT A PC103-AG6  
 \* 159 - SELECT OUT A PC102-AJ2  
 \* 160 + SELECT OUT A AJ6  
 \* LPC102 LPC105  
 \* 117 - COMMAND OUT A AK2  
 \* LPC101 LPC103 LPC104 LPC105  
 \* 144 + INITIATE SRVCE CYCLE INTRNL AT2  
 \* LPC102 LPC103  
 \* 145 - INITIATE SRVCE CYCLE INTERNAL AT6  
 \* LPC102 LPC103  
 \* 165 + CA ACTIVE PC105-AU2  
 \* 166 - CA ACTIVE A AU6  
 \* LPC104 LPC105  
 \* 124 - SUPPR OUT A AV2  
 \* LPC104 LPC105  
 \* 125 + SUPPR OUT A AV6  
 \* LPC104 LPC105  
 \* 130 + OP OUT A PC105-CK2  
 \* 134 - OP OUT A CK6  
 \* LPC102 LPC105  
 \* 153 + CA DR CHNL RST INT DL6  
 \* LPC101 LPC104  
 \* 211 + RESET SYS RST OR NSC FC2  
 \* LPC105 PF103 LPG101  
 \* 118 + COMMAND OUT A PC103-FK2  
 \* 178 - GATE INTF A SEL OUT DRVR RCV GE2  
 \* PB101 PH105  
 \* 171 - ENABLE INTF A GH2  
 \* PH105 LPH106  
 \* 203 - ALLOW PGR INT PC102-GJ4

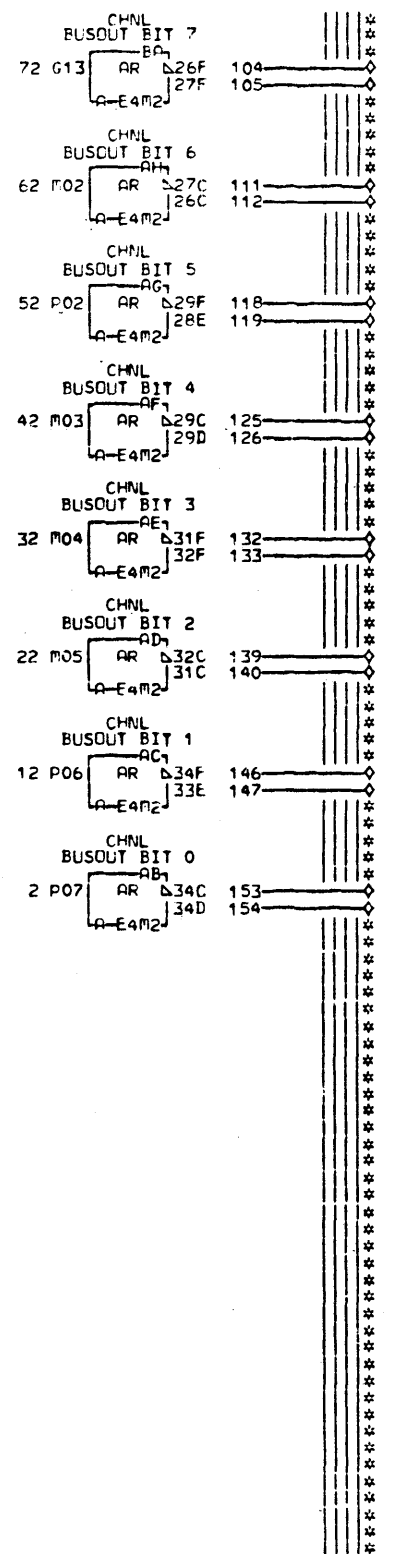
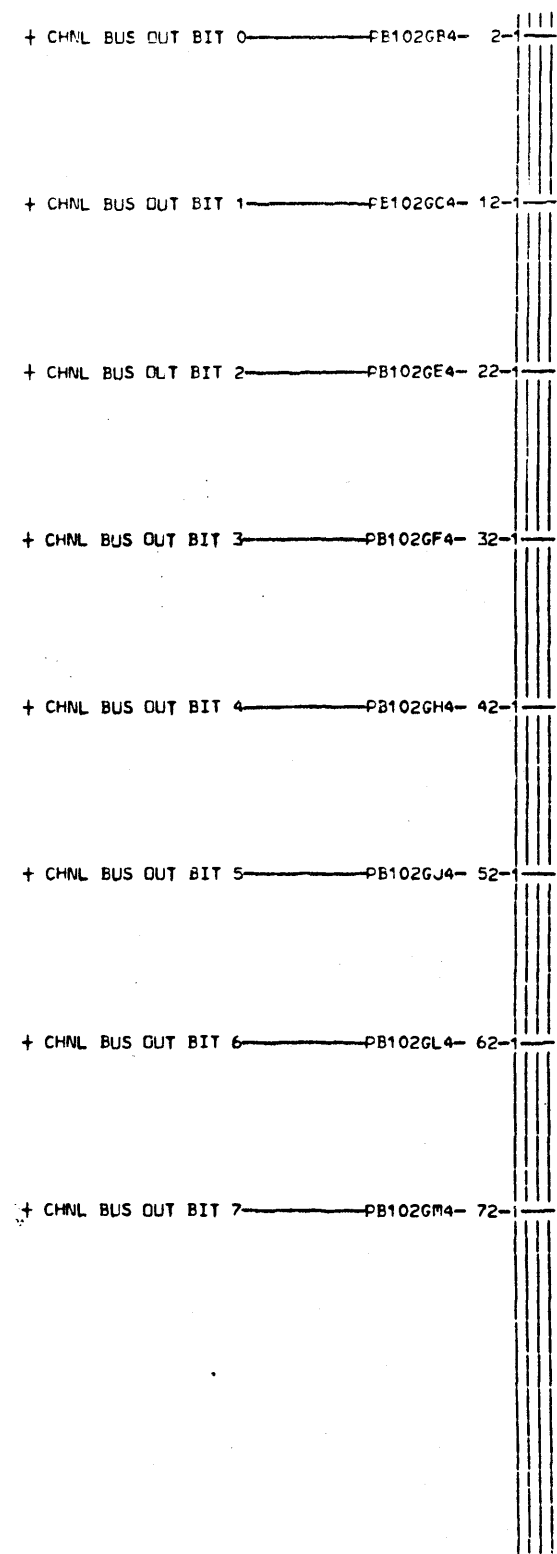
EDGE CONN.  
 203 RESISTOR  
 A-E4N2U03

LIC. TYPE  
 A-E4N2 CE26

PC106  
000

CHANNEL TAG CONTROL			
POWERING			
E.C.-HISTORY	E-MACH.27RN8		
314402		FRAME	01
314424		IBM CORP.SDP	PC106
316677			
DATE	LAST EC		
08-09-79	321749	P.N.	1755029 000



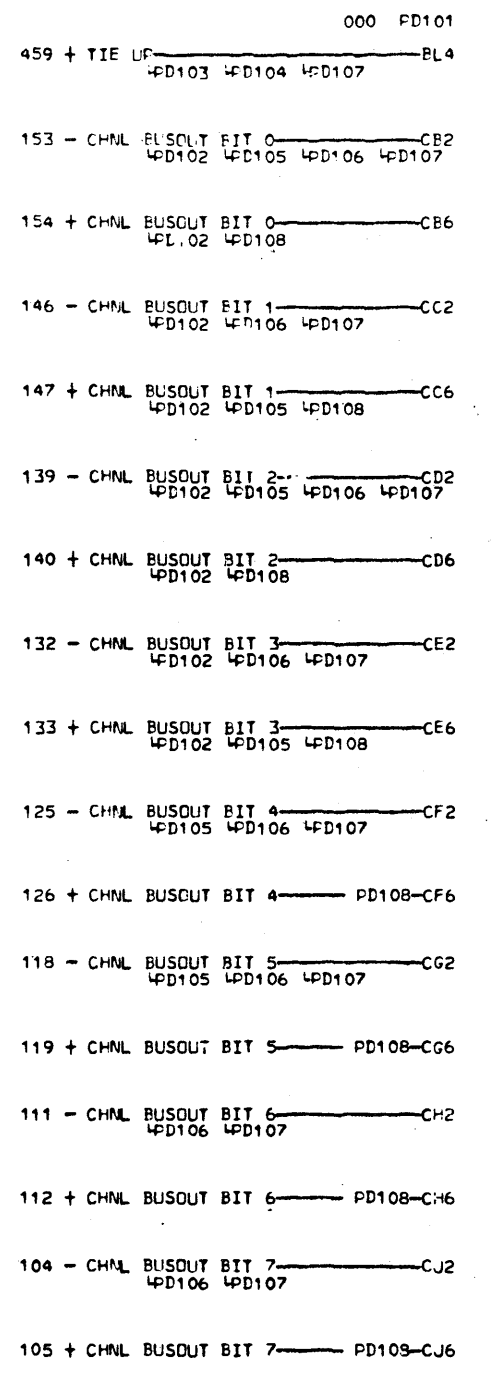


-BLANK COLUMN-

-BLANK COLUMN-

TIE UP  
PWR  
-75V  
A-E4M2

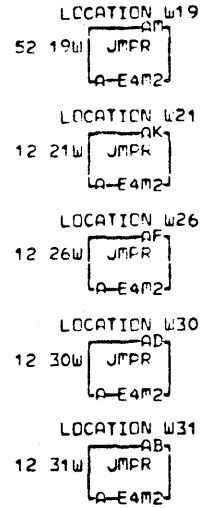
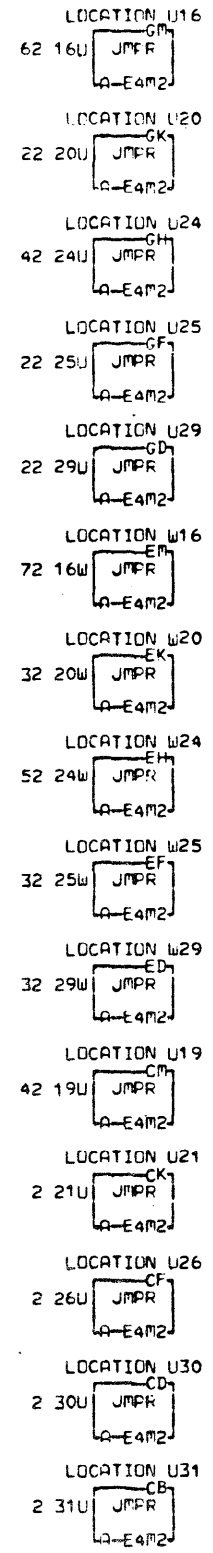
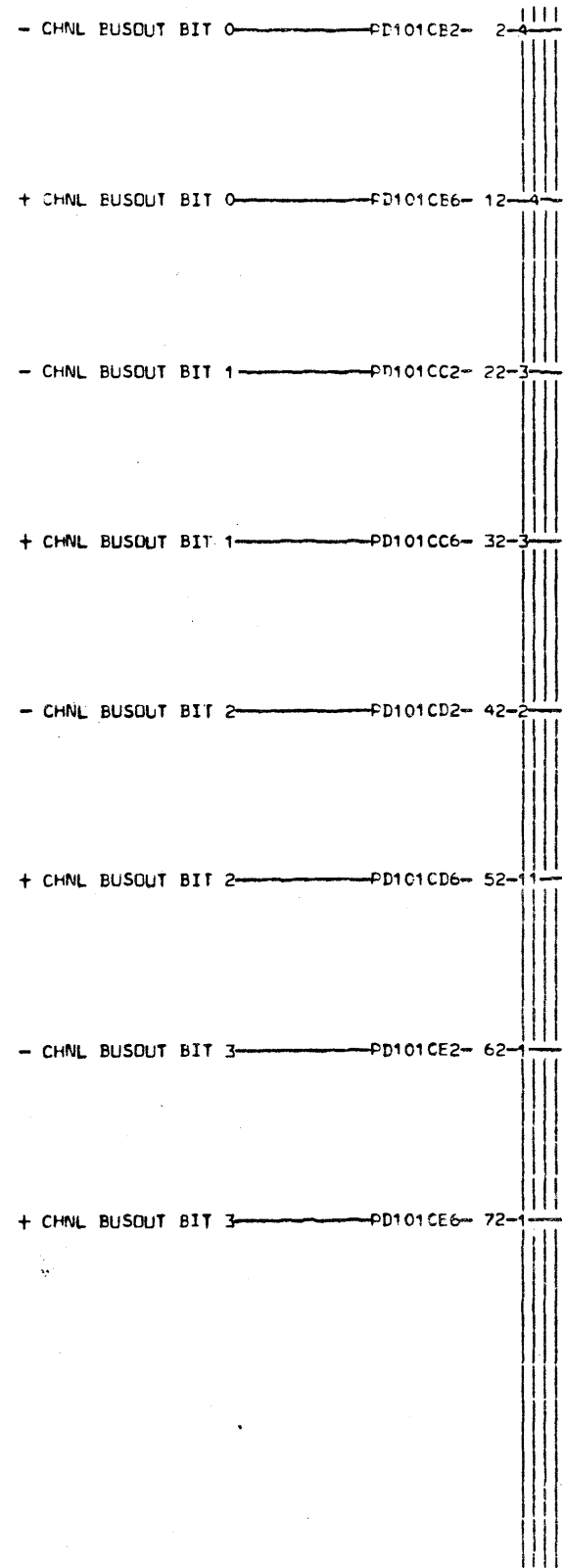
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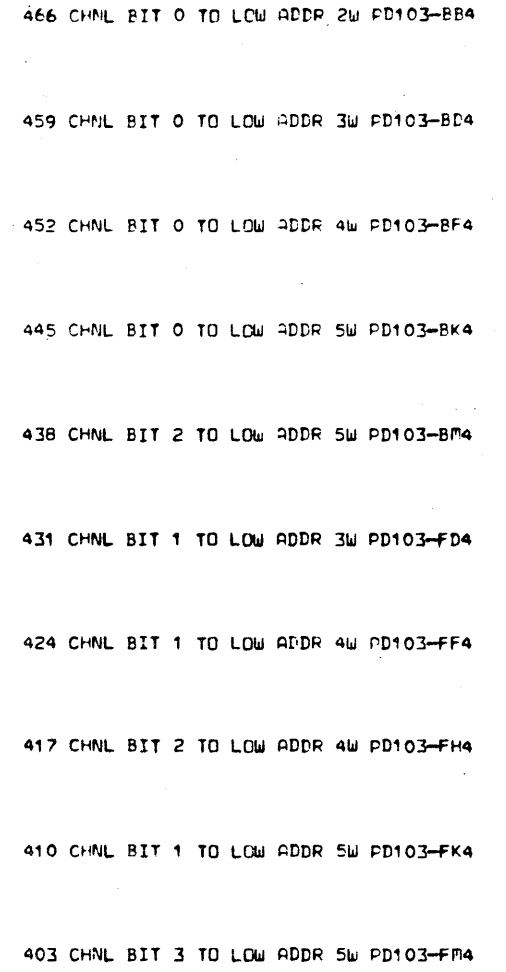
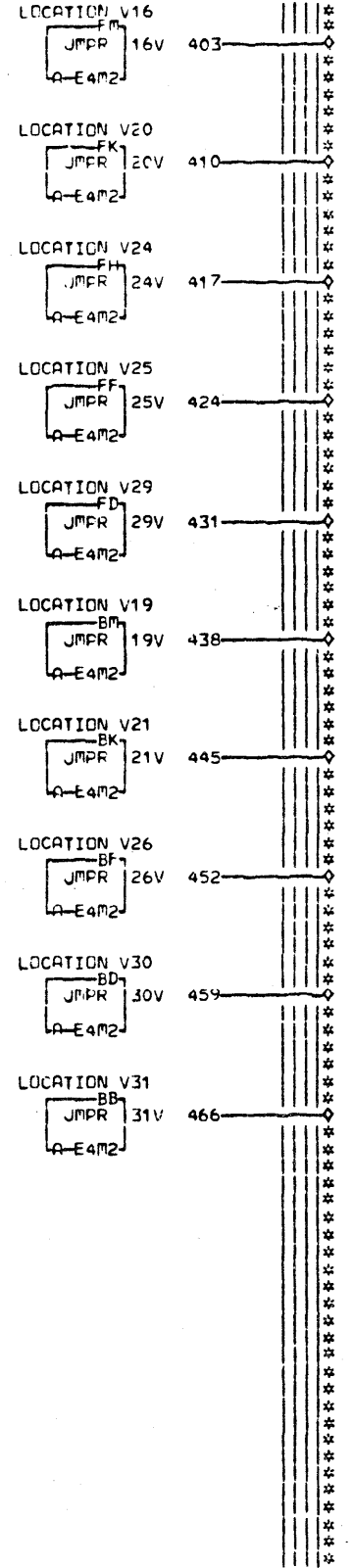
FD101  
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LUC TYPE  
A-E4M2 2325

CHNL BUS OUT REPOWER			
E.C.	HISTORY	C	MACH#27RNB
		FRAME	01
DATE	LAST EC	IBM CORP.	SDD FD101
02-23-76	314402	P.No.	1755030 000



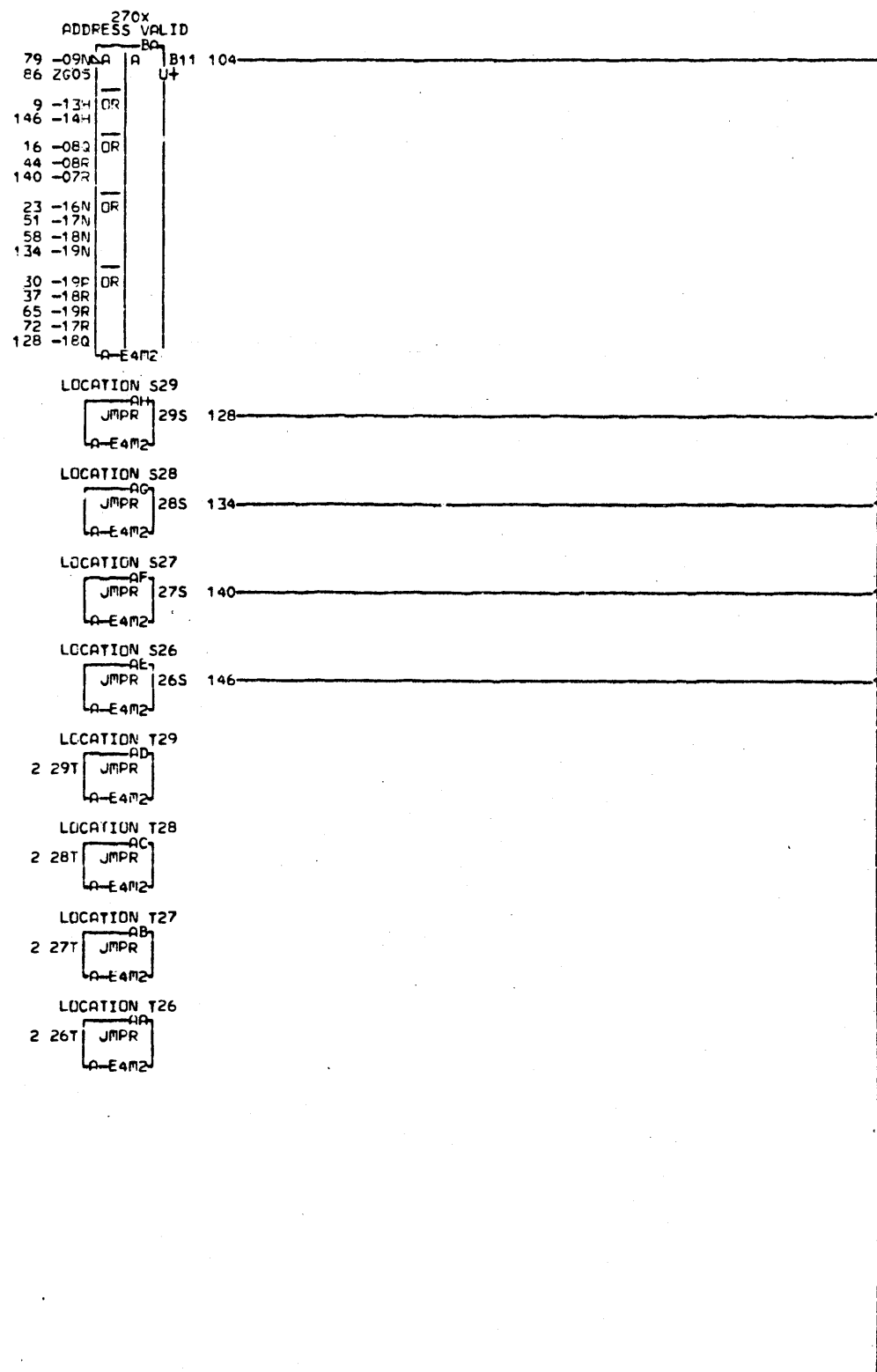
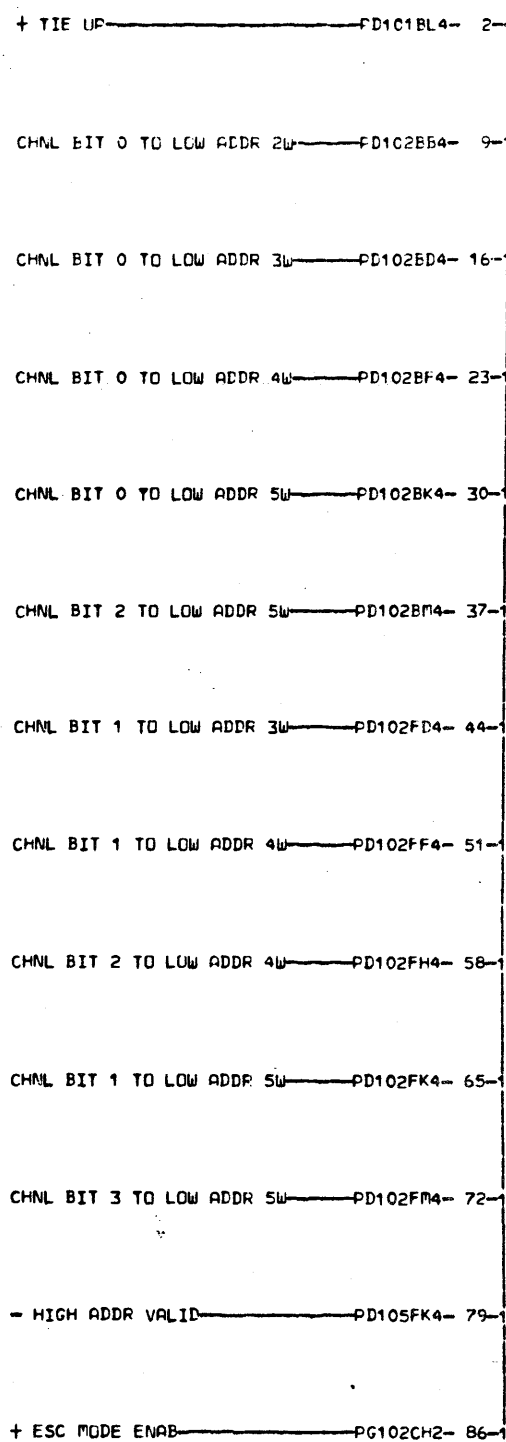
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NOTE 1 SEE PAGE PA051 NOTES FOR LOW AND HIGH ESC ADDRESS JUMPERING

LOC. TYPE A-E4M2 2325

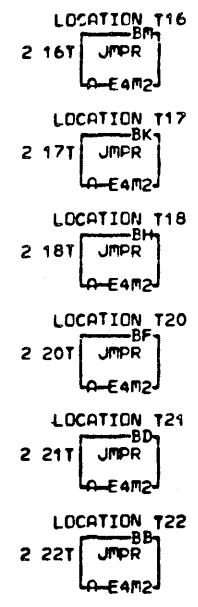
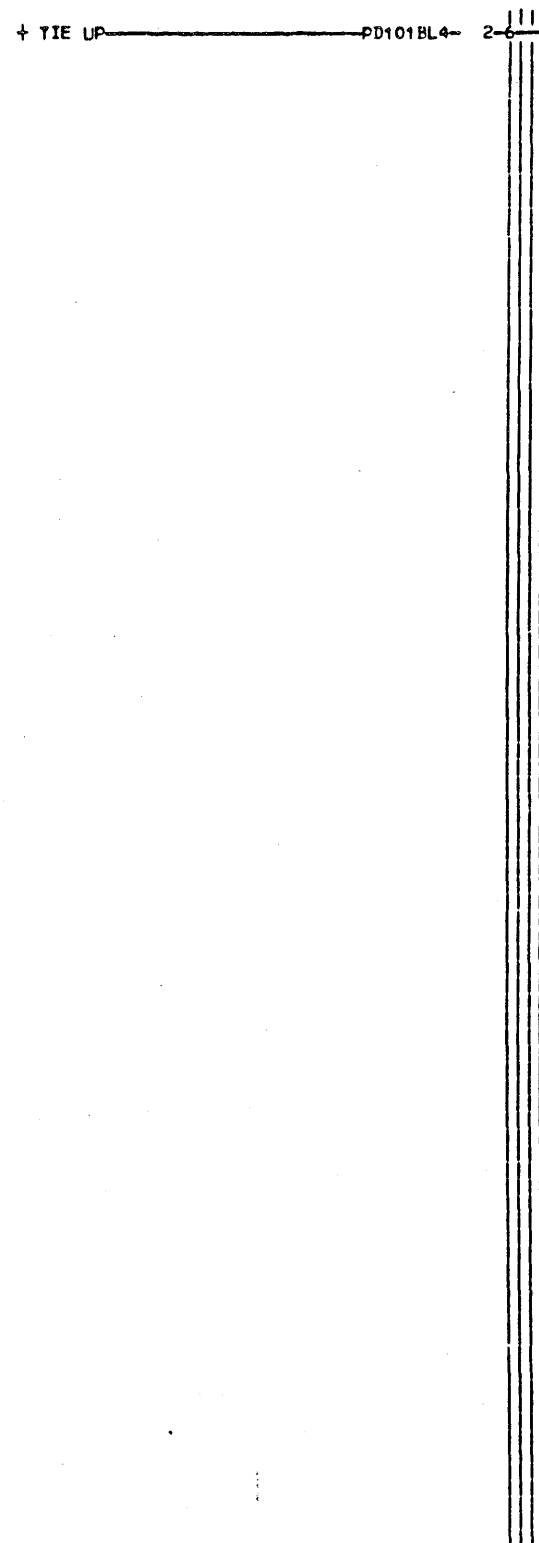
LOW ADDRESS JUMPERS			
-E.C.-HISTORY-		C-MACH.27FNB	
		FRAME	01
		IBM CORP.SDD	PD102
DATE	LAST EC	P.No.	1755031 000
02-23-76	314402		



LDC. TYPE  
A-E4M2 2325

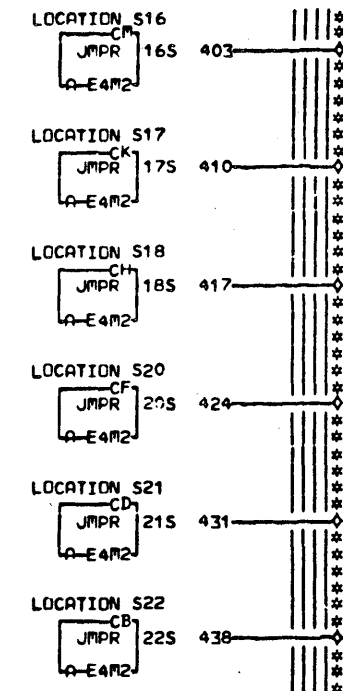
NOTE 1 SEE PAGE PA051 NOTES  
FOR LOW AND HIGH ESC ADDRESS  
JUMPERING

LOW ADDRESS LOGIC	
-E.C.-HISTORY-	C1 PACH.27RNB
	FRAME 01
DATE LAST EC	IBM CORP.SDB PD103
02-23-76 314402	P.N. 1755032 000



-BLANK COLUMN-

-BLANK COLUMN-



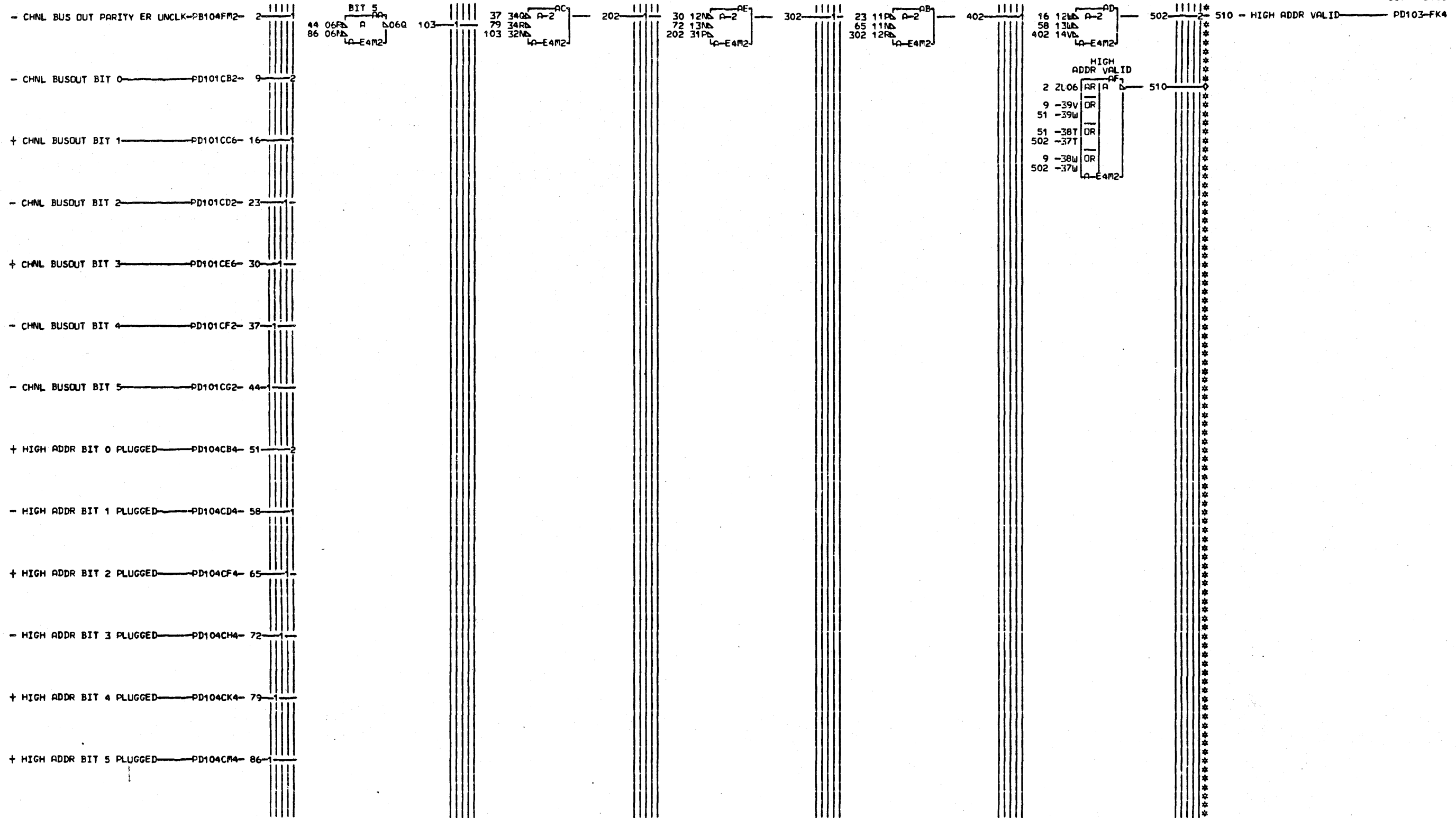
- 438 + HIGH ADDR BIT 0 PLUGGED PD105-CB4
- 431 - HIGH ADDR BIT 1 PLUGGED PD105-CD4
- 424 + HIGH ADDR BIT 2 PLUGGED PD105-CF4
- 417 - HIGH ADDR BIT 3 PLUGGED PD105-CH4
- 410 + HIGH ADDR BIT 4 PLUGGED PD105-CK4
- 403 + HIGH ADDR BIT 5 PLUGGED PD105-CM4

NOTE 1. SEE PAGE PA051 NOTES  
FOR LOW AND HIGH ESC ADDRESS  
JUMPERING

PD104  
000

LOC. TYPE  
A-E4M2 2325

HIGH ADDRESS JUMPERS			
-E.C.-HISTORY-		C MACH#27RNB	
DATE	LAST EC	FRAME	01
02-23-76	314402	IBM CORP.SDD	PD104
P#N# 1755033			000

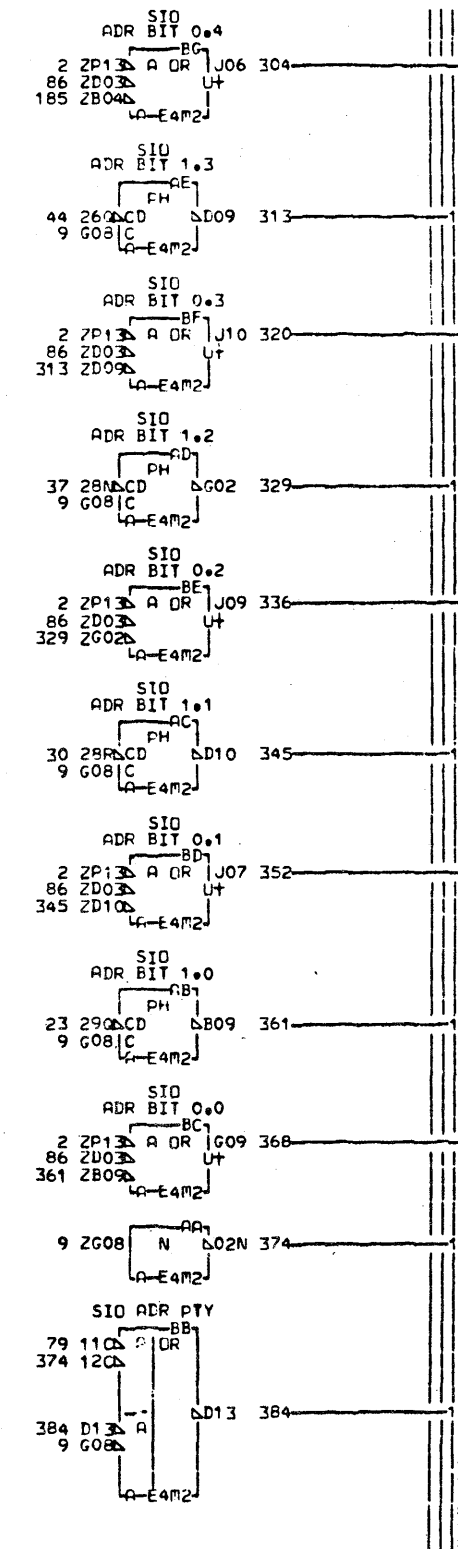
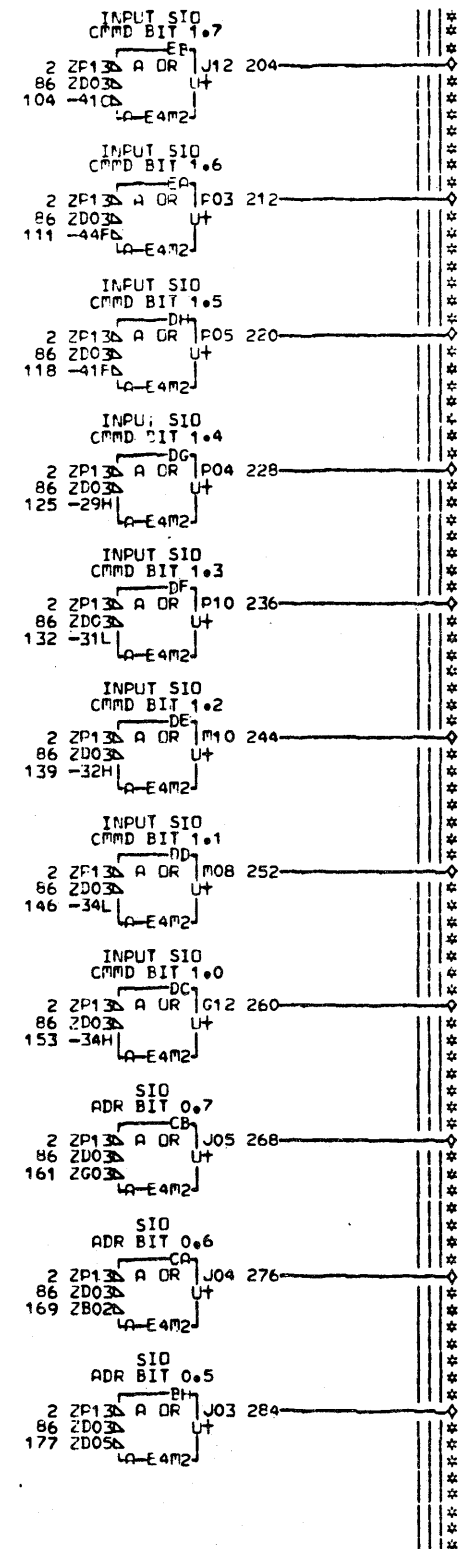
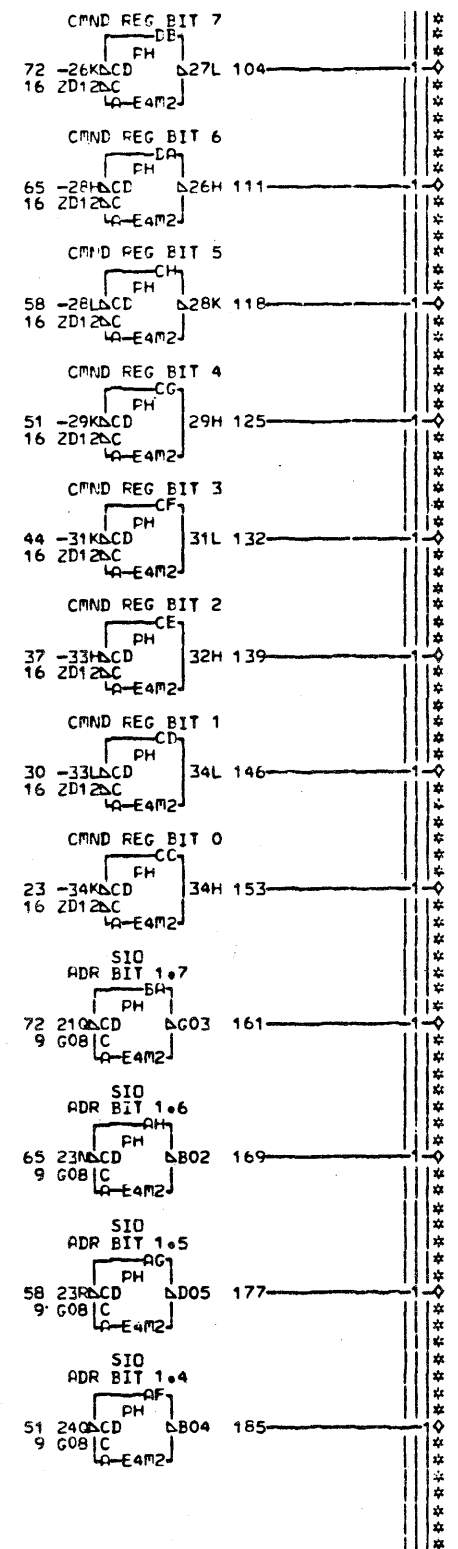


LDC. TYPE  
A-E4M2 2325

PD105  
000

HIGH ADDRESS LOGIC	
E.C.-HISTORY	C-MACH.27RNB
DATE	LAST EC
02-23-76	314402
FRAME	01
IBP CORP.SDD	PD105
P.No. 1755034	000

- 61 DECODE-----PA101GL4- 2-B5
- + GATE SID ADDR REG-----PC102HC2- 9-4-6
- DECODE CMD-----PC104FG6- 16-8
- CHNL BUSOUT BIT 0-----PD101CB2- 23-1
- CHNL BUSOUT BIT 1-----PD101CC2- 30-1
- CHNL BUSOUT BIT 2-----PD101CD2- 37-1
- CHNL BUSOUT BIT 3-----PD101CE2- 44-1
- CHNL BUSOUT BIT 4-----PD101CF2- 51-2
- CHNL BUSOUT BIT 5-----PD101CG2- 58-2
- CHNL BUSOUT BIT 6-----PD101CH2- 65-2
- CHNL BUSOUT BIT 7-----PD101CJ2- 72-2
- CHNL OUT PTY 0-7-----PD109BL2- 79-1
- GATE INPUT DATA-----PM104EG2- 86-E5

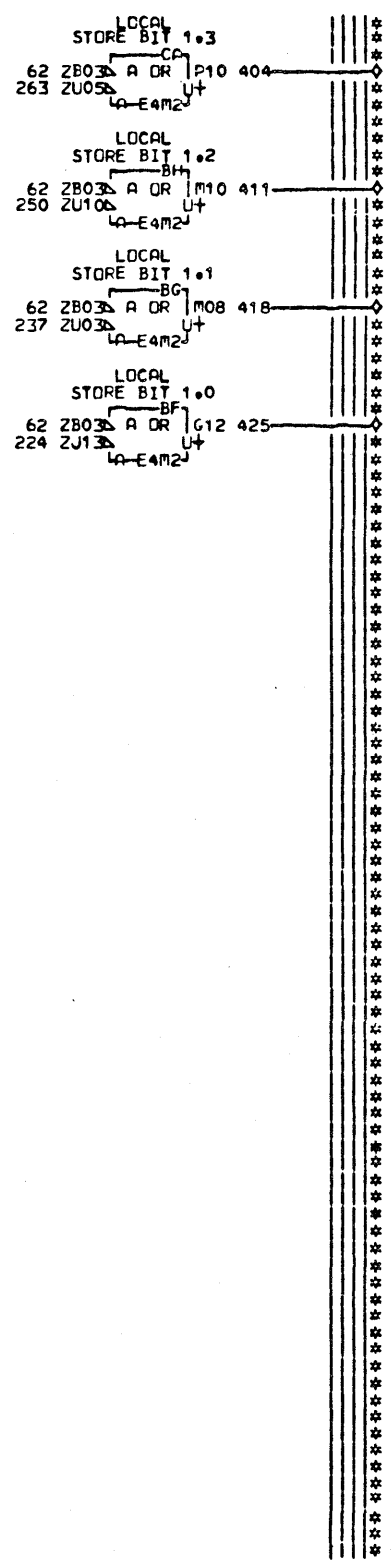
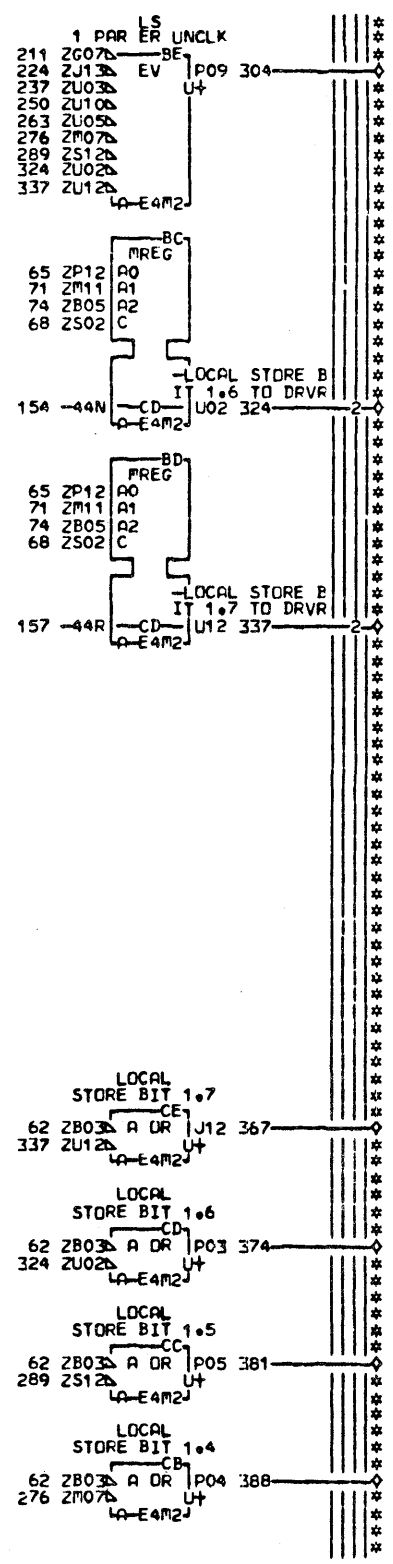
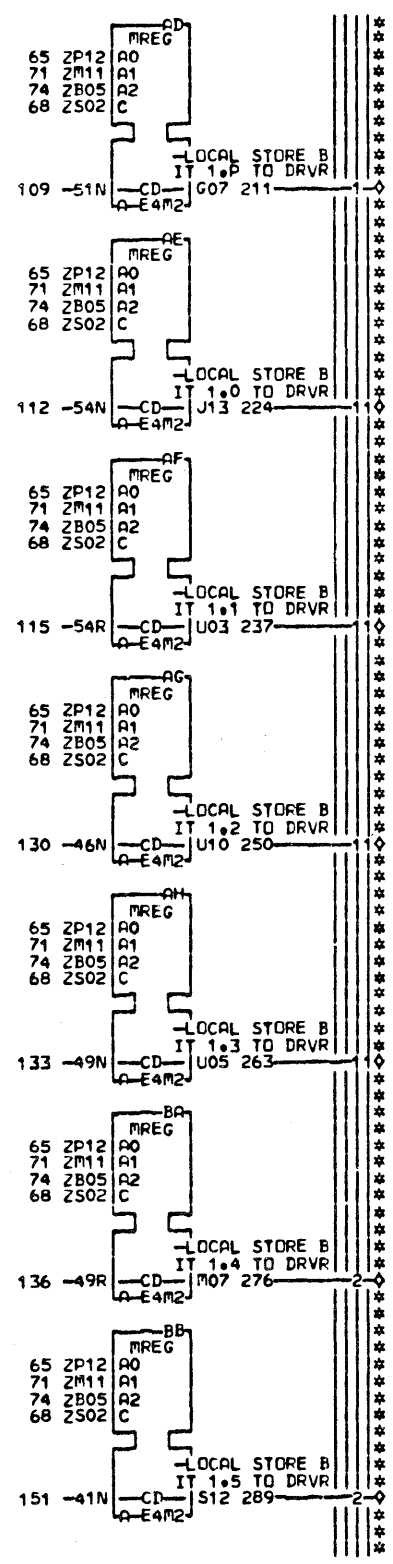
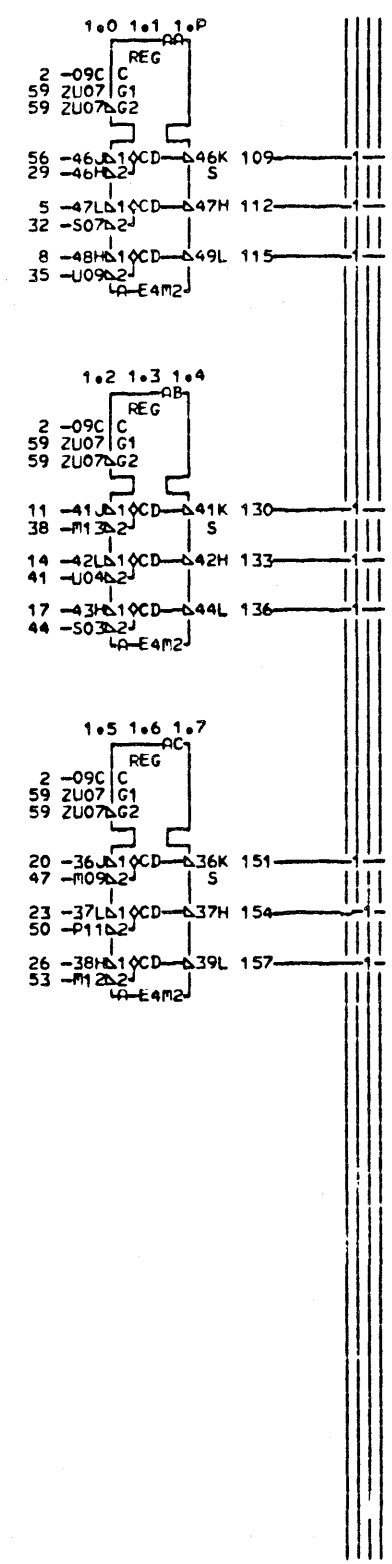
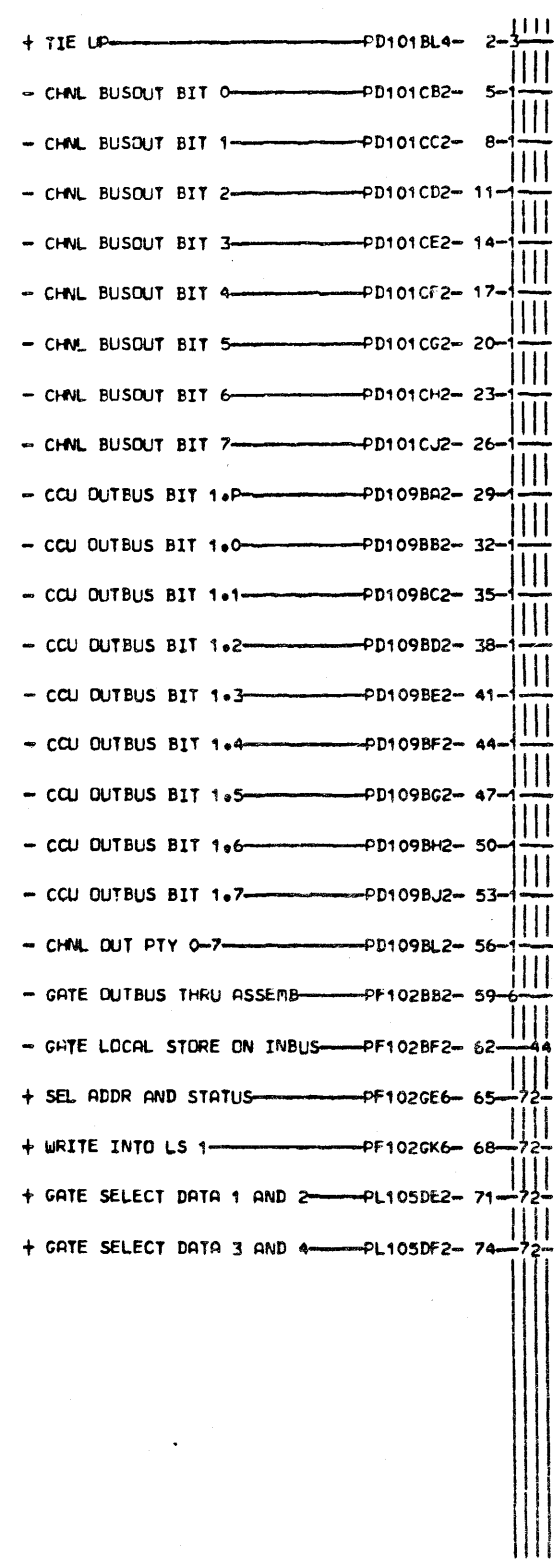


000 PD106

- 384 - SID ADR PTY 1.0-1.7-----BB6
- 361 - SID ADR BIT 1.0-----BC6
- 345 - SID ADR BIT 1.1-----BD6
- 329 - SID ADR BIT 1.2-----EF6
- 313 - SID ADR BIT 1.3-----BG6
- 185 - SID ADR BIT 1.4-----BH6
- 177 - SID ADR BIT 1.5-----BK6
- 169 - SID ADR BIT 1.6-----BL6
- 161 - SID ADR BIT 1.7-----BM6
- 368 + SID ADR BIT 0.0-----FA104-CC2
- 352 + SID ADR BIT 0.1-----FA104-CD2
- 336 + SID ADR BIT 0.2-----FA104-CF2
- 320 + SID ADR BIT 0.3-----FA104-CG2
- 304 + SID ADR BIT 0.4-----FA104-CI2
- 284 + SID ADR BIT 0.5-----FA104-CK2
- 276 + SID ADR BIT 0.6-----FA104-CL2
- 268 + SID ADR BIT 0.7-----FA104-CM2
- 153 + CMD REG BIT 0-----PD108-FD2
- 146 + CMD REG BIT 1-----PD108-FE2
- 139 + CMD REG BIT 2-----PD108-FG2
- 132 + CMD REG BIT 3-----PD108-FH2
- 125 + CMD REG BIT 4-----FD108-FJ2
- 118 - CMD REG BIT 5-----PD108-FL6
- 111 - CMD REG BIT 6-----PD108-FM6
- 104 - CMD REG BIT 7-----PD108-FN6
- 260 + INPUT SID CMD BIT 1.0- FA105-GD2
- 252 + INPUT SID CMD BIT 1.1- FA105-GE2
- 244 + INPUT SID CMD BIT 1.2- PA105-GG2
- 236 + INPUT SID CMD BIT 1.3- PA105-GH2
- 228 + INPUT SID CMD BIT 1.4- PA105-GJ2
- 220 + INPUT SID CMD BIT 1.5- PA105-GL2
- 212 + INPUT SID CMD BIT 1.6- PA105-GM2
- 204 + INPUT SID CMD BIT 1.7- PA105-GN2

LUC. TYPE  
A-E4M2 2325

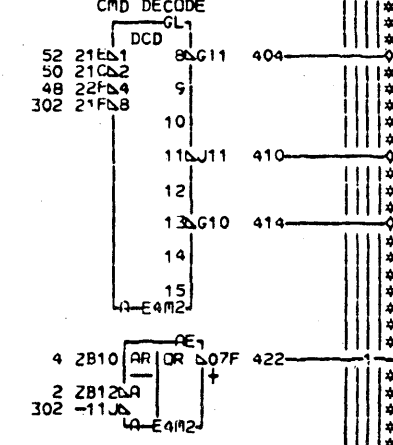
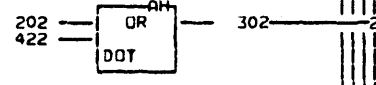
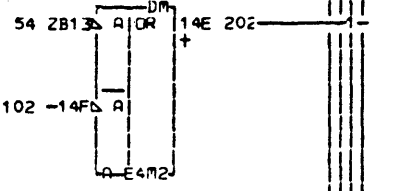
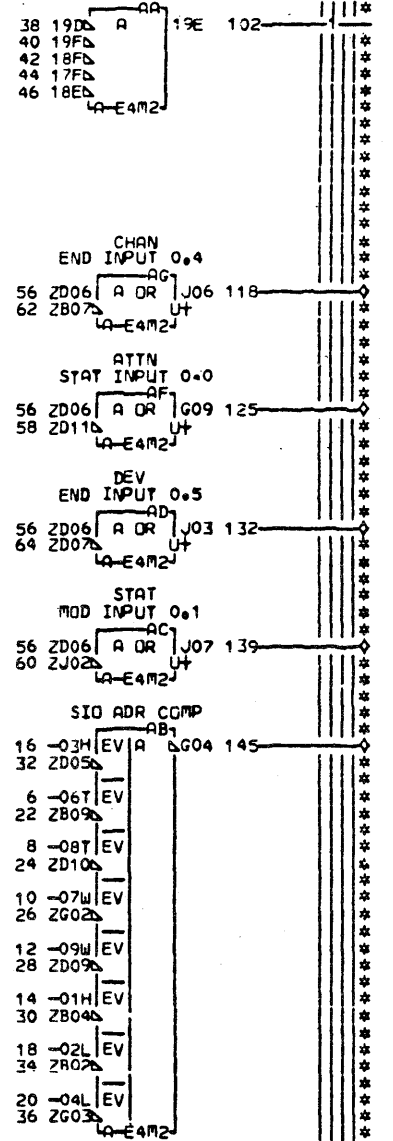
START I/O ADR + CMD REGS			
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		FRANE 01	
DATE	LAST EC	IBM CORP. SDD	PD106
02-23-76	314402	P.No. 1755035	000



LDC TYPE A-E4M2 2325

NON EB LOCAL STORE			
E.C.—HISTORY	C MACH#27RNB		
DATE	LAST EC	FRAME	01
02-23-76	314402	IBM CORP.SDD	PD107
P.#	1755036	000	

+ RESET STATUS IN AND SERV IN—PC101GC2— 2  
 + START ID LT—PC102AN2— 4  
 + CHNL BUSOUT BIT 0—PD101CB6— 6  
 + CHNL BUSOUT BIT 1—PD101CC6— 8  
 + CHNL BUSOUT BIT 2—PD101CD6— 10  
 + CHNL BUSOUT BIT 3—PD101CE6— 12  
 + CHNL BUSOUT BIT 4—PD101CF6— 14  
 + CHNL BUSOUT BIT 5—PD101CG6— 16  
 + CHNL BUSOUT BIT 6—PD101CH6— 18  
 + CHNL BUSOUT BIT 7—PD101CJ6— 20  
 - SIO ADR BIT 1.0—PD106BC6— 22  
 - SIO ADR BIT 1.1—PD106BD6— 24  
 - SIO ADR BIT 1.2—PD106BF6— 26  
 - SIO ADR BIT 1.3—PD106BG6— 28  
 - SIO ADR BIT 1.4—PD106BH6— 30  
 - SIO ADR BIT 1.5—PD106BK6— 32  
 - SIO ADR BIT 1.6—PD106BL6— 34  
 - SIO ADR BIT 1.7—PD106BM6— 36  
 + CMD REG BIT 0—PD106FD2— 38  
 + CMD REG BIT 1—PD106FE2— 40  
 + CMD REG BIT 2—PD106FG2— 42  
 + CMD REG BIT 3—PD106FH2— 44  
 + CMD REG BIT 4—PD106FJ2— 46  
 - CMD REG BIT 5—PD106FL6— 48  
 - CMD REG BIT 6—PD106FM6— 50  
 - CMD REG BIT 7—PD106FN6— 52  
 - SET UNIT CHECK—PE102EK6— 54  
 + INPUT 66—PF101CJ2— 56  
 - ATTN STATUS TO DRIVER—PG101FC6— 58  
 - STAT MOD STATUS TO DRIVER—PG101FD6— 60  
 - CHAN END STATUS TO DRIVER—PG101FG6— 62  
 - DEVICE END STATUS TO DRIVER—PG101FH6— 64



000 PD108  
 145 - SIO ADR COMP—PE106-CD4  
 139 + STAT MOD INPUT 0.1—PA104-DB2  
 132 + DEV END INPUT 0.5—PA104-DF2  
 125 + ATTN STAT INPUT 0.0—PA104-EA2  
 118 + CHAN END INPUT 0.4—PA104-EE2  
 404 - TEST TO DECODE—GL3  
 PC102 WPE102 WPE104 WPE106  
 410 - NO OP DECODE—GL6  
 WPE102 WPE106  
 414 - IPL DECODE—PE105-GL8

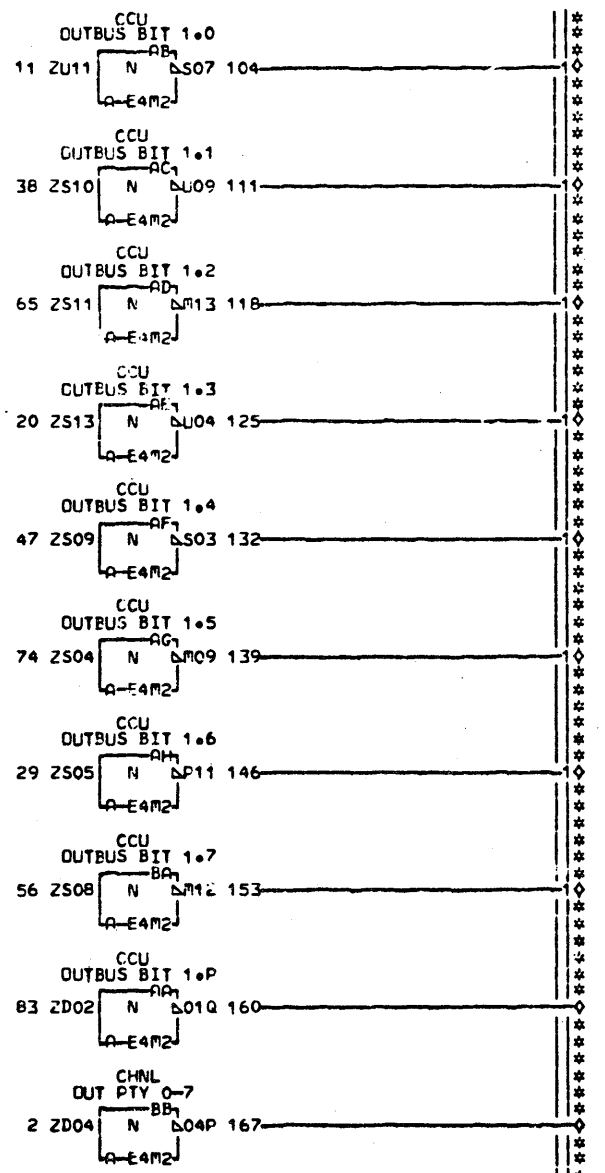
LOC. TYPE  
 A-E4M2 2325

PD108  
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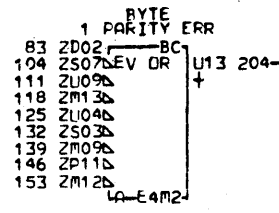
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E.C. HISTORY 314402	D. MACH. 27RNB
DATE 07-14-76	LAST EC 315620
FRAME 01	IBM CORP. SDD P.N. 1755037
PD108 000	



+ CHNL BUS OUT BIT P — PB103GB4- 2-11  
 + OUTBUS REG 1.0 — PM101AA6- 11-11  
 + OUTBUS REG 1.3 — PM101AC6- 20-11  
 + OUTBUS REG 1.6 — PM101AE6- 29-11  
 + OUTBUS REG 1.1 — PM101BA6- 38-11  
 + OUTBUS REG 1.4 — PM101BC6- 47-11  
 + OUTBUS REG 1.7 — PM101BE6- 56-11  
 + OUTBUS REG 1.2 — PM101CA6- 65-11  
 + OUTBUS REG 1.5 — PM101CC6- 74-11  
 + OUTBUS REG 1.P — PM101CE6- 83-11



LOC. TYPE  
A-E4M2 2325

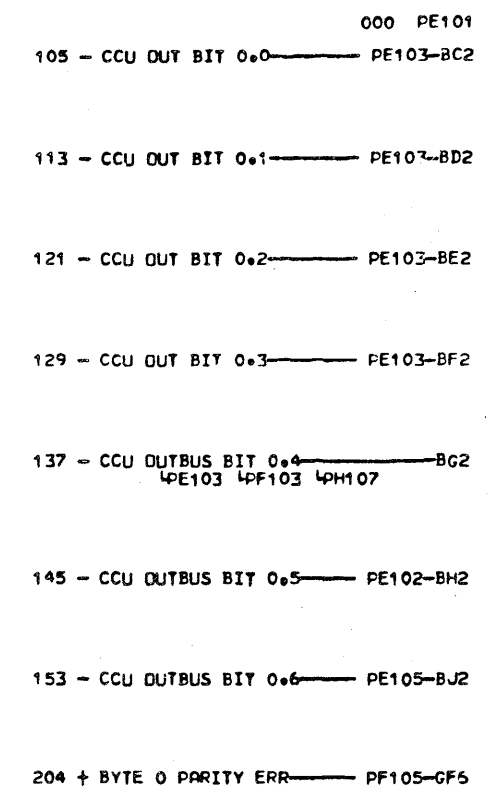
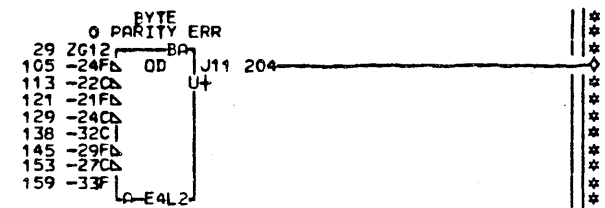
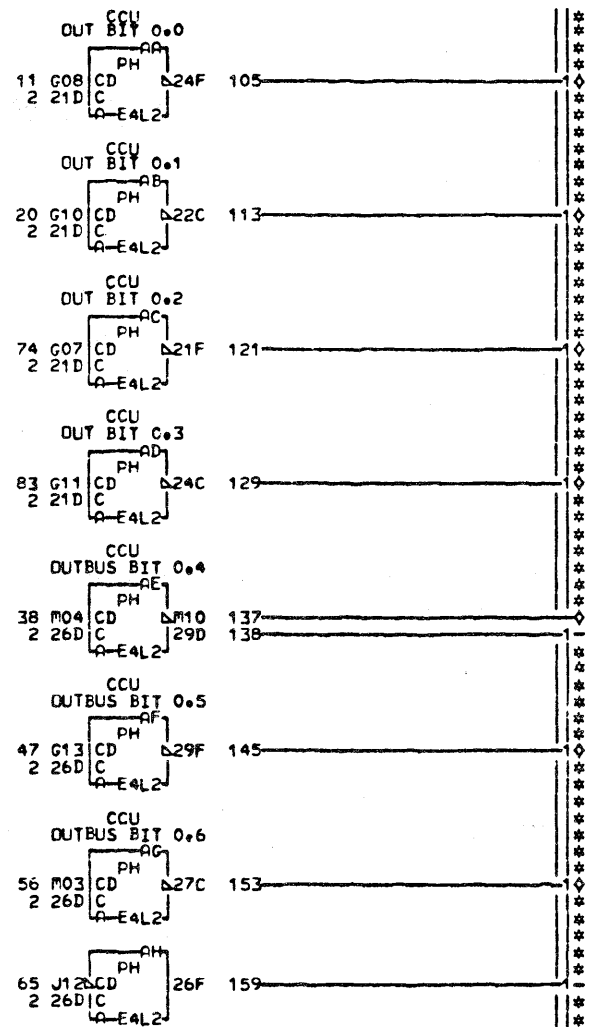
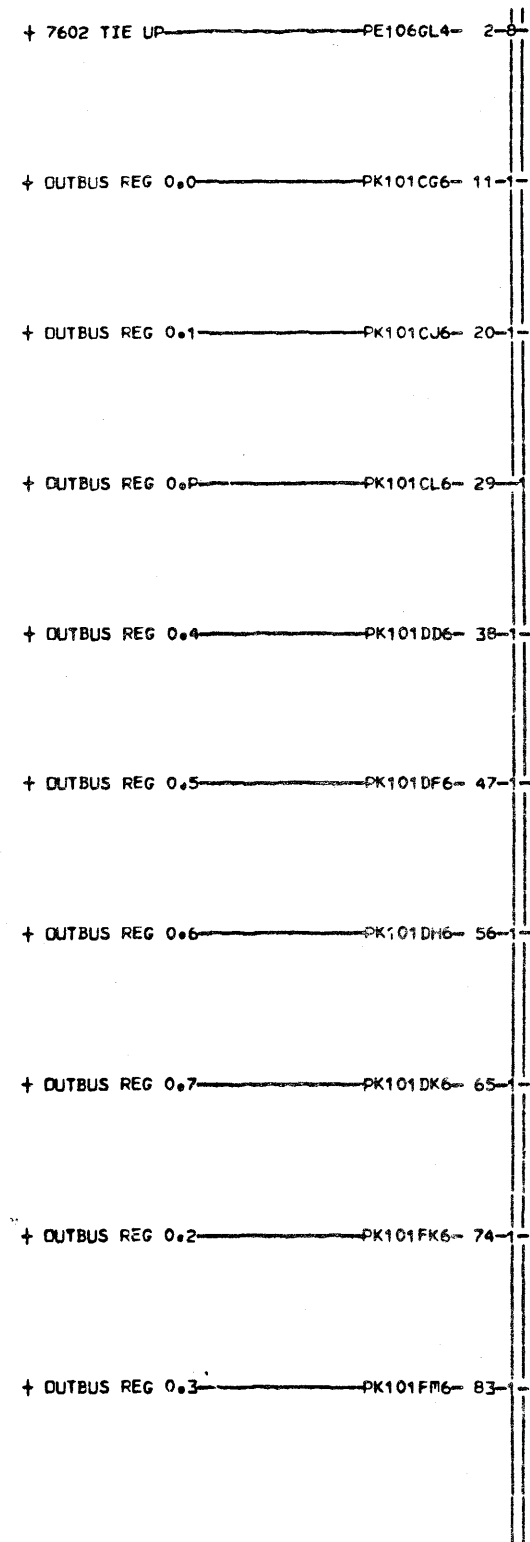


000 PD109

160 - CCU OUTBUS BIT 1.P — PD:07-BA2  
 104 - CCU OUTBUS BIT 1.0 — BB2  
 111 - CCU OUTBUS BIT 1.1 — BC2  
 118 - CCU OUTBUS BIT 1.2 — BD2  
 125 - CCU OUTBUS BIT 1.3 — BE2  
 132 - CCU OUTBUS BIT 1.4 — BF2  
 139 - CCU OUTBUS BIT 1.5 — BG2  
 146 - CCU OUTBUS BIT 1.6 — BH2  
 153 - CCU OUTBUS BIT 1.7 — BJ2  
 167 - CHNL OUT PTY 0-7 — BL2  
 204 + BYTE 1 PARITY ERR — PF105-FE2

PD109  
000

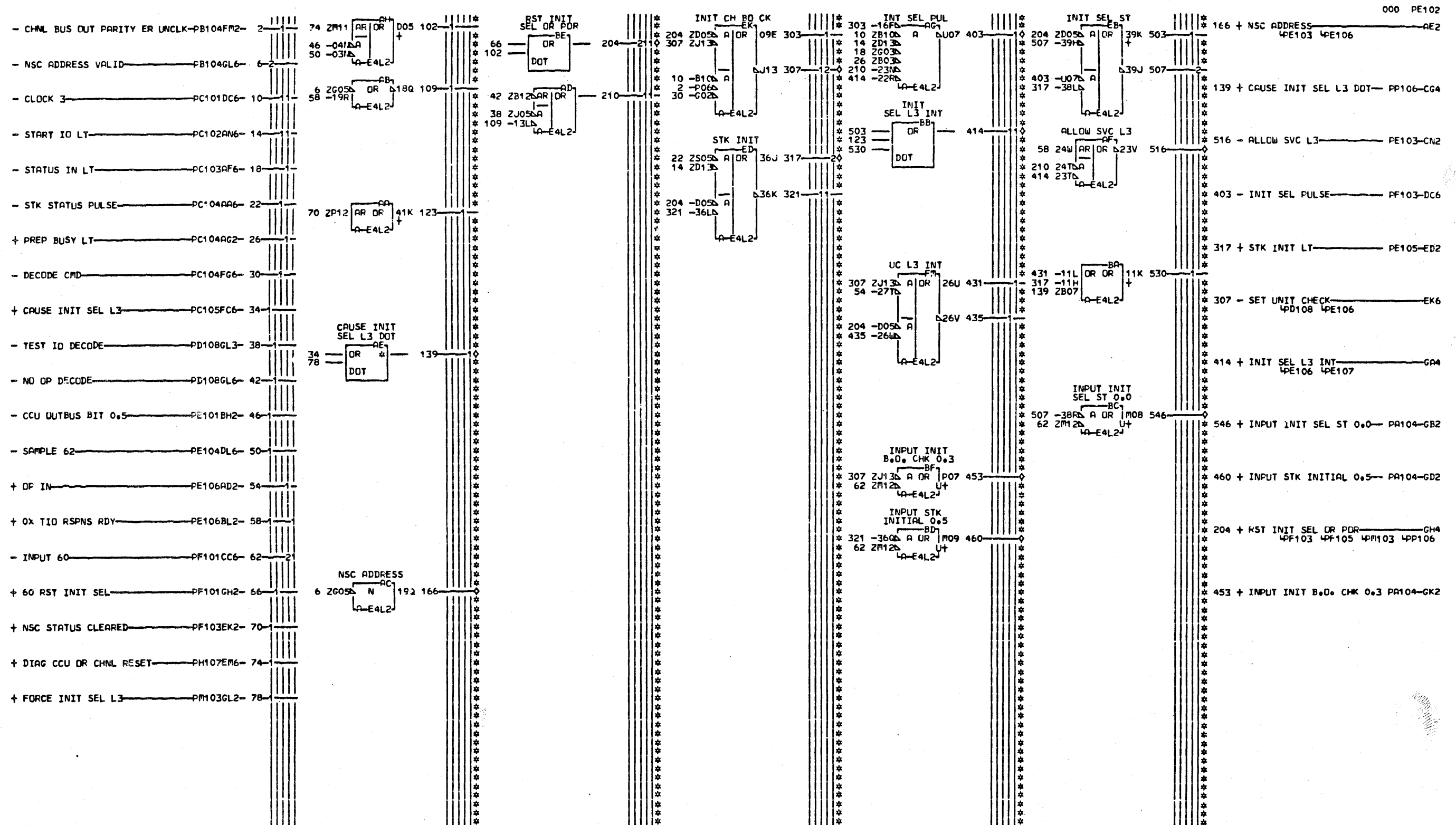
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E.C.—HISTORY—	C1 MACH.27RNB
DATE	LAST EC
02-23-76	314402
FRAME	01
IBM CORP.SDD	PD109
P.N.	1755038
	000



LDC TYPE  
A-E4L2 7602

PE101  
000

CCU OUTBUS TERMINATION			
E-C-HISTORY	C-MACH	27RNB	
	FRAME	01	
DATE	LAST EC	IBM CORP. SDD	PE101
02-23-76	314402	P.No. 1755039	000

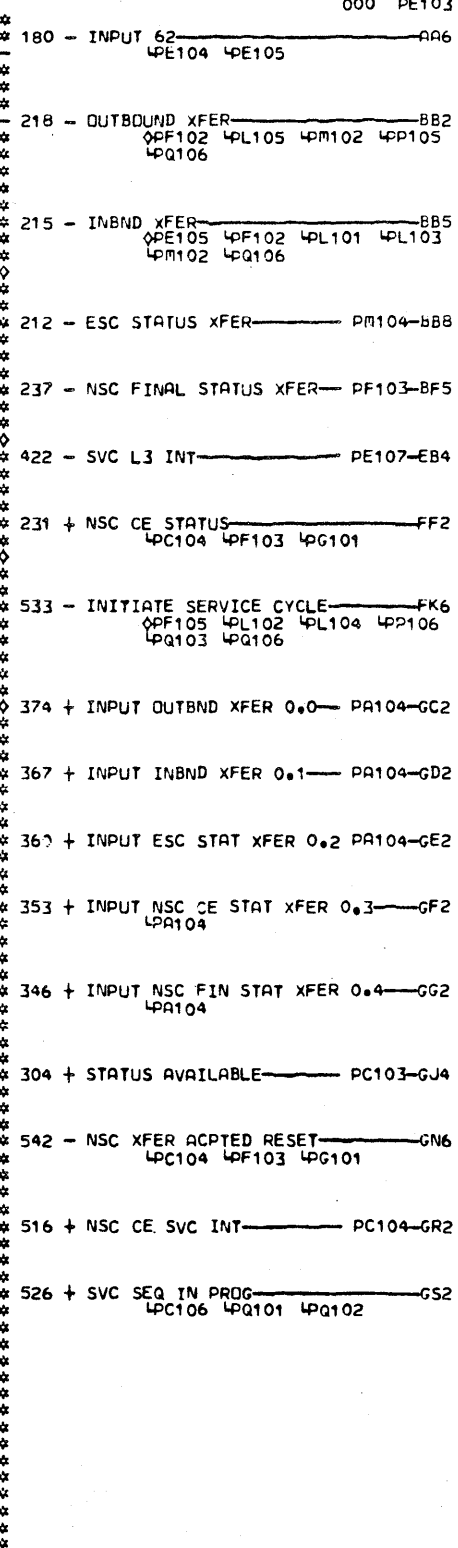
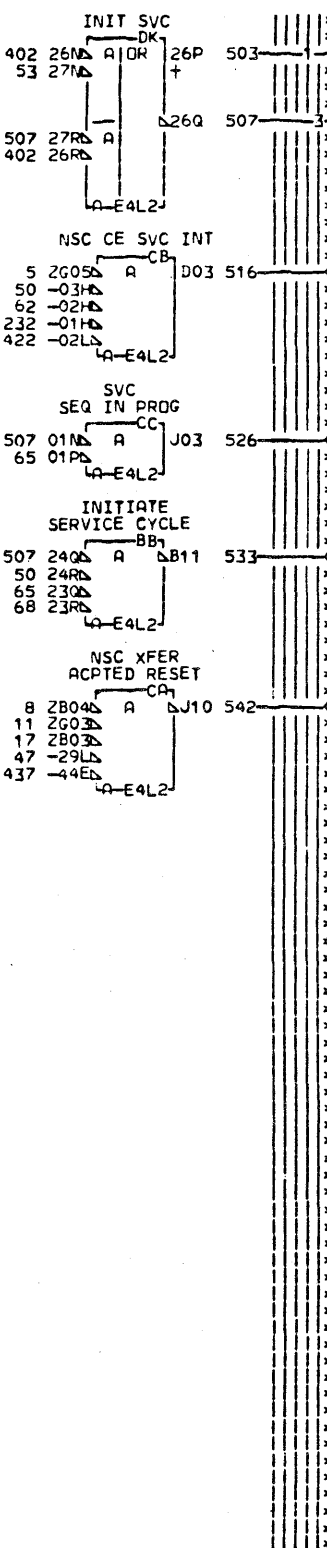
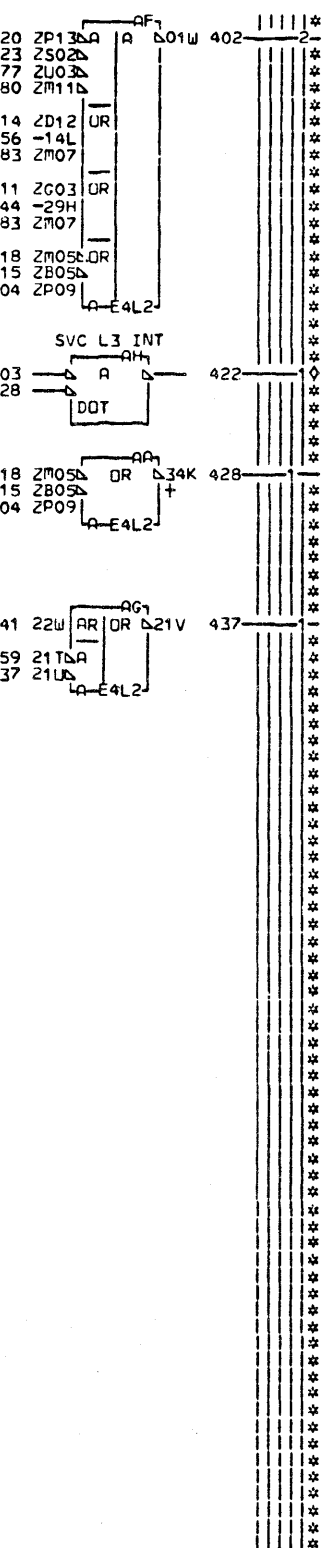
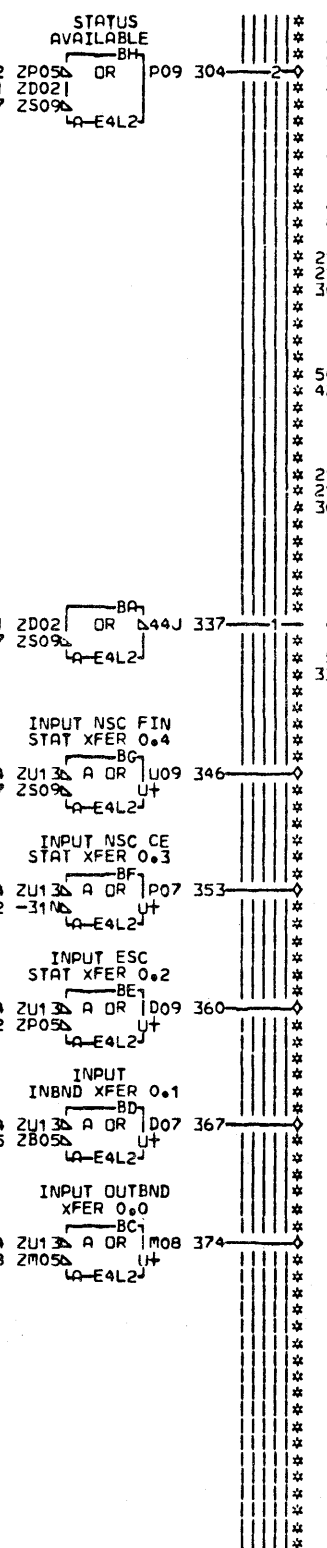
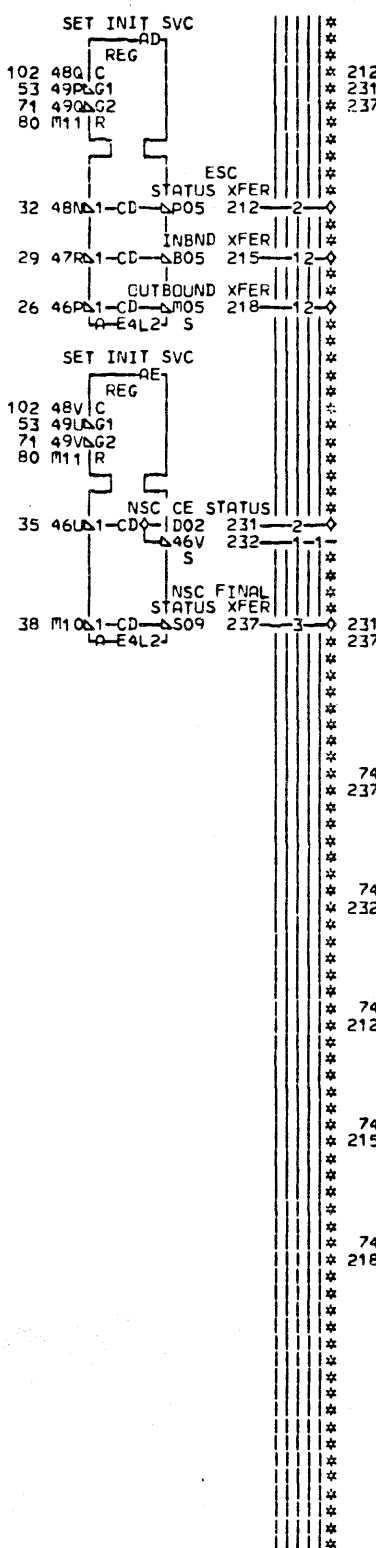
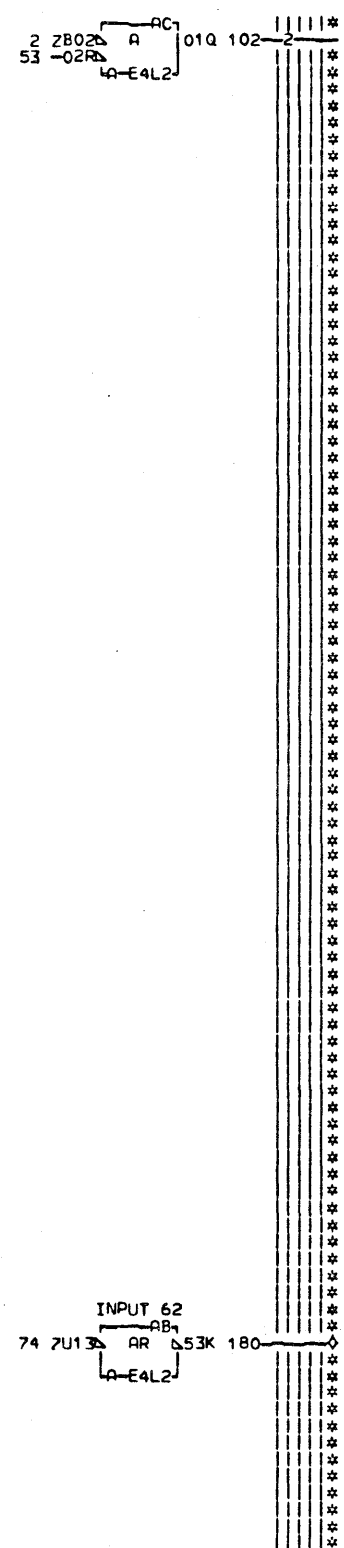
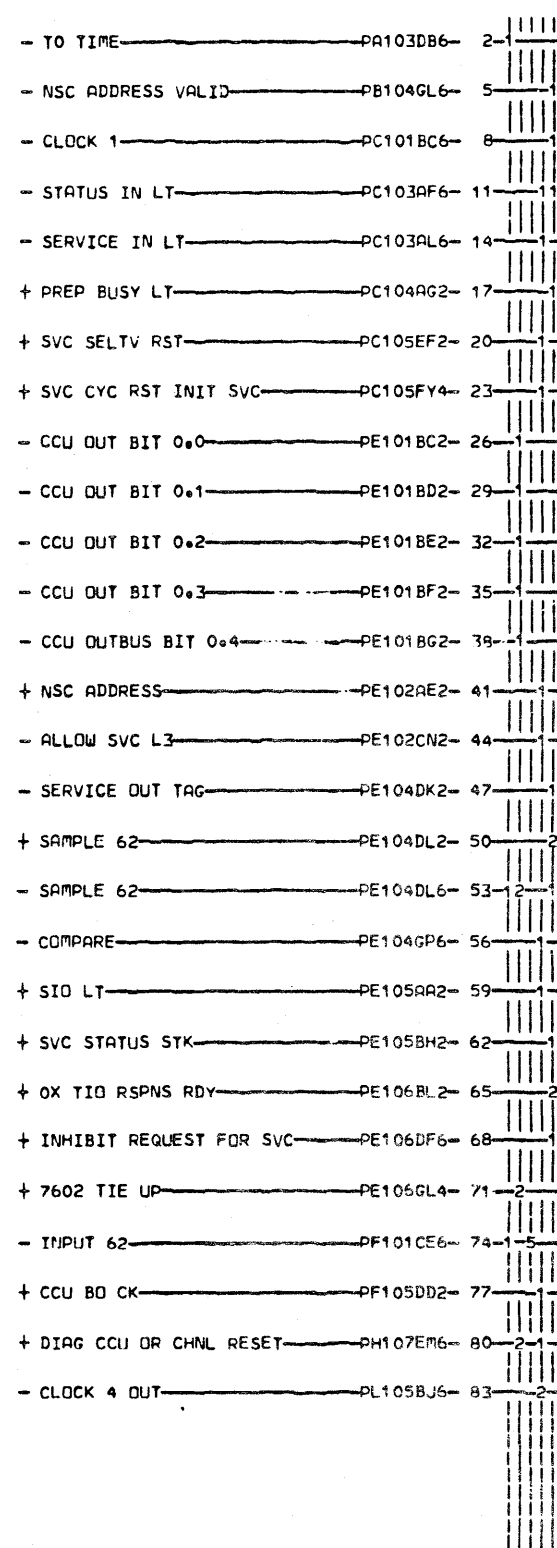


000 PE102

LOC. TYPE  
A-E4L2 7602

INITIAL SELECTION CONTROL	
E.C. HISTORY	C. MACH. 27RNB
FRAME	01
DATE LAST EC	IBM CORP. SDD PE102
02-23-76 314402	P. No. 1755040 000

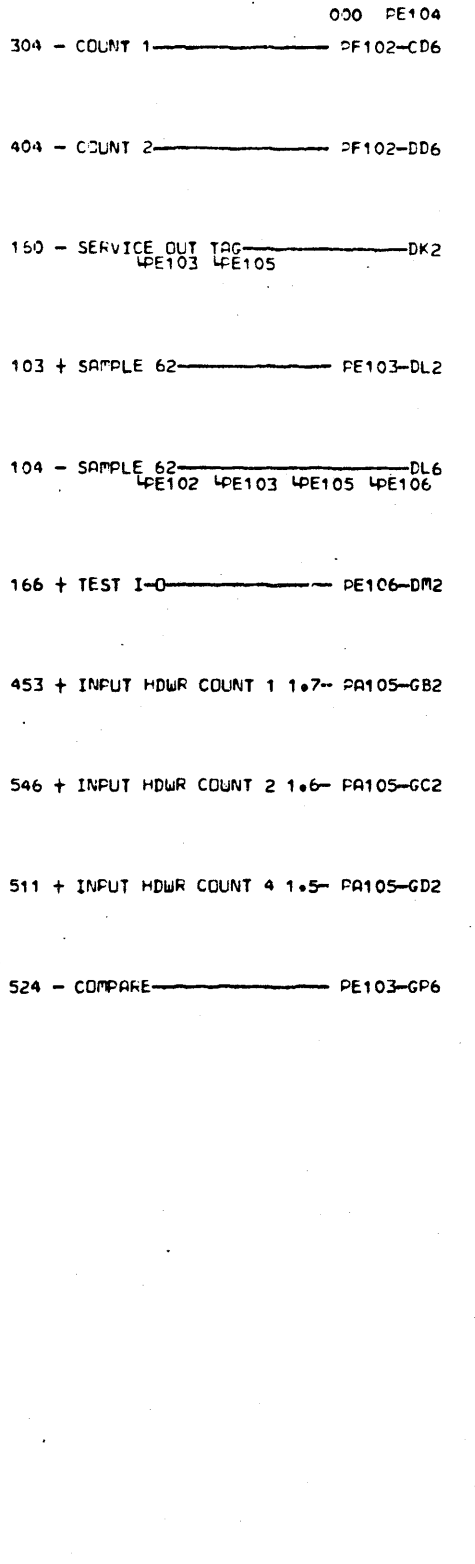
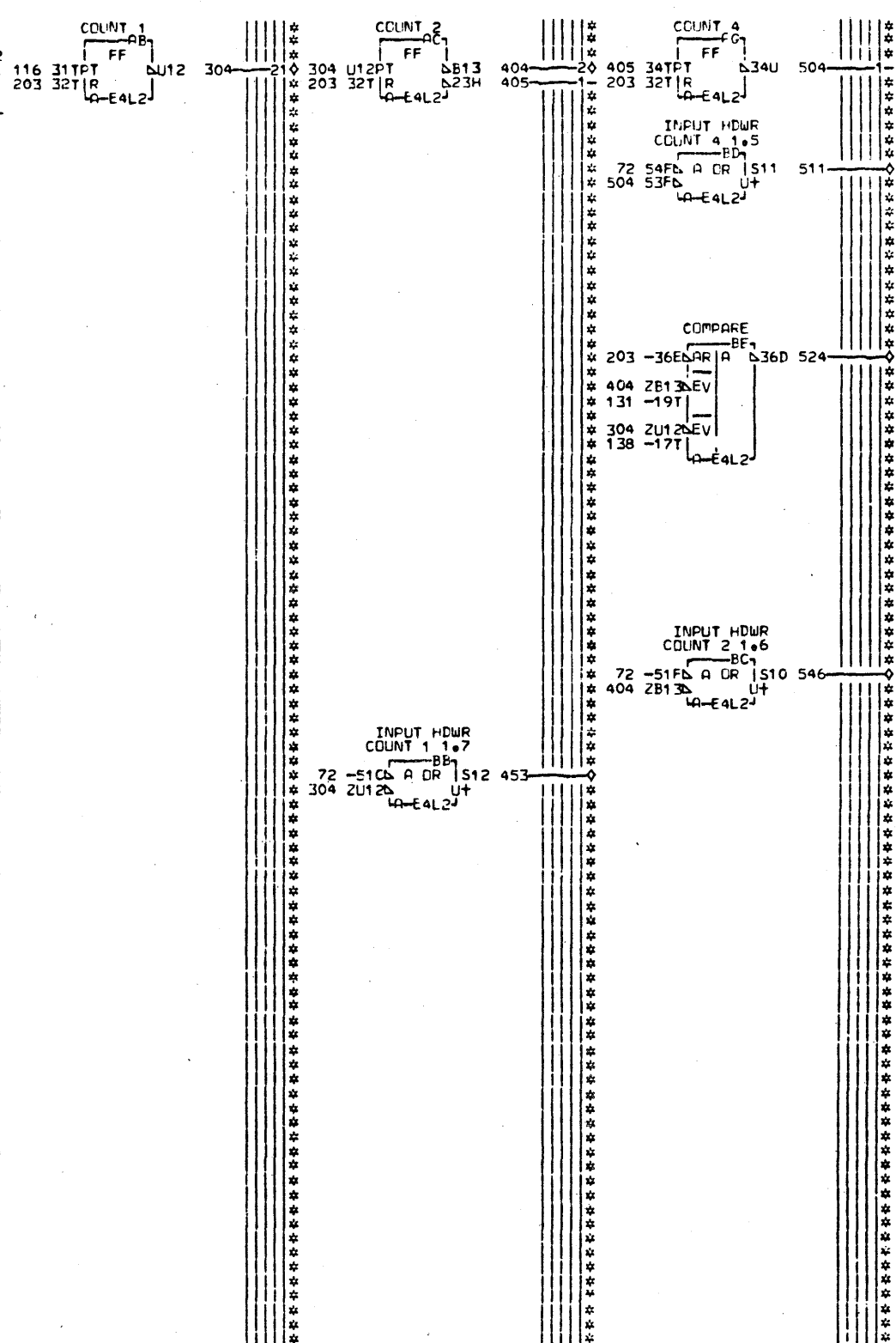
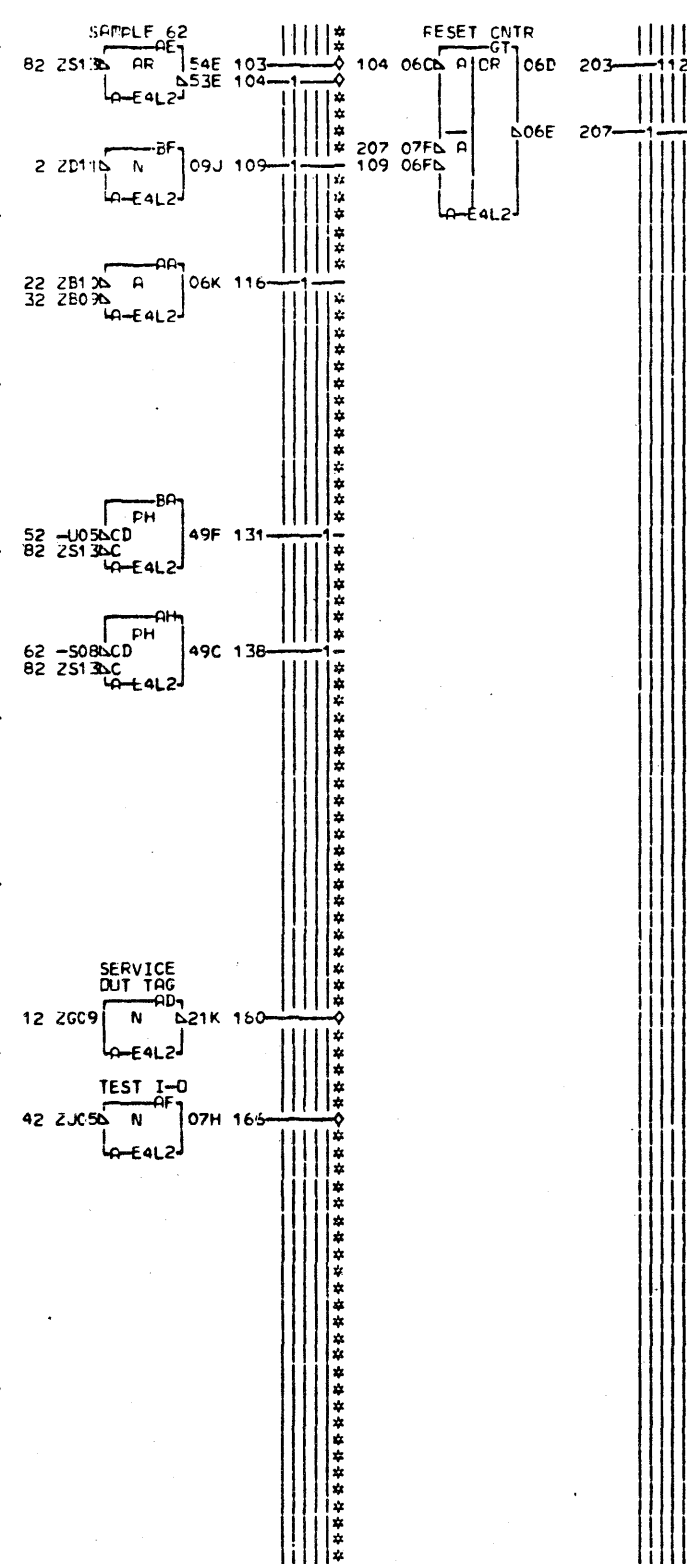
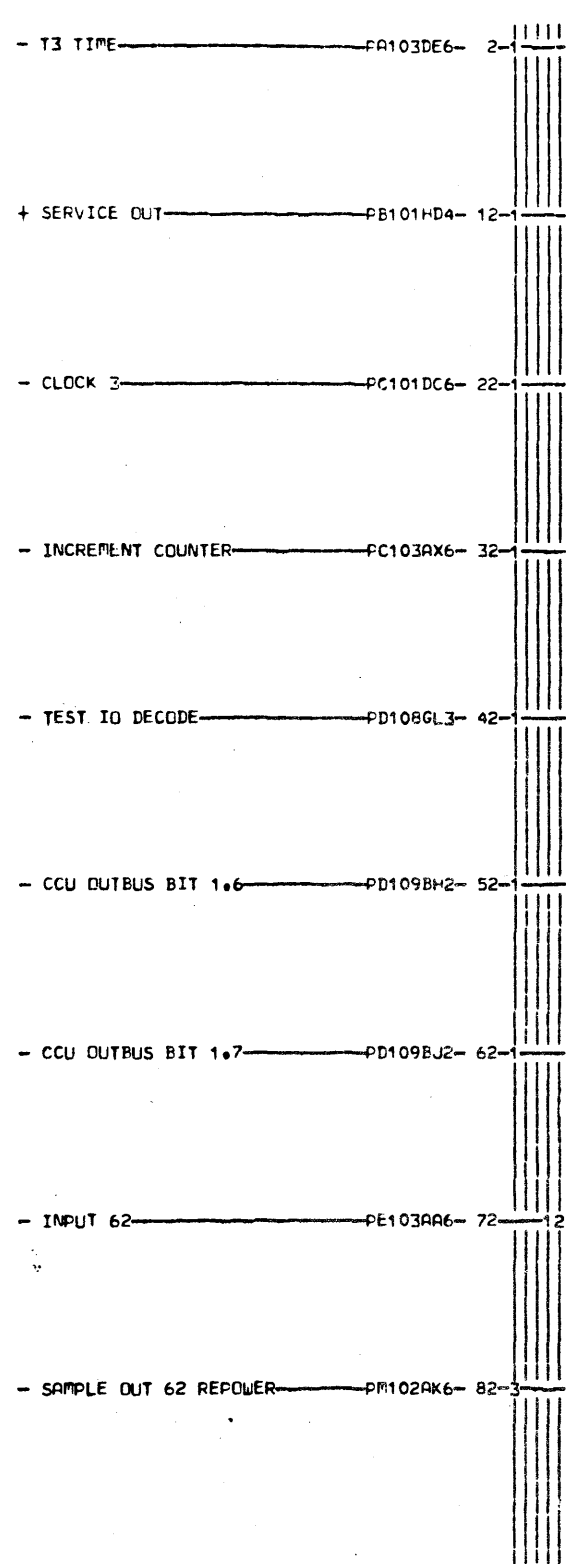
PE102  
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LOC. TYPE  
A-E4L2 7602

SERVICE TRANSFER CONTROL			
E.C. HISTORY	C. MACH. 27RNB	FRAME	01
314402		IBM CORP. SDD	PE103
DATE	LAST EC	P.No.	1755041 000
05-17-76	314424		

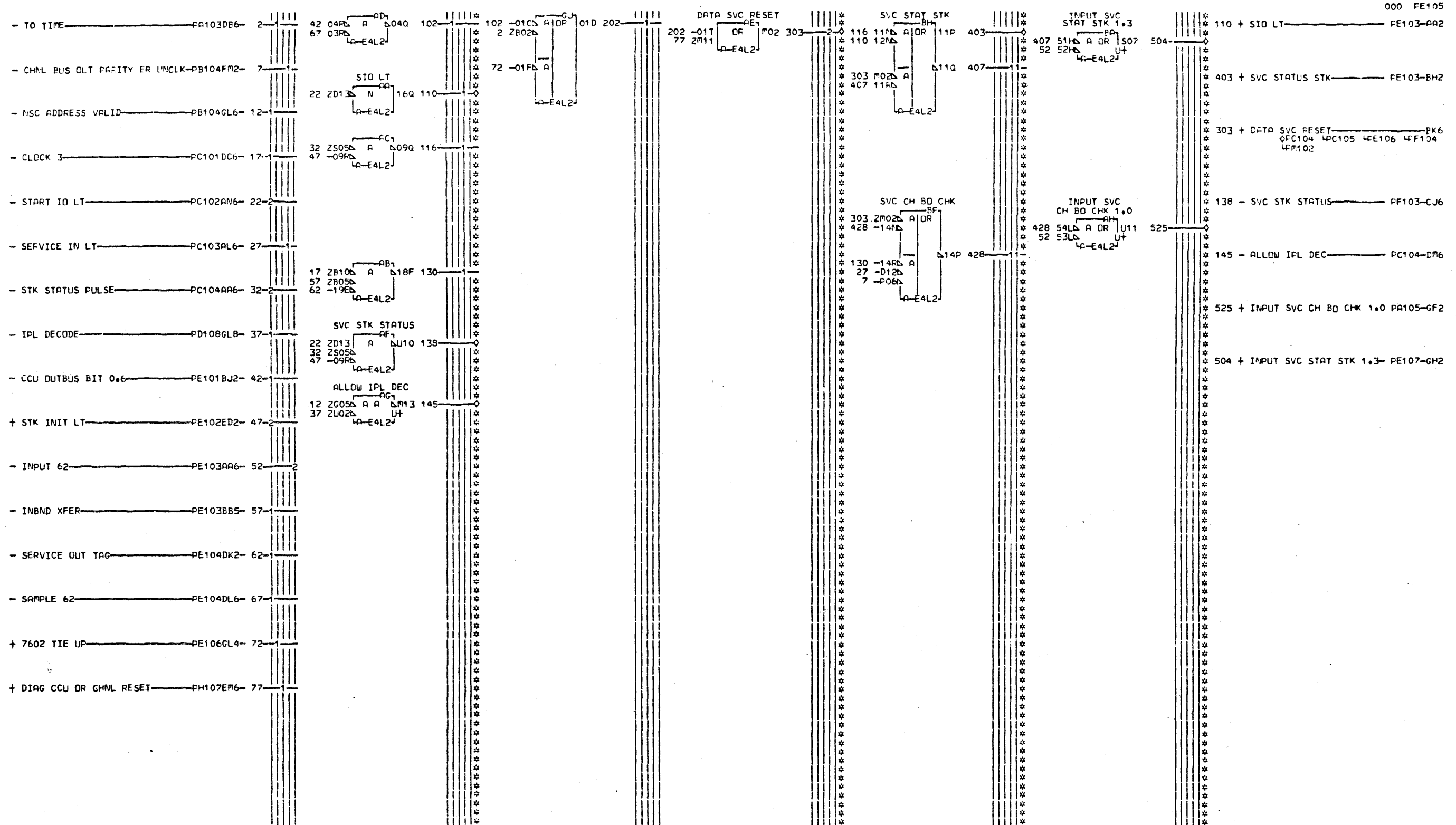
PE103  
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LDC. TYPE  
A-E4L2 7602

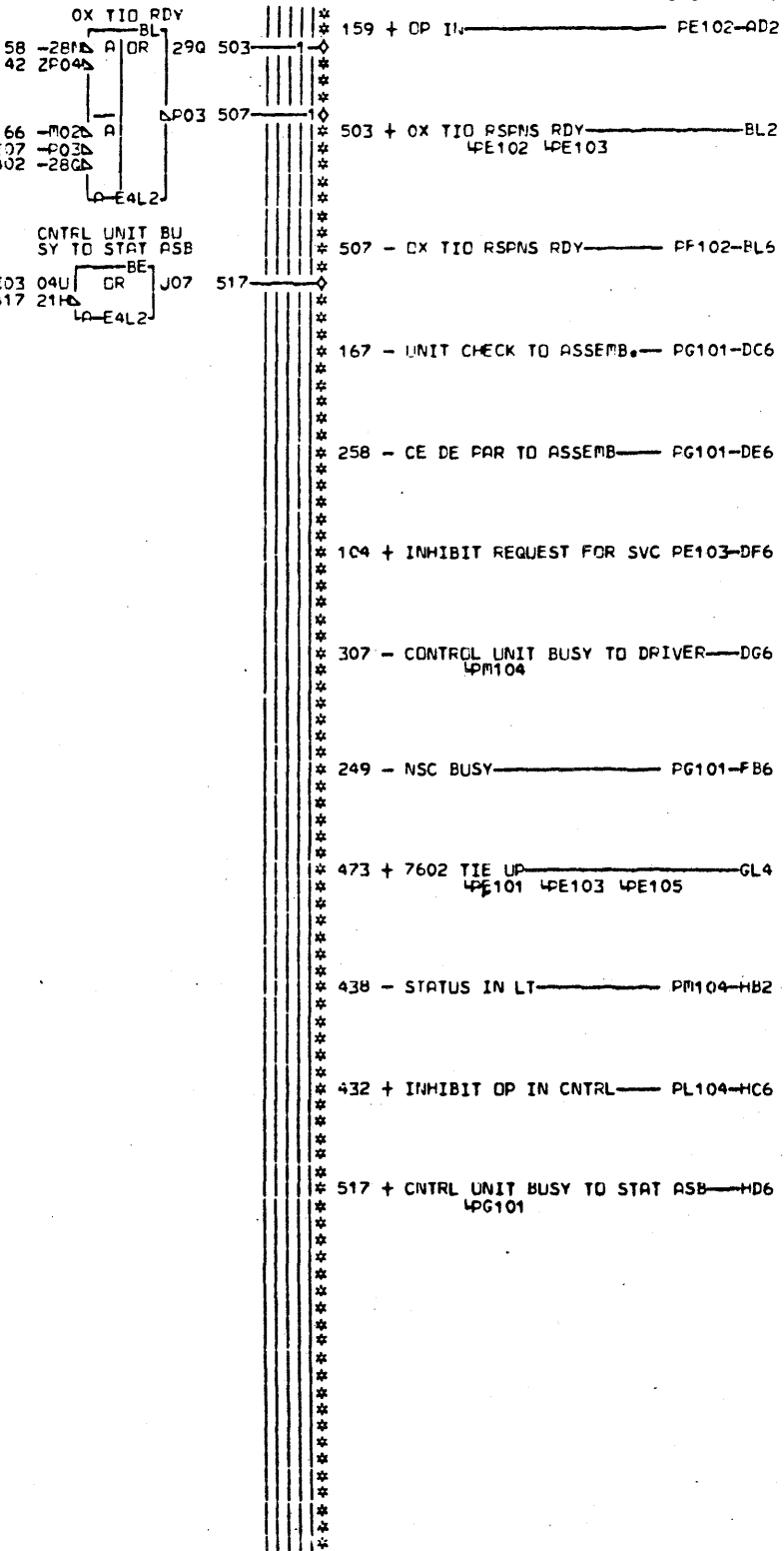
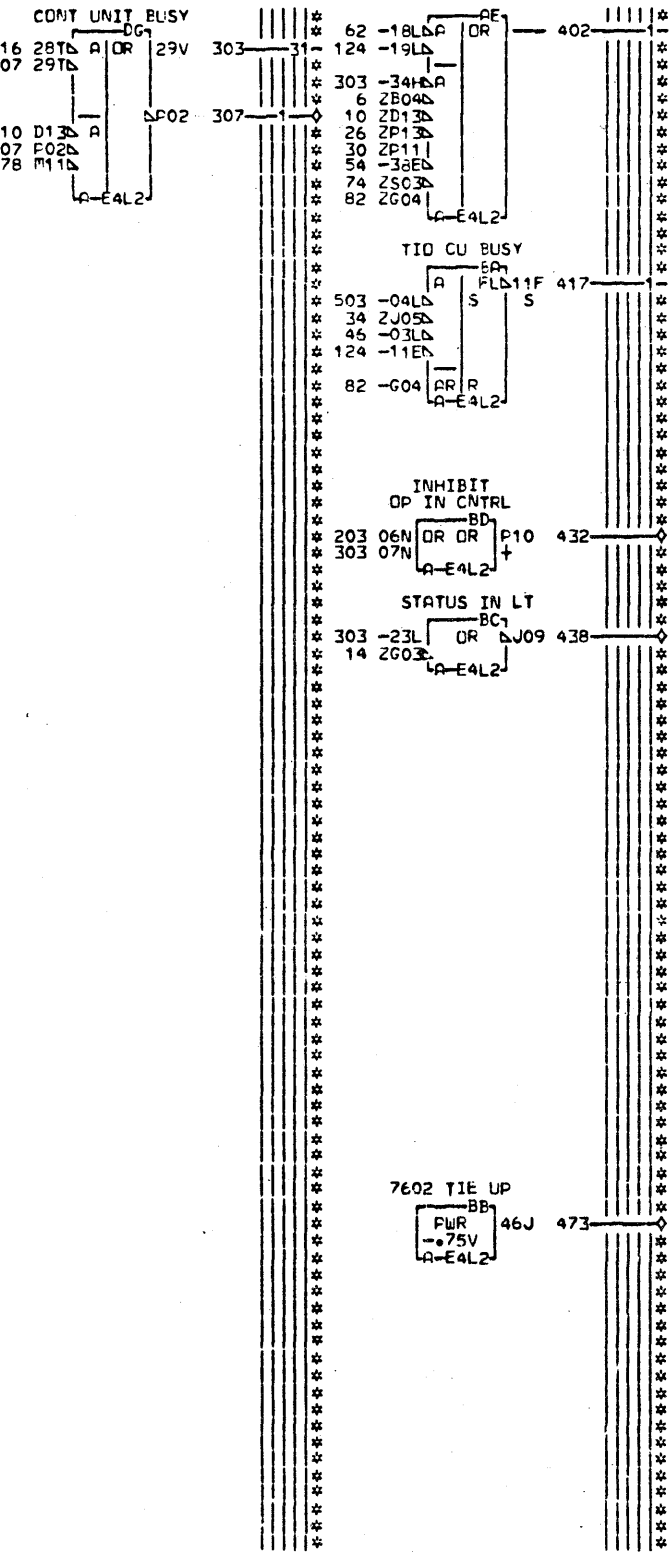
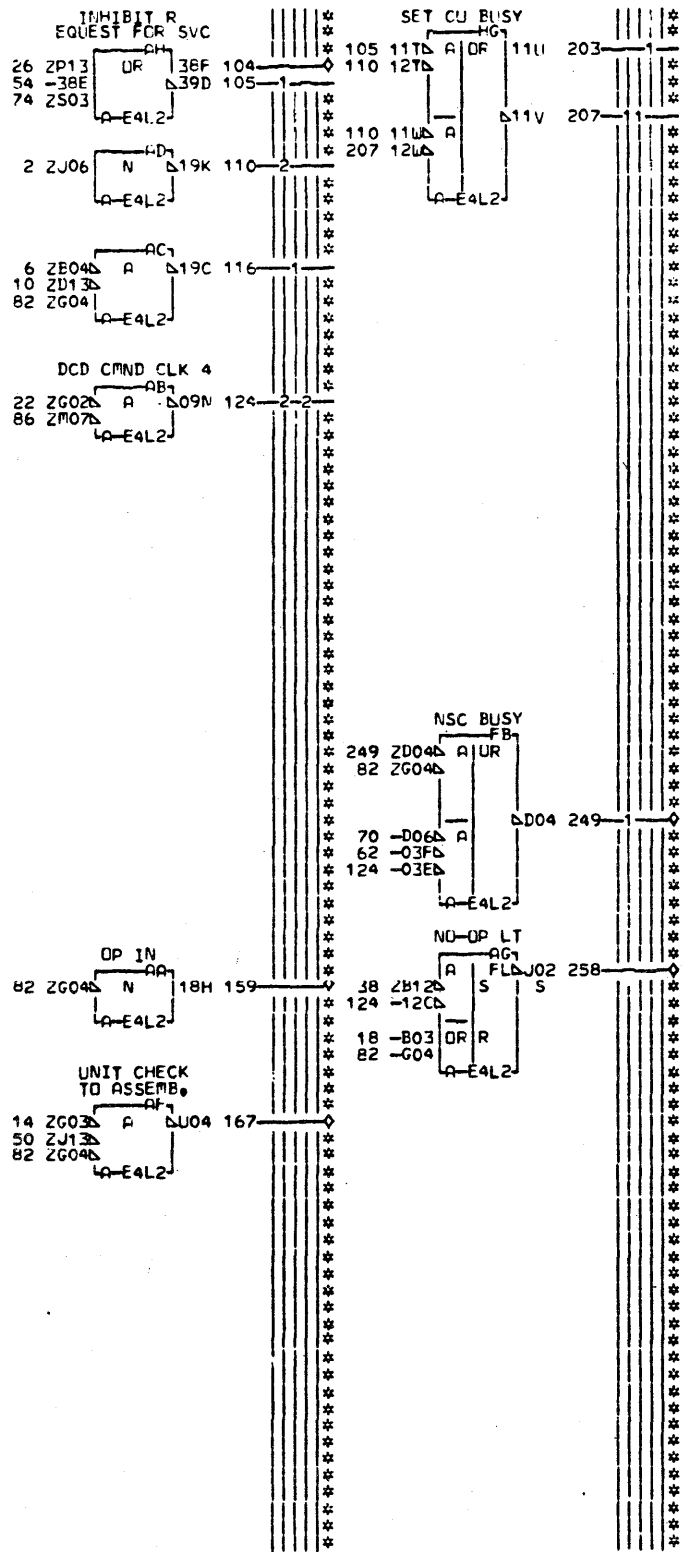
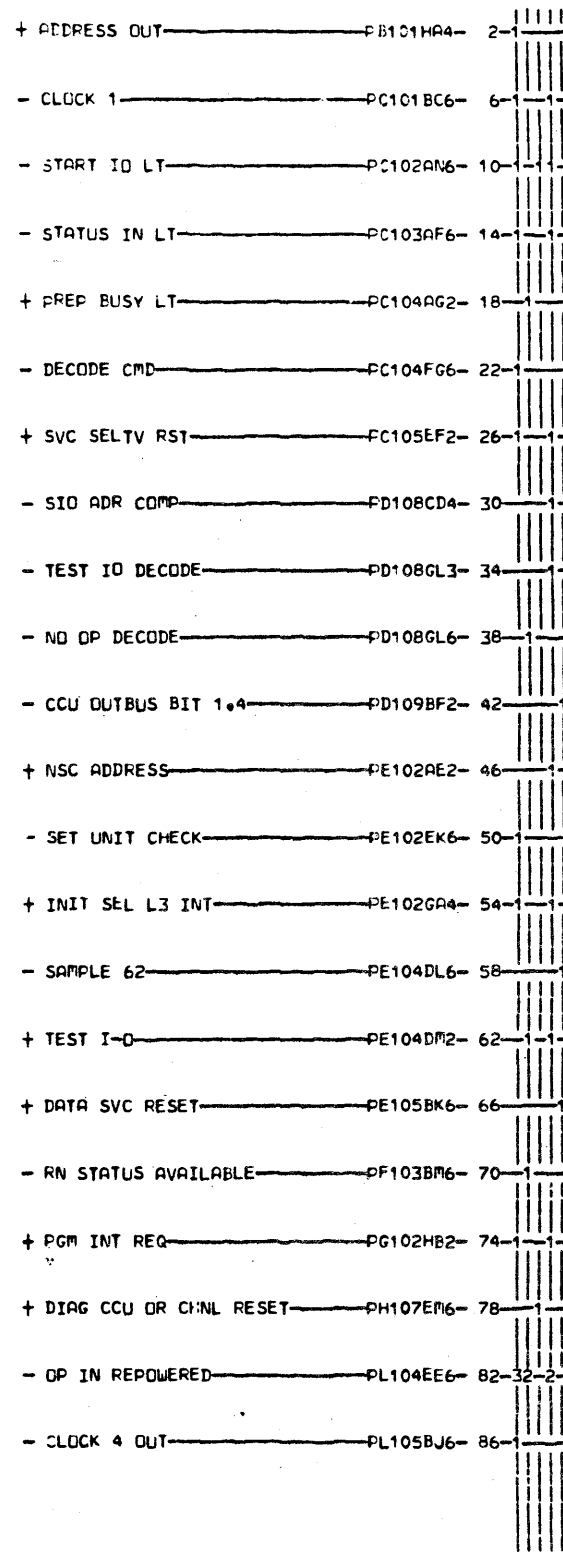
FE104  
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BYTE TRANSFER COUNT			
-E.C.-	HISTORY	C-MACH-27RNB	
		FRAME	01
DATE	LAST EC	IBM COMP.SDD	PE104
02-23-76	314402	IP.N. 1755042	000



PE105  
000

SERVICE TRANSFER	
E.C. HISTORY	C. MACH. 27RNB
DATE	LAST EC
02-23-76	31-402
FRAME	01
IBM CORP. SDD	PE105
P.N.	1755043
	000

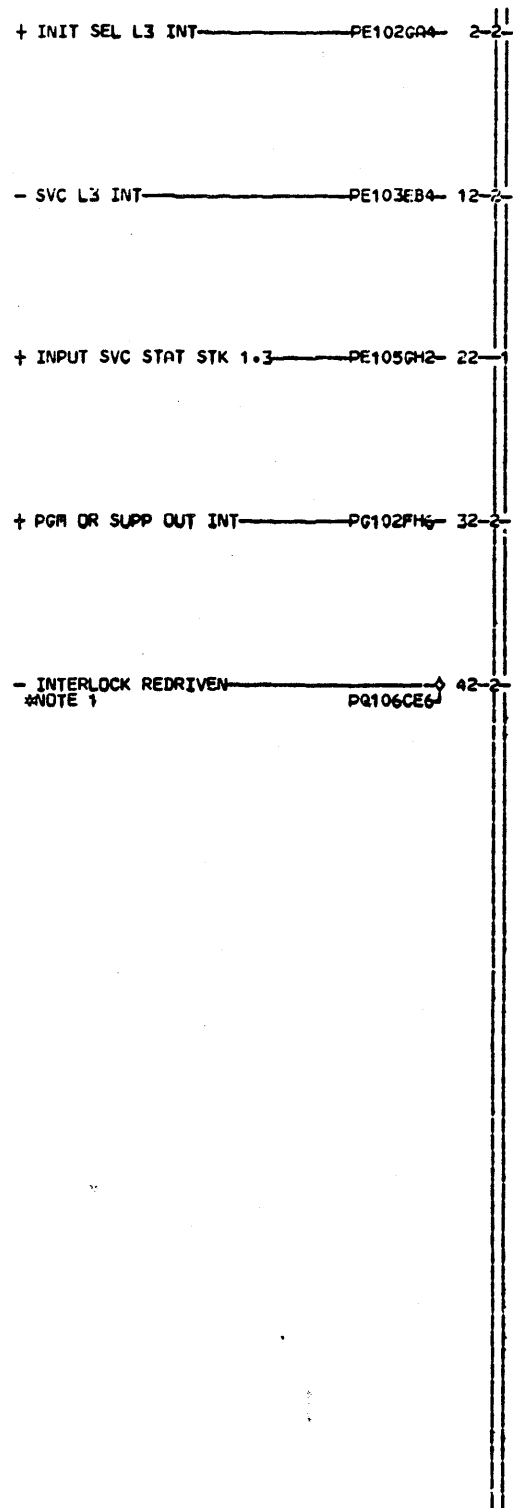


000 PE106

LDC. TYPE  
A-E4L2 7602

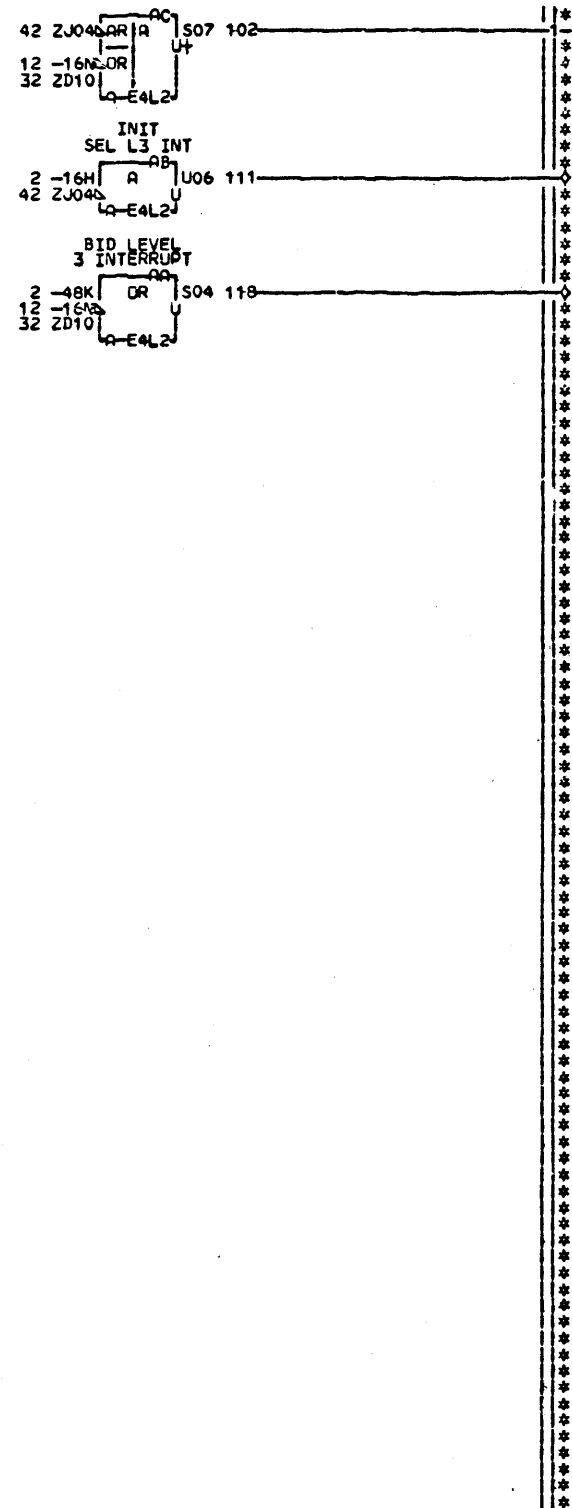
INITIAL STATUS GEN	
E.C. HISTORY	MACH. 27RNB
DATE LAST EC	FRAME 01
02-23-76 314402	IBM CORP. SDD
	P.No. 1755044
	000

PE106  
000

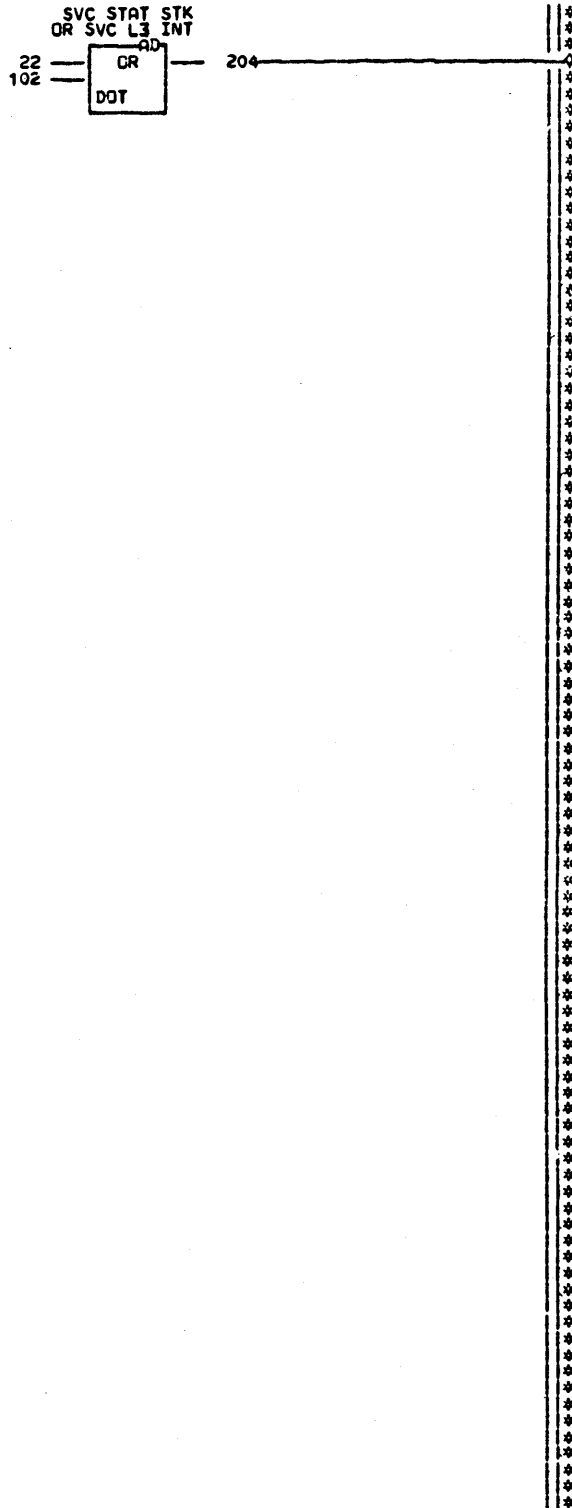


NOTE 1. IF BOARD IS AT EC  
LEVEL 3156200 SOURCE FOR  
- INTERLOCK IS PM101FB6

PE107  
000



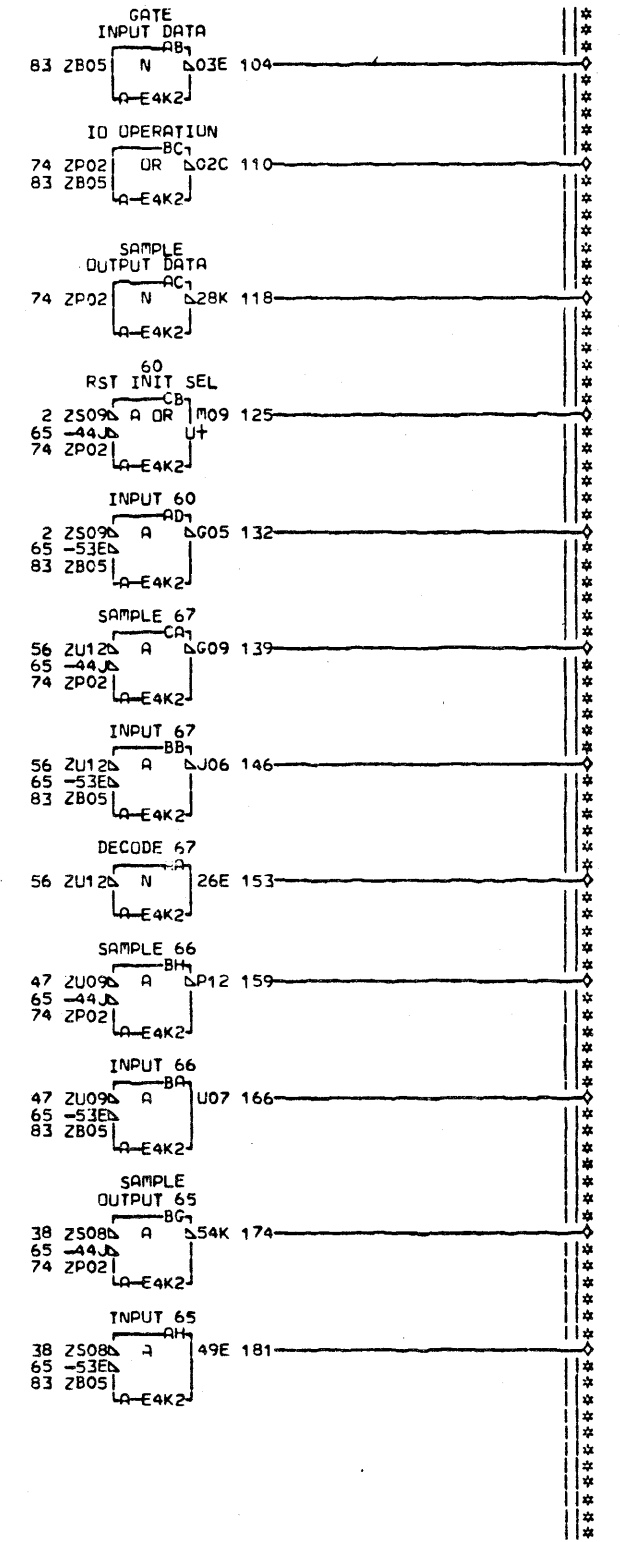
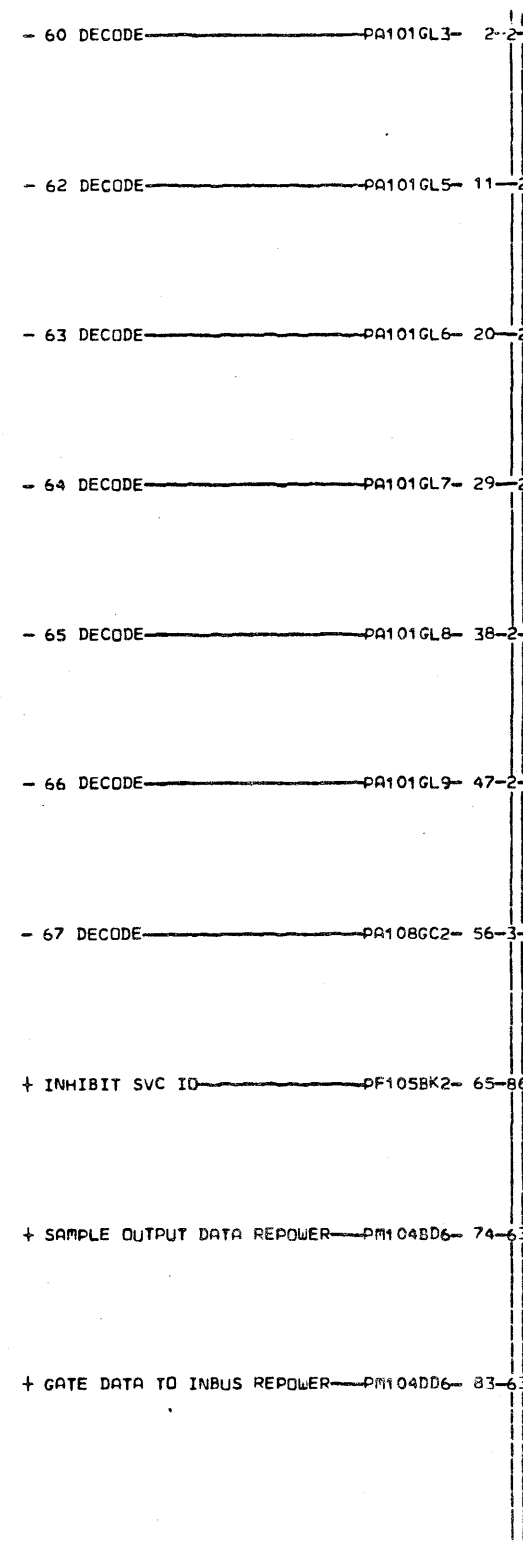
LOC. TYPE  
A-E4L2 7602



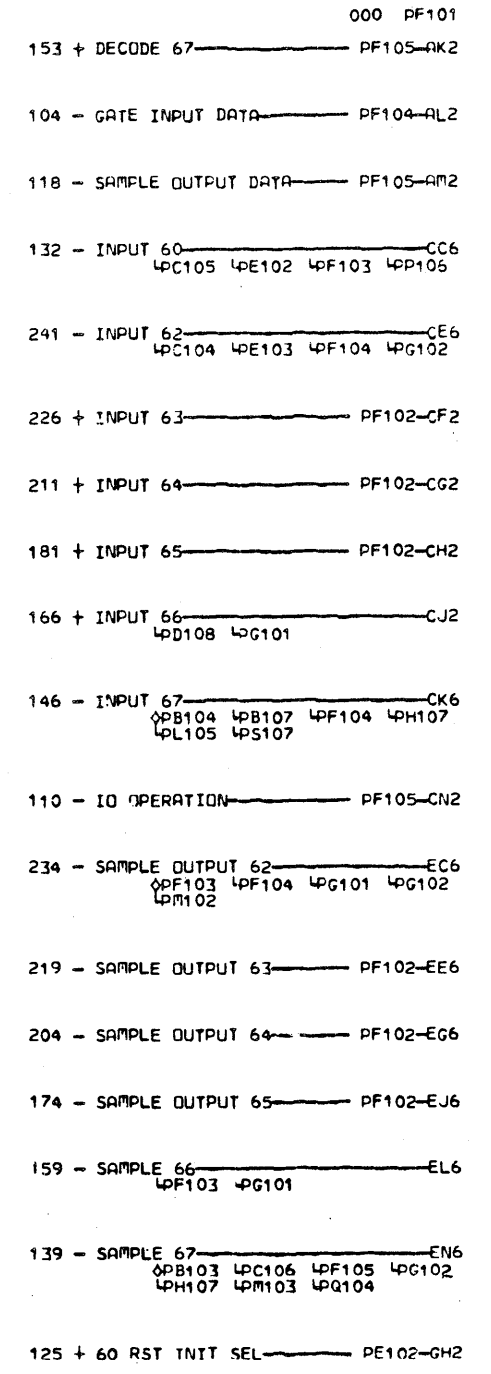
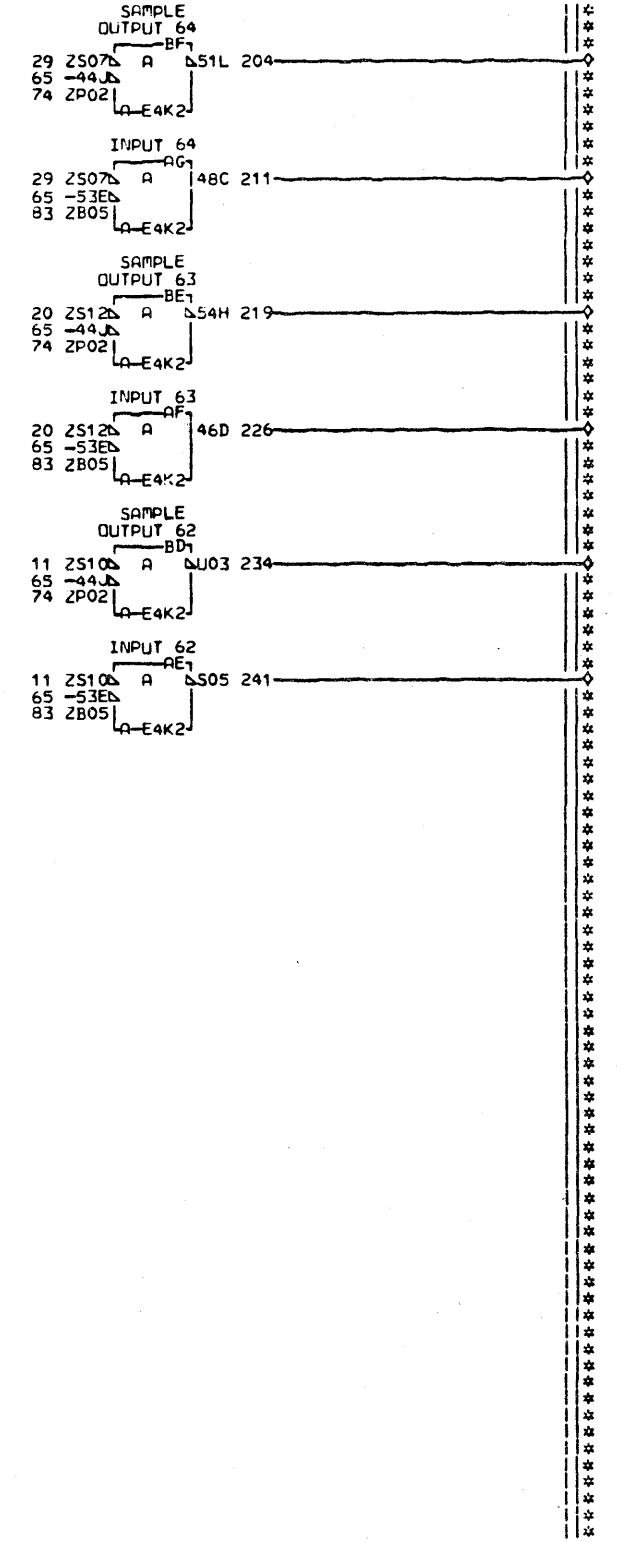
000 PE107  
118 + BID LEVEL 3 INTERRUPT—PR103-CB6  
111 + INIT SEL L3 INT—PR104-CC2  
204 + SVC STAT STK OR SVC L3 INT—EE4  
LPM104

OR DOTS BYTE 0			
E.C. HISTORY	314402	RACH.27RNB	
		FRAME	01
		IBM CORP.SDD	PE107
DATE	LAST EC	P.N.	1755045 000
01-09-78	318552		



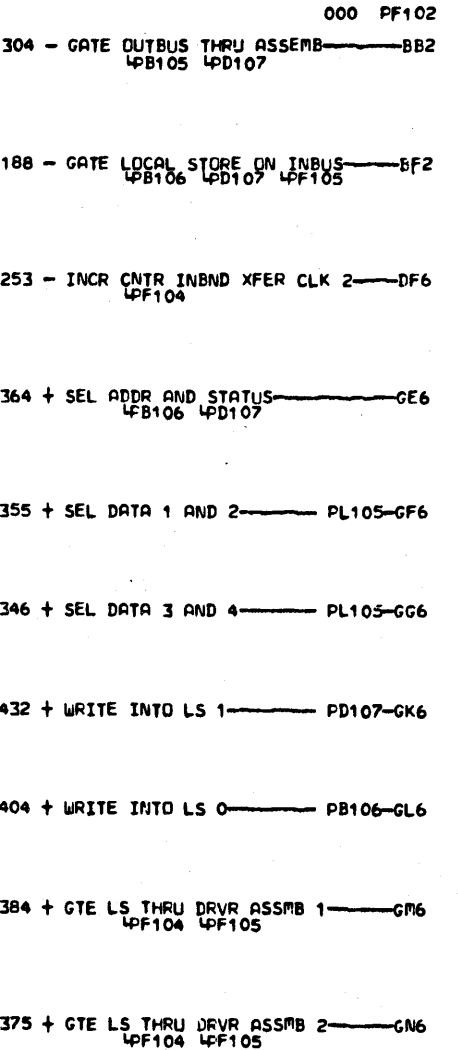
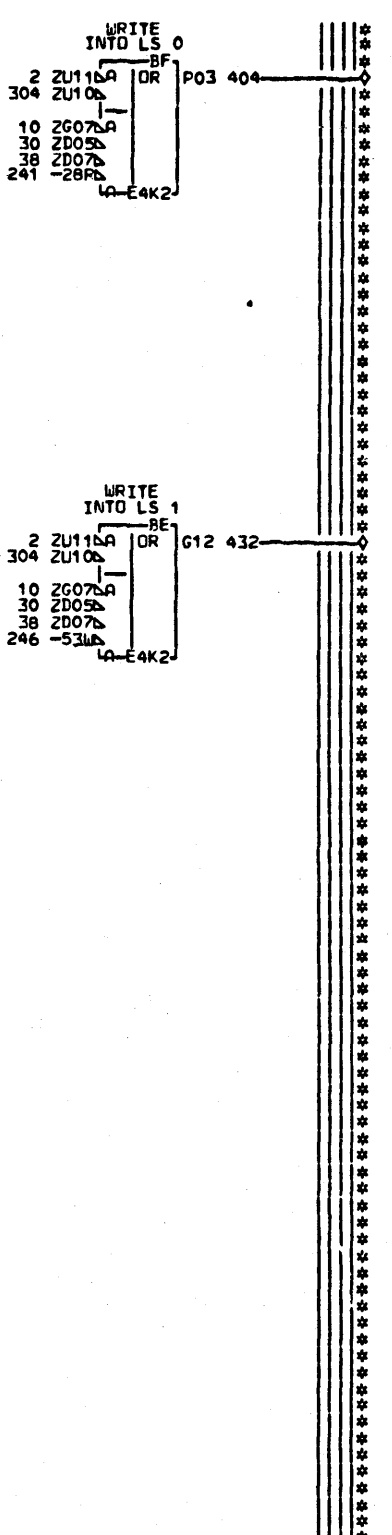
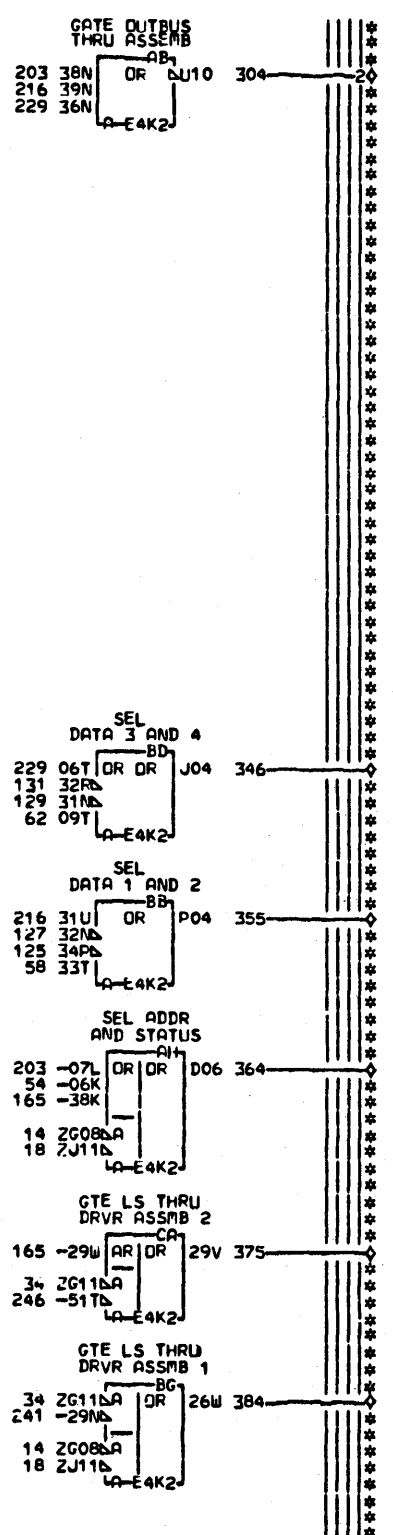
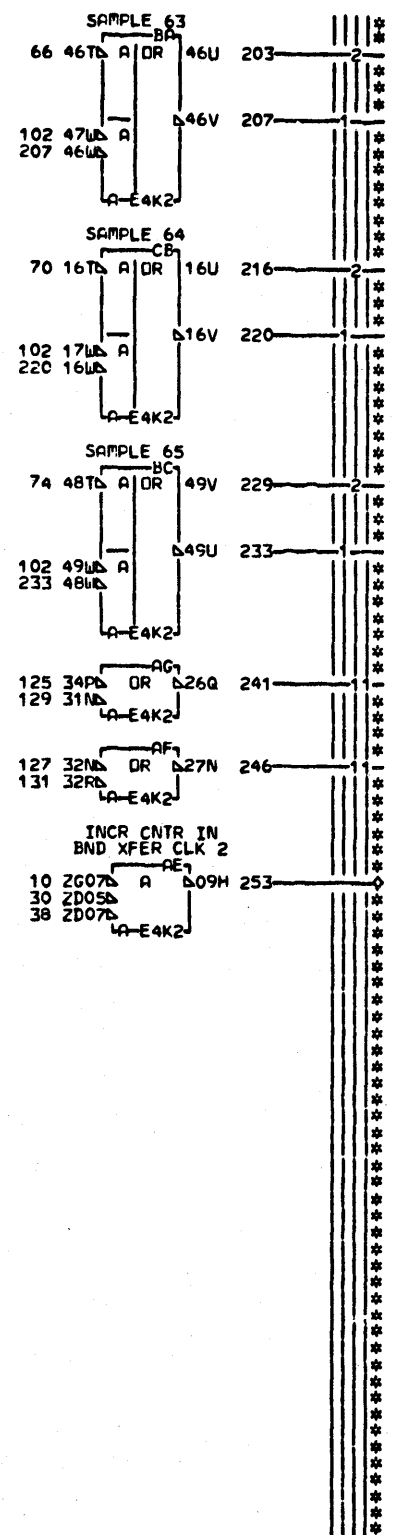
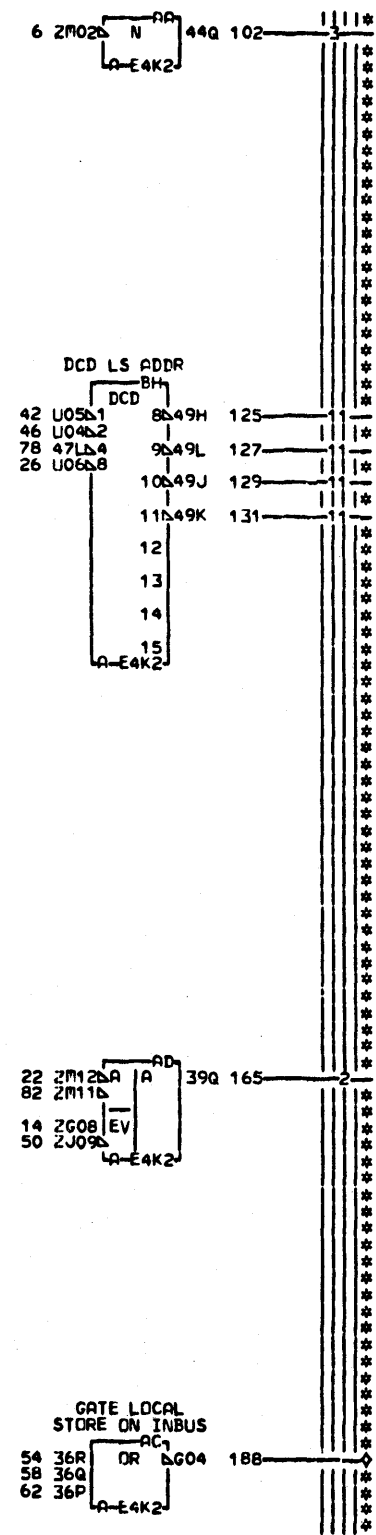
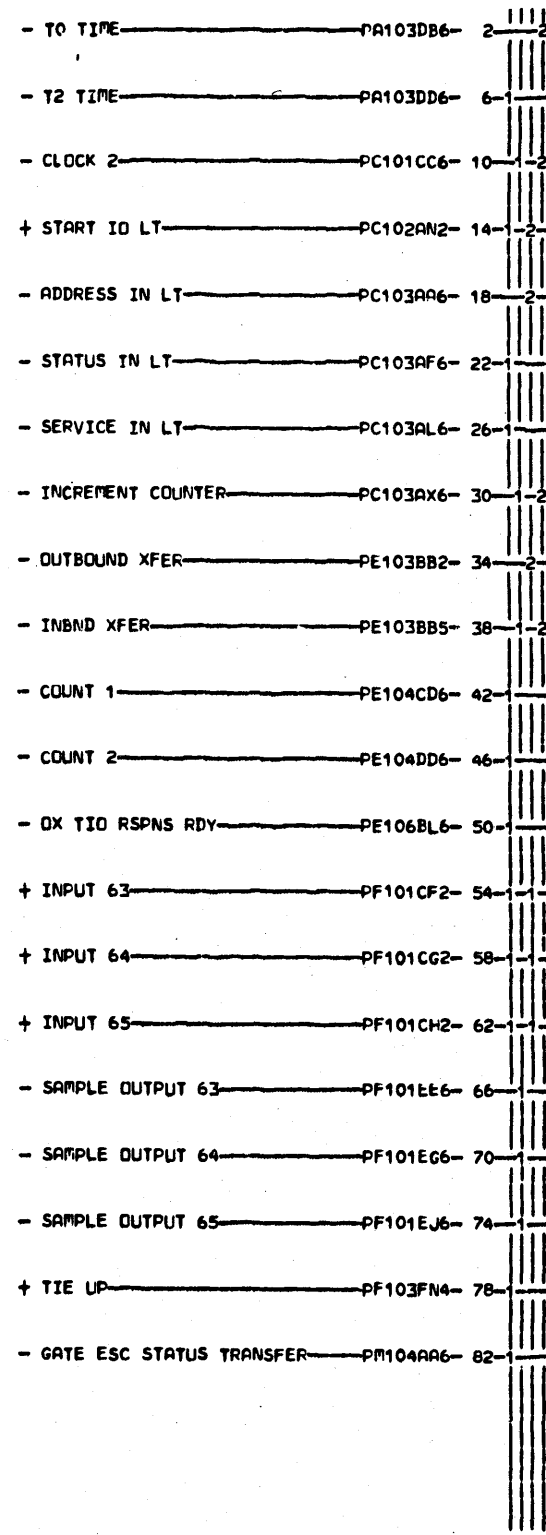


LDC. TYPE  
A-E4K2 7603

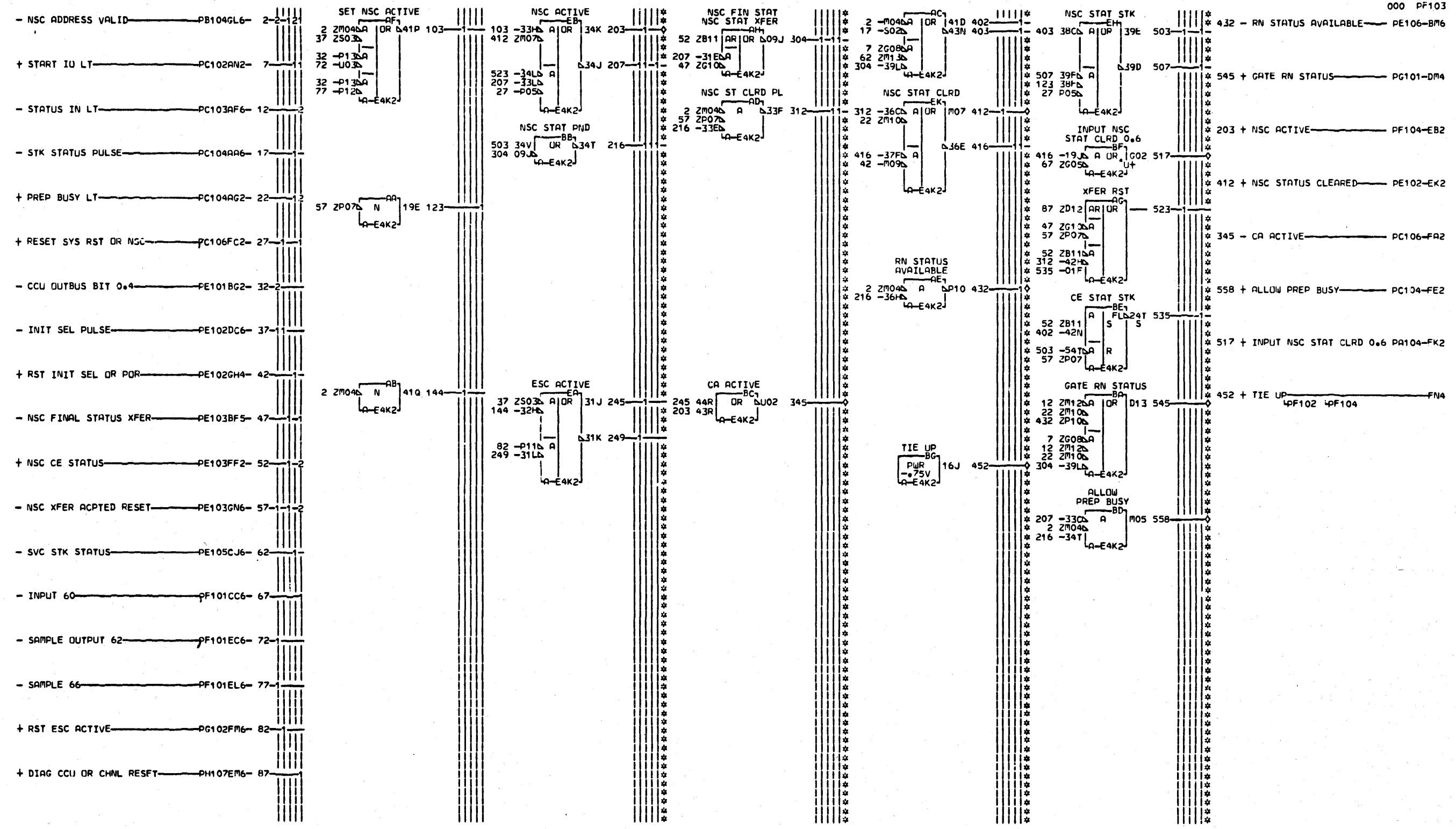


PF101  
000

INPUT-OUTPUT CONTROL	
E.C. HISTORY — 314402	C. MACH. #27RNB
DATE — 05-27-76	LAST EC — 314424
FRAME — 01	IBM CORP. SDD
P.N. — 1755046	000

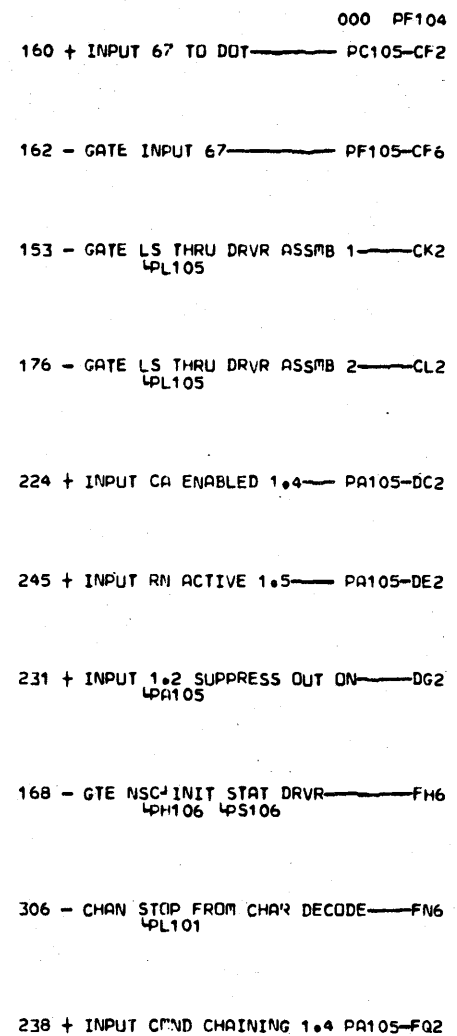
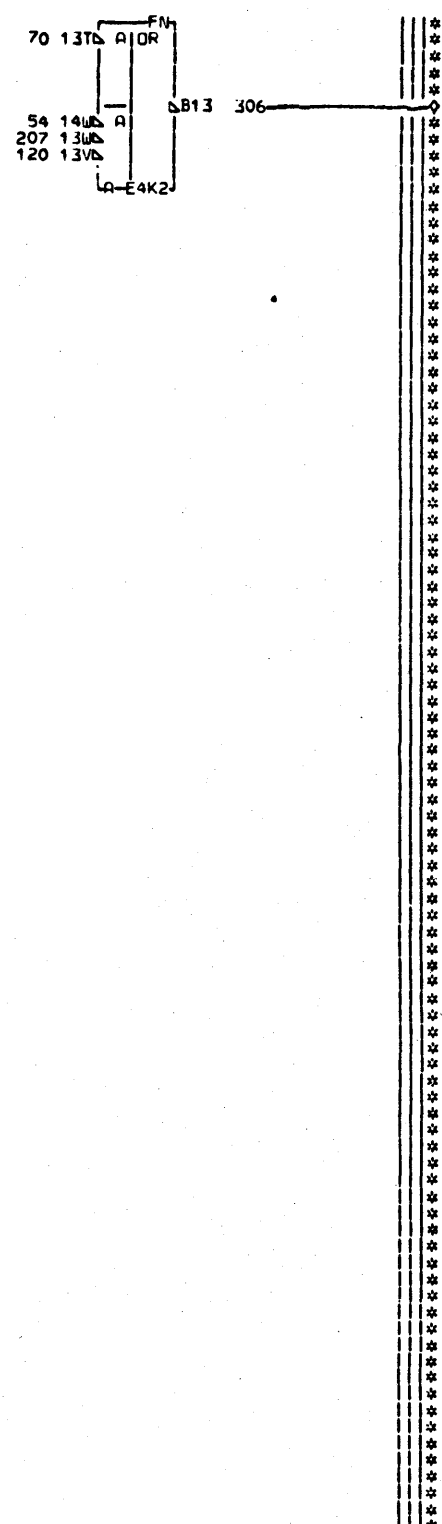
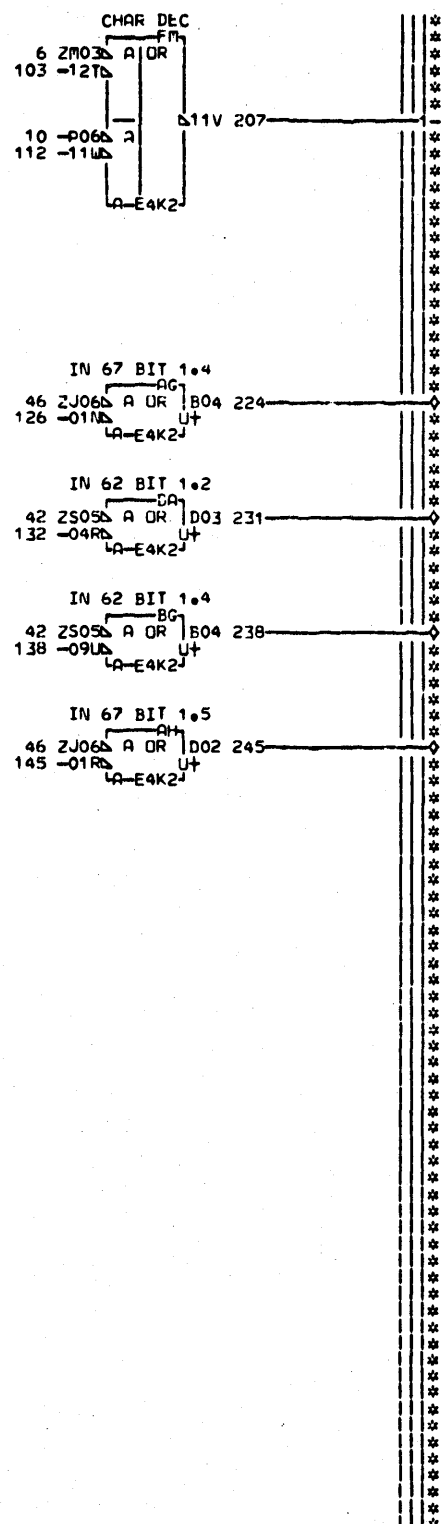
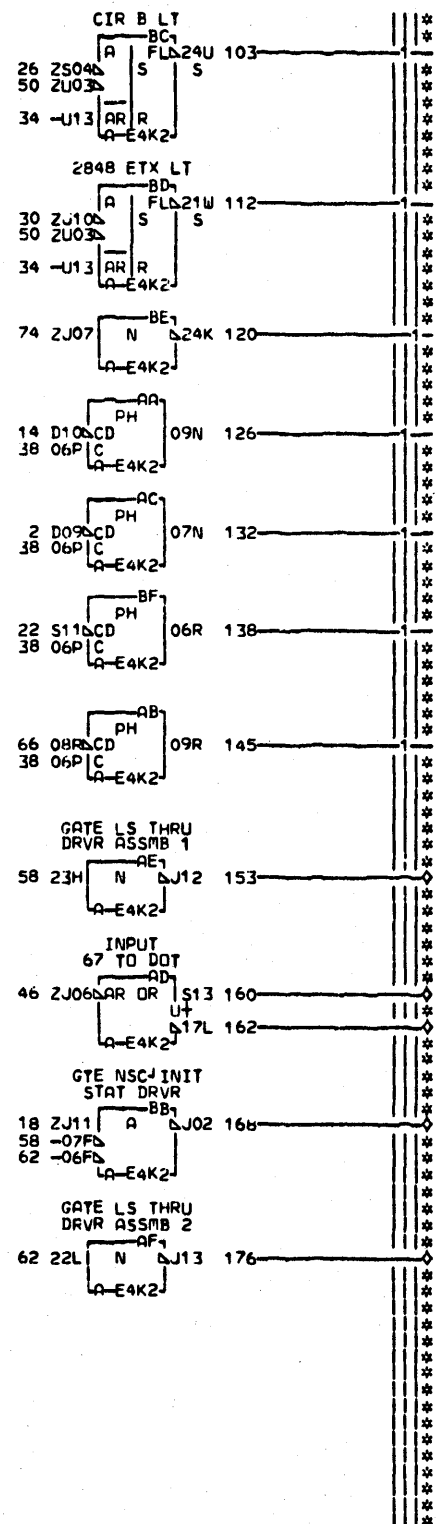
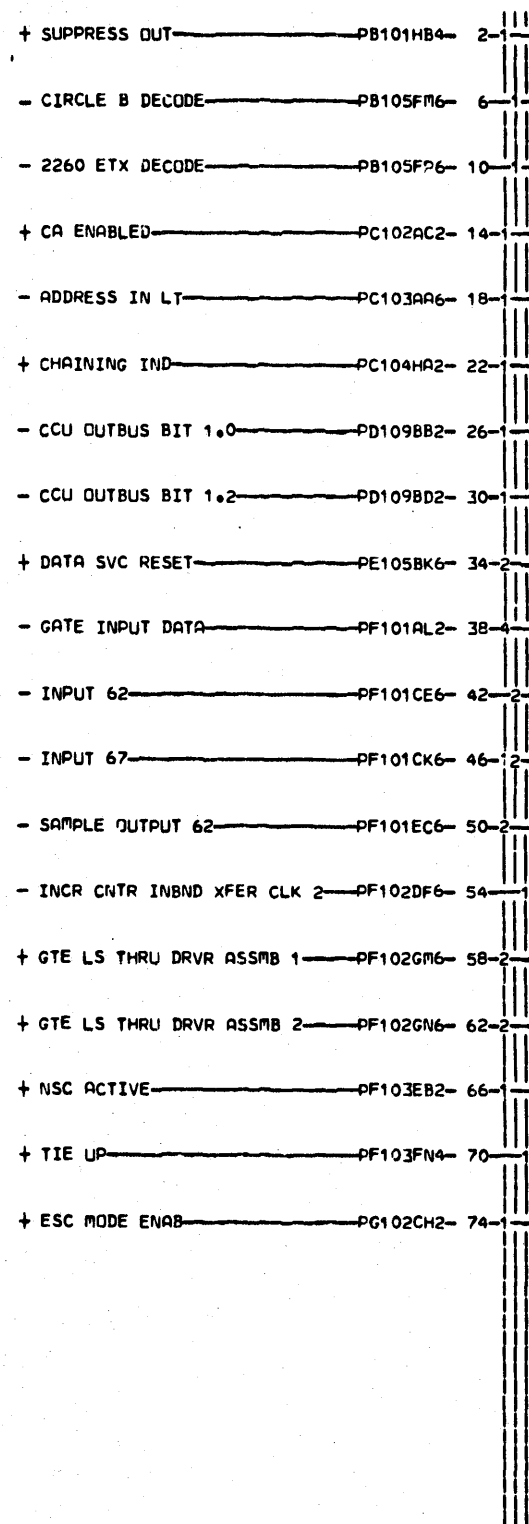


LOC. TYPE  
A-E4K2 7603



LOC. TYPE  
A-E4K2 7603

NSC CONTROL			
E.C. HISTORY	C	MACH#27RNB	
314402		FRAM# 01	
DATE LAST EC		IBM CORP. SDD	PF103
05-17-76 314424		P.N. 1755048	000

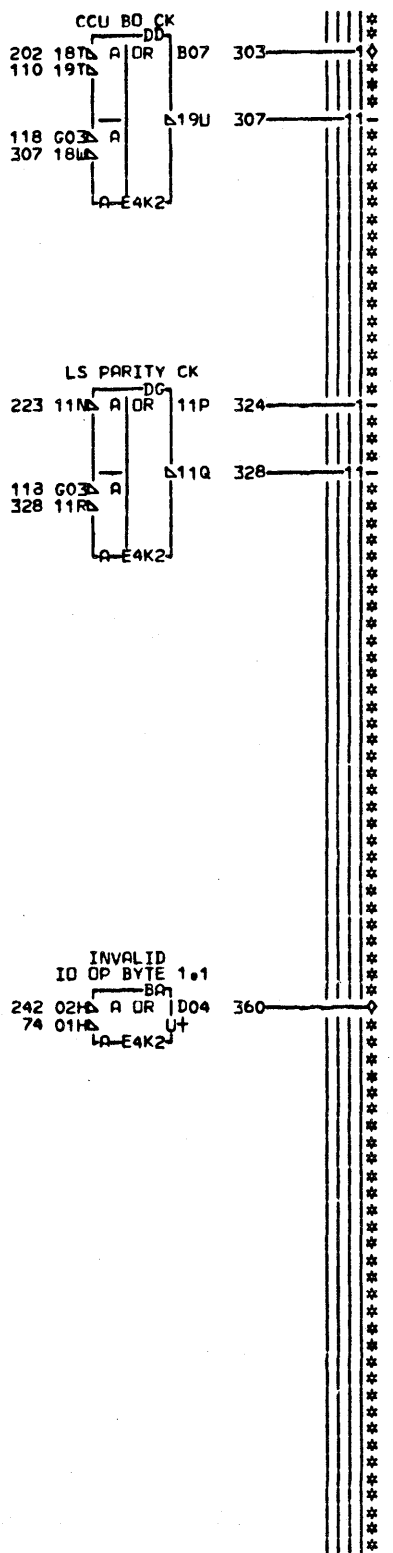
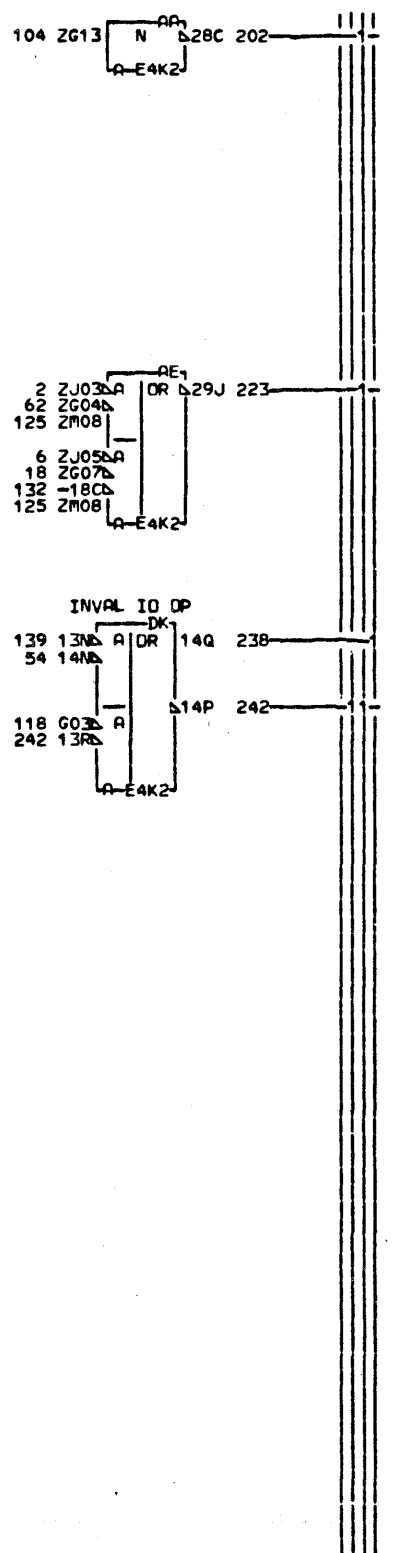
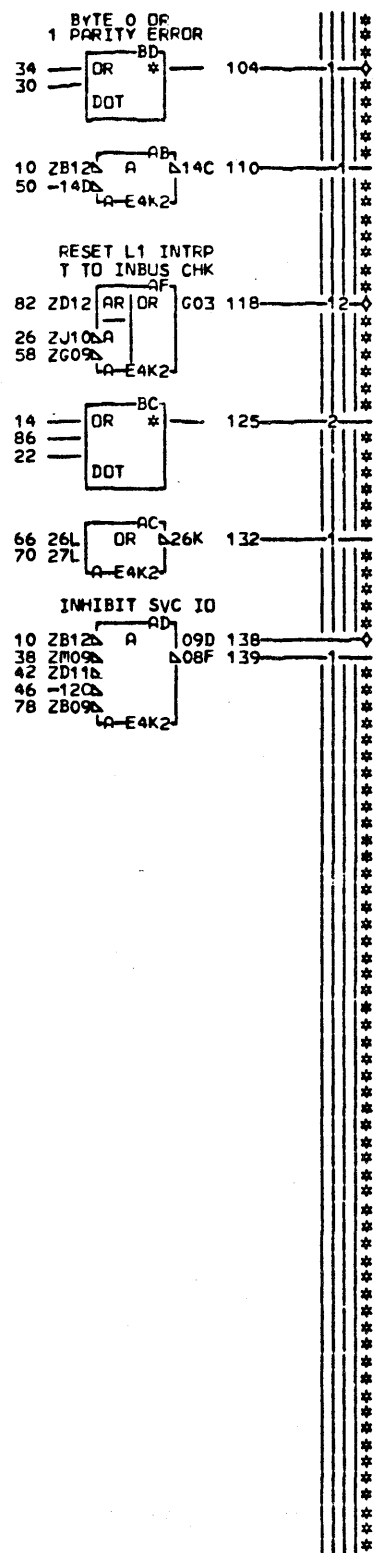


LOC. TYPE  
A-E4K2 7603

PF104  
000

RN ASYNCHRONOUS INFO  
 -E.C.-HISTORY — C1 MACH. 27RNB  
 314402 FRAME 01  
 DATE LAST EC IBM CORP. SDD PF104  
 05-17-76 314424 P.N. 1755049 000

- T3 TIME PA103DE6 2  
 + CA 50 OR 62.5 NS CLOCK PA103FG2 6  
 - TYPE 4 CA DECODES PA108EE6 10-2  
 + LS 0 PAR ER UNCLK PB106FM2 14-1  
 - CLOCK 2 PC101CC6 18-1  
 + LS 1 PAR ER UNCLK PD107HB2 22-1  
 - CCU OUTBUS BIT 1.2 PD109BD2 26-1  
 + BYTE 1 PARITY ERR PD109FE2 30-1  
 + BYTE 0 PARITY ERR PE101GF6 34-1  
 + RST INIT SEL DR POR PE102GH4 38-1  
 - INITIATE SERVICE CYCLE PE103FK6 42-1  
 + DECODE 67 PF101AK2 46-1  
 - SAMPLE OUTPUT DATA PF101AM2 50-1  
 - ID OPERATION PF101CN2 54-1  
 - SAMPLE 67 PF101EN6 58-1  
 - GATE LOCAL STORE ON INBUS PF102BF2 62-1  
 + GTE LS THRU DRVR ASSMB 1 PF102GM6 66-1  
 + GTE LS THRU DRVR ASSMB 2 PF102GN6 70-1  
 - GATE INPUT 67 PF104CF6 74-1-2  
 - REQ ENAB INTF PG101GN2 78-1  
 + DIAG CCU DR CHNL RESET PH107EM6 82-1  
 + PAR ERR UNCLK OUT PL105FB2 86-1  
 + INTF B CHNL BUS IN ERR PS107CC2 90-1

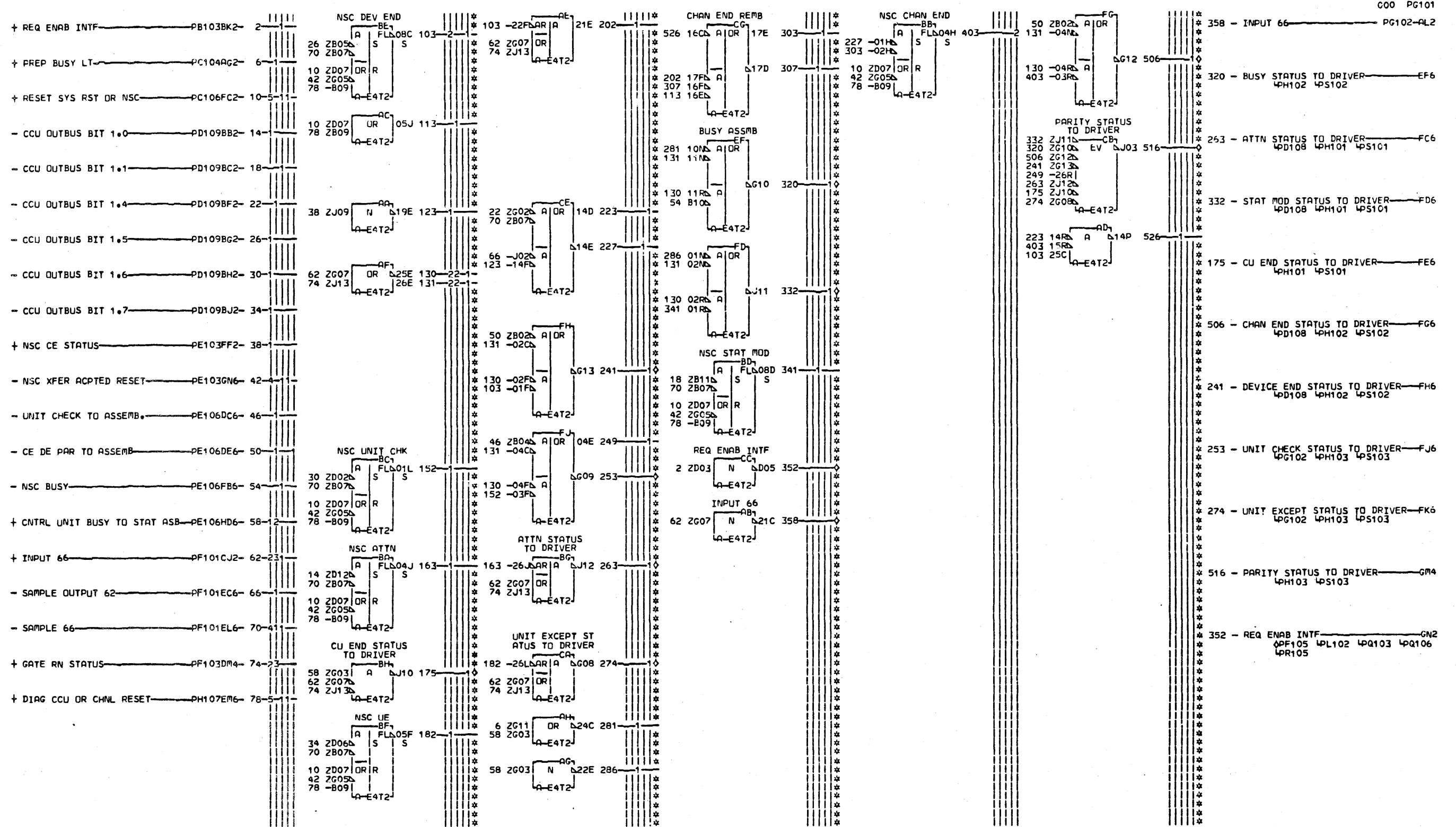


000 FF105  
 138 + INHIBIT SVC IO PF101-BK2  
 118 + RESET L1 INTRPT TO INBUS CHK-CL4  
 WPH107 WPL103 WPQ105 WPS107  
 303 + CCU BO CK PE103-DD2  
 453 + CCU BUS OUT CK BYTE 1.2 PA105-GD2  
 446 + LOCAL STORE PAR CK BYTE 1.3-GG2  
 WPA105  
 360 + INVALID IO OP BYTE 1.1- PA105-GK2  
 404 + CA L1 INTERRUPT REQ PA102-GM6  
 104 + BYTE 0 OR 1 PARITY ERROR-GP4  
 WPA105

LOC. TYPE  
A-E4K2 7603

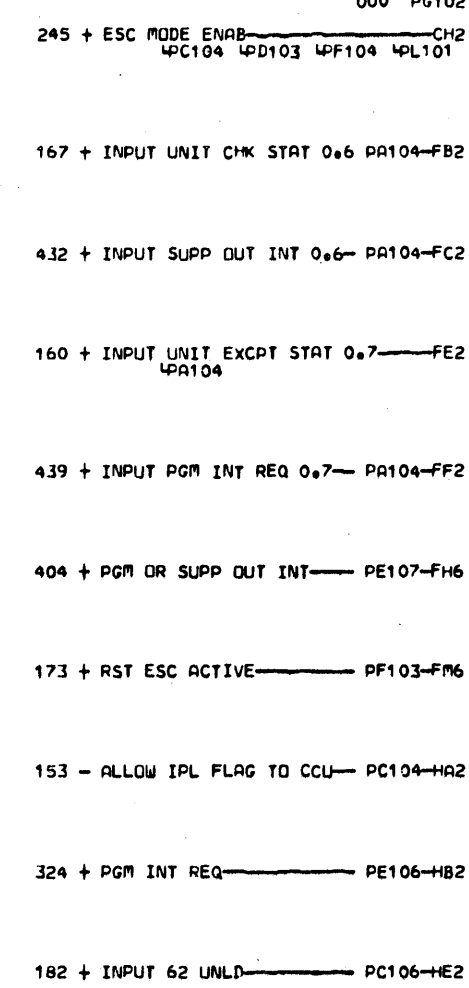
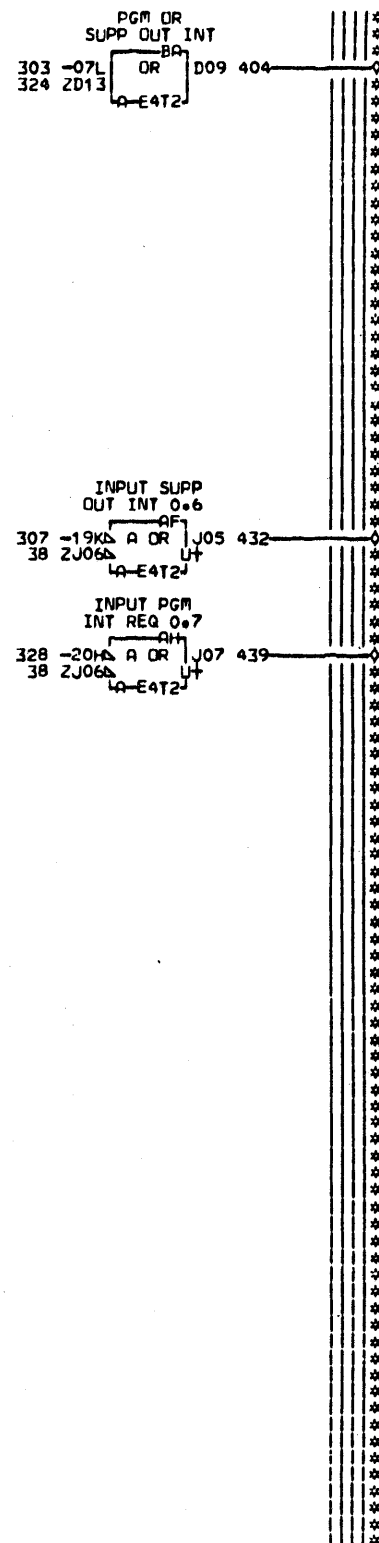
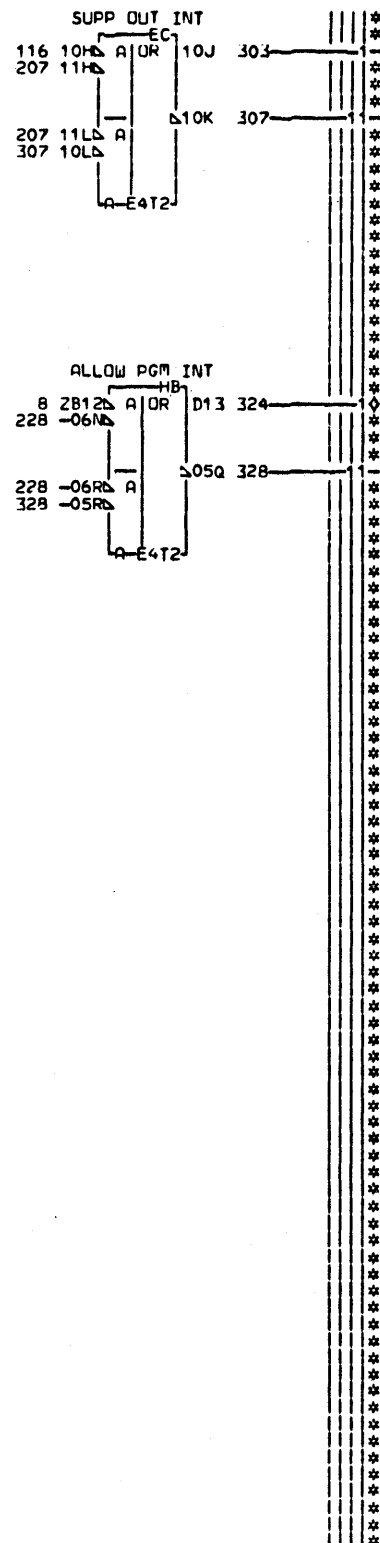
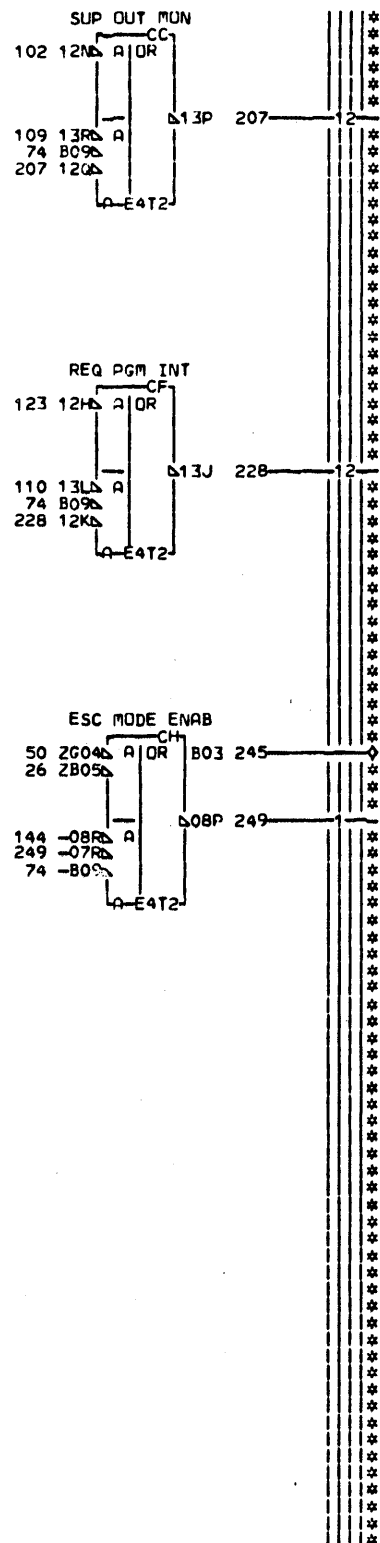
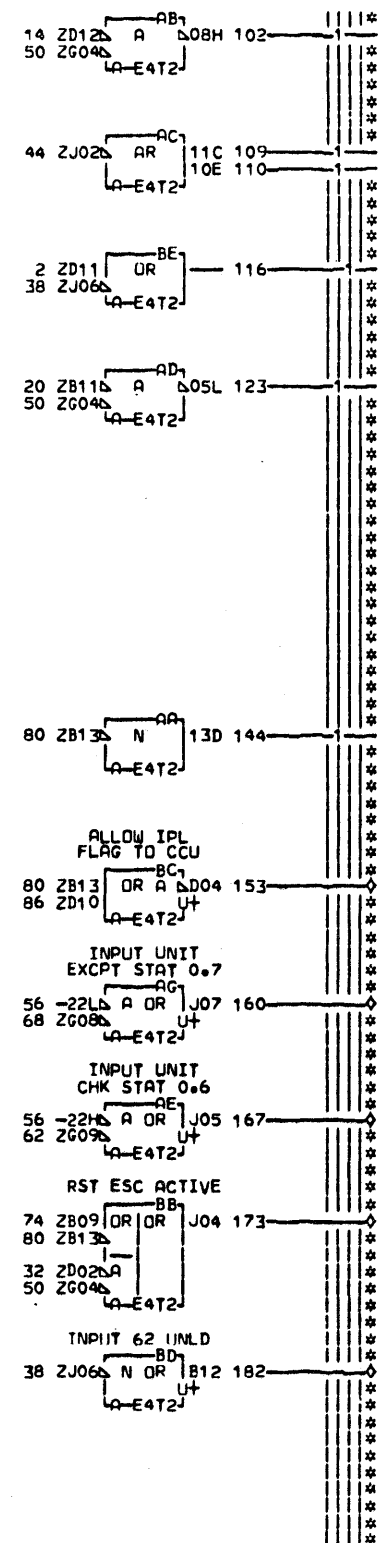
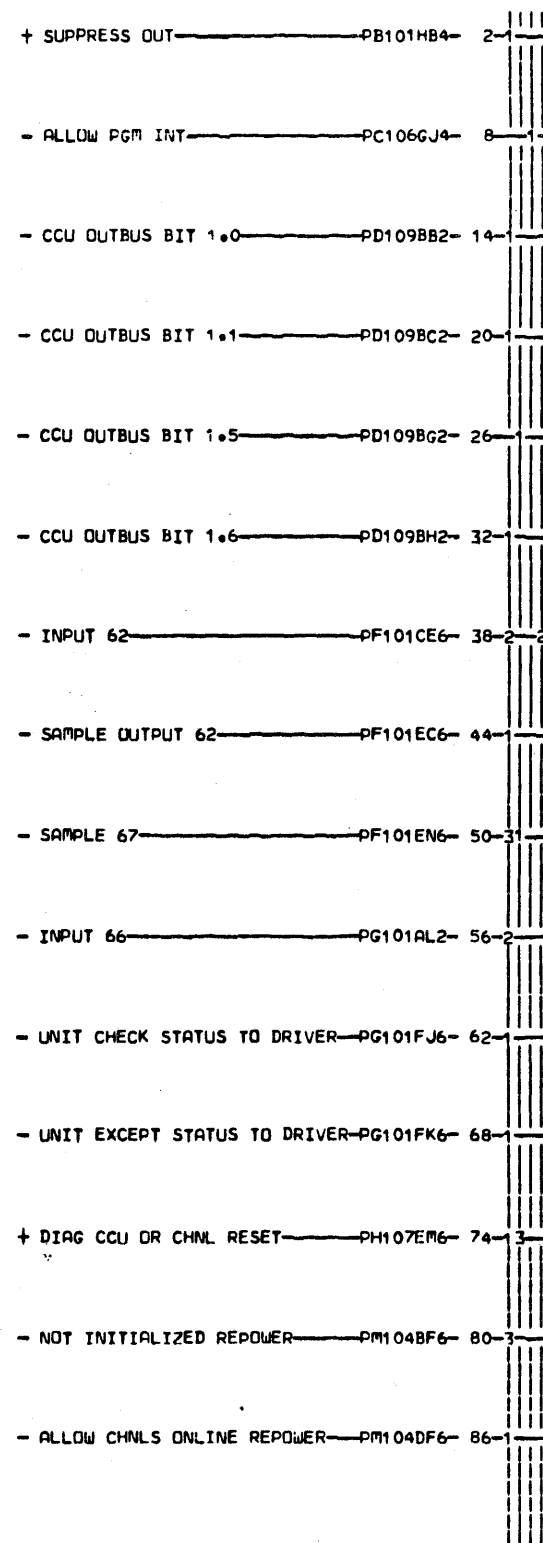
PF105  
000

ERROR LATCHES  
 -E.C.-HISTORY-C MACH.27RNB  
 FRAME 01  
 IBM CORP.SDC PF105  
 DATE LAST EC P.No. 1755050 000  
 02-23-76 314402



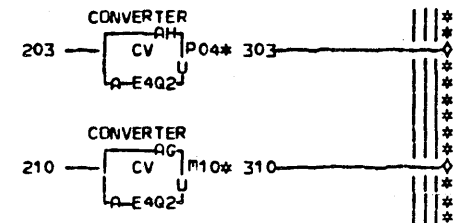
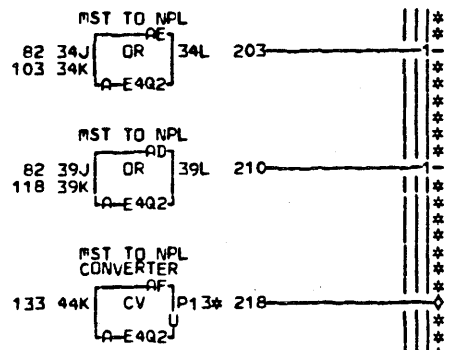
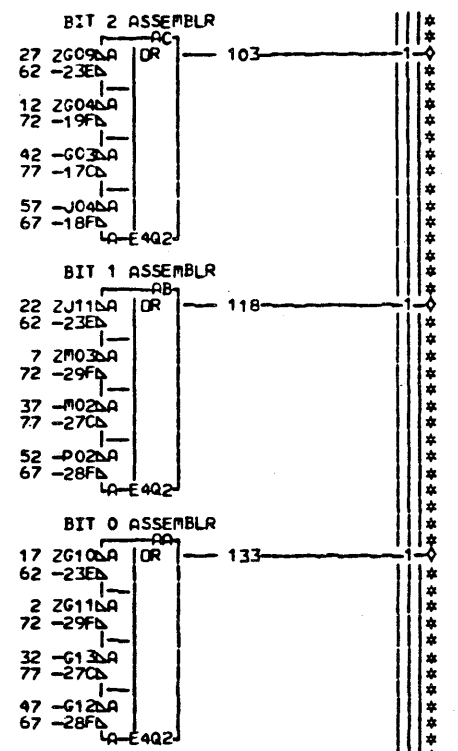
LDC. TYPE  
A-E4T2 2326

NSC STATUS REG			
E.C. HISTORY	C1	MACH.27RNB	
3144C2		FRAME	01
DATE	LAST EC	IBM CORP.SDD	PG101
05-17-76	314424	P.No. 1755051	000



LOC. TYPE  
A-E4T2 2326

- LOCAL STORE BIT 0.0 TO DRVR—PB106DC4— 2—1  
 - LOCAL STORE BIT 0.1 TO DRVR—PB106DD4— 7—1  
 - LOCAL STORE BIT 0.2 TO DRVR—PB106DE4— 12—1  
 - SIO ADR BIT 1.0 — PD106BC6— 17—1  
 - SIO ADR BIT 1.1 — PD106BD6— 22—1  
 - SIO ADR BIT 1.2 — PD106BF6— 27—1  
 - LOCAL STORE BIT 1.0 TO DRVR—PD107DC6— 32—1  
 - LOCAL STORE BIT 1.1 TO DRVR—PD107DD6— 37—1  
 - LOCAL STORE BIT 1.2 TO DRVR—PD107DF6— 42—1  
 - ATTN STATUS TO DRIVER—PG101FC6— 47—1  
 - STAT MOD STATUS TO DRIVER—PG101FD6— 52—1  
 - CU END STATUS TO DRIVER—PG101FE6— 57—1  
 - GATE ADDRESS TO CHANNEL—PH106CF6— 62—3  
 - GATE STATUS TO CHAN—PH106CG6— 67—3  
 - GATE DATA BYTE 1 TO CHANNEL—PH106CH6— 72—3  
 - GATE DATA BYTE 2 TO CHANNEL—PH106CJ6— 77—3  
 + GEN CTRL UNIT BUSY STATUS—PH106GN2— 82—2



000 PH101

133 + BUS IN BIT 0 ASSEMBLED— PH103-BB4  
 118 + BUS IN BIT 1 ASSEMBLED— PH103-BF4  
 103 + BUS IN BIT 2 ASSEMBLED— PH103-BK4  
 218 + MPL BIT 0 TO INTF A— PA014-EC4  
 310 + MPL BIT 1 TO INTF A— PA014-EG4  
 303 + MPL BIT 2 TO INTF A— PA014-EL4

EDGE CONN.  
 218 A-E4V3D05  
 303 A-F4V3D06  
 310 A-F4V3R05

LOC. TYPE  
 A-E4Q2 CE27

PH101  
 000

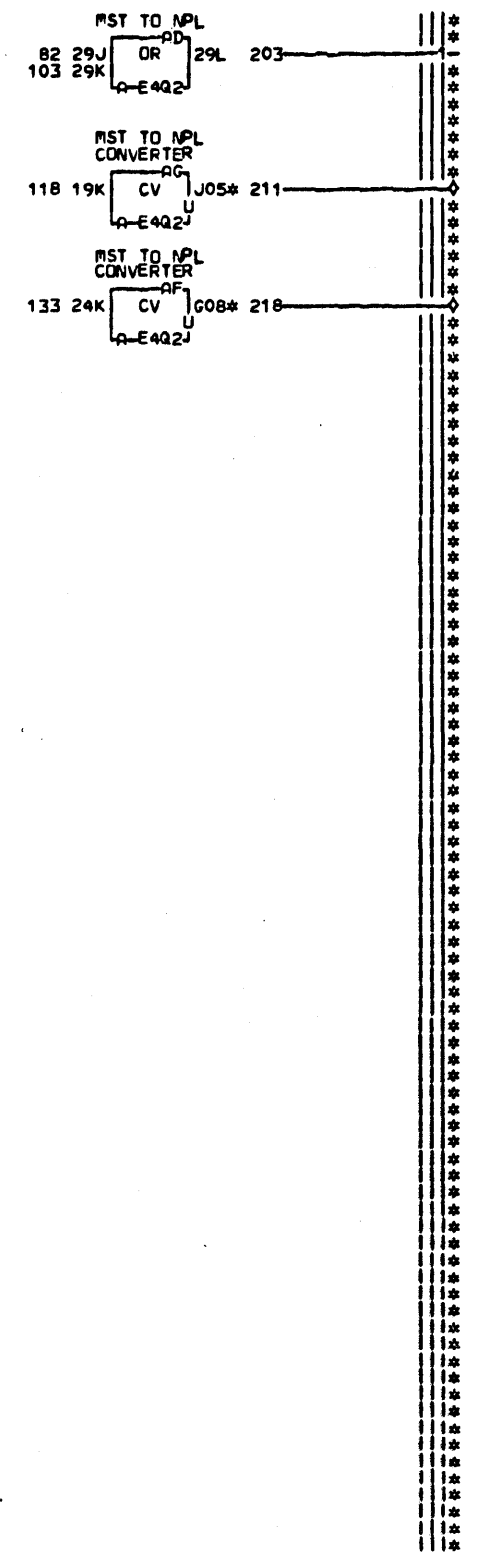
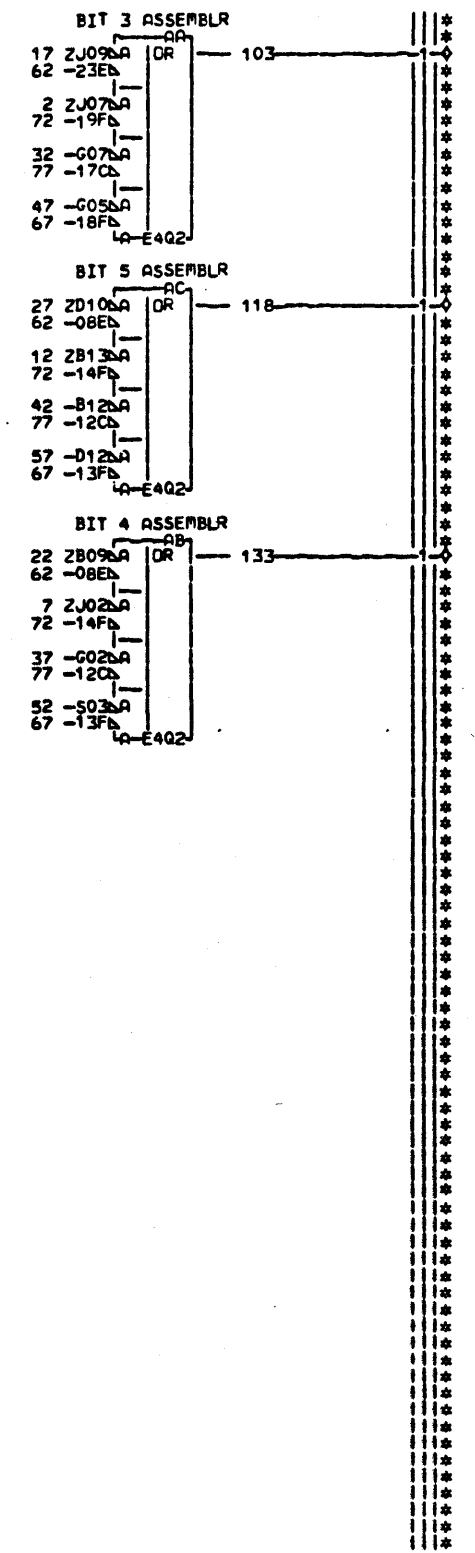
CHANNEL DRIVERS BUS IN	
BITS 0 1 2	
E.C. HISTORY 314402	D. MACH. 27RNB
FRAME 01	
DATE 11-19-76	IBM CORP. SDD PH101
LAST EC 316677	P.No. 1755053 000



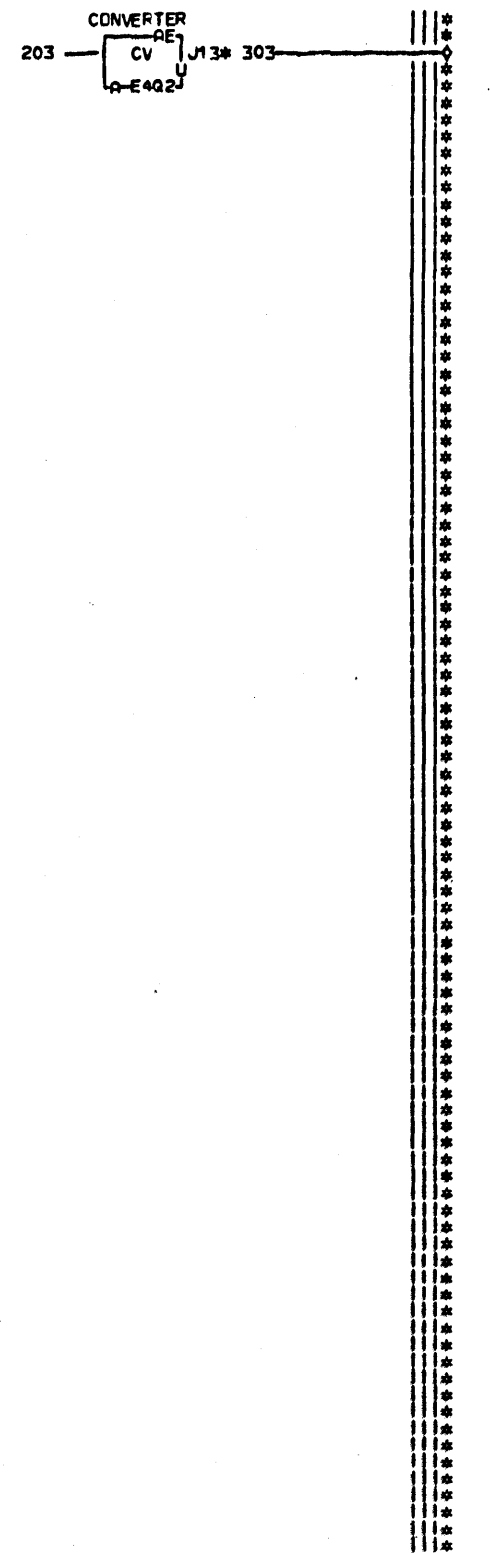
- LOCAL STORE BIT 0.3 TO DRVR—PB106DF4— 2—|||
- LOCAL STORE BIT 0.4 TO DRVR—PB106DG4— 7—|||
- LOCAL STORE BIT 0.5 TO DRVR—PB106DH4— 12—|||
- SID ADR BIT 1.3—PD106BG6— 17—|||
- SID ADR BIT 1.4—PD106BH6— 22—|||
- SID ADR BIT 1.5—PD106BK6— 27—|||
- LOCAL STORE BIT 1.3 TO DRVR—PD107DG6— 32—|||
- LOCAL STORE BIT 1.4 TO DRVR—PD107DH6— 37—|||
- LOCAL STORE BIT 1.5 TO DRVR—PD107DK6— 42—|||
- BUSY STATUS TO DRIVER—PG101EF6— 47—|||
- CHAN END STATUS TO DRIVER—PG101FG6— 52—|||
- DEVICE END STATUS TO DRIVER—PG101FH6— 57—|||
- GATE ADDRESS TO CHANNEL—PH106CF6— 62—|||
- GATE STATUS TO CHAN—PH106CG6— 67—|||
- GATE DATA BYTE 1 TO CHANNEL—PH106CH6— 72—|||
- GATE DATA BYTE 2 TO CHANNEL—PH106CJ6— 77—|||
- + GEN CTRL UNIT BUSY STATUS—PH106GN2— 82—|||

EDGE CONN.  
 211 A-E4V3B10  
 218 A-F4V3D09  
 01A-F4V3D10  
 303 A-F4V3R08  
 01A-E4V3R09

PH102  
 000



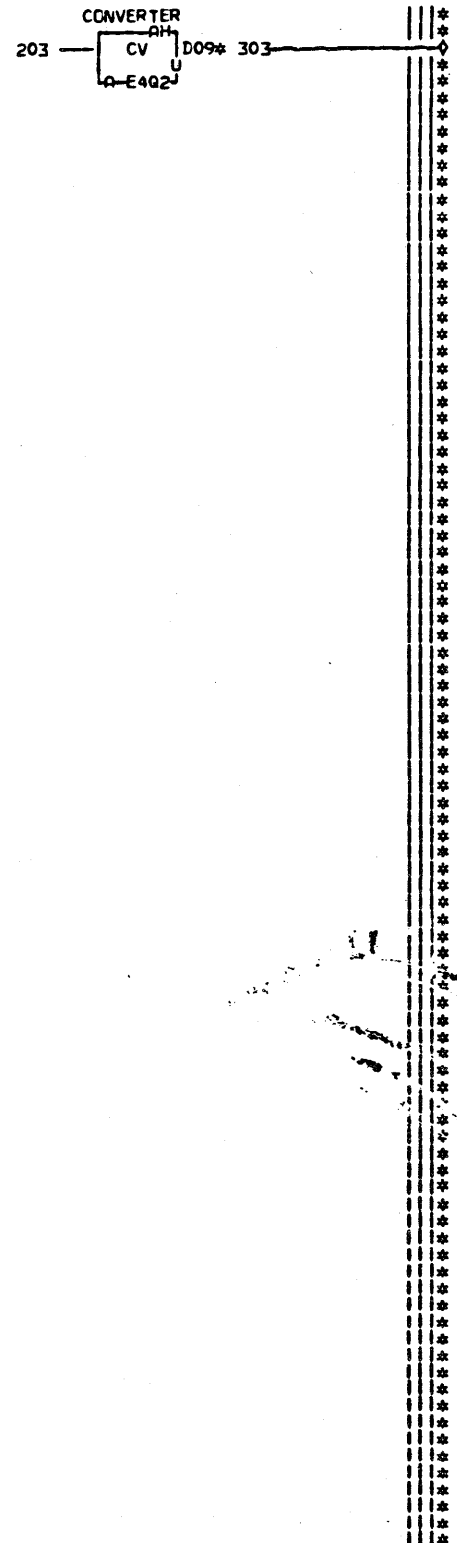
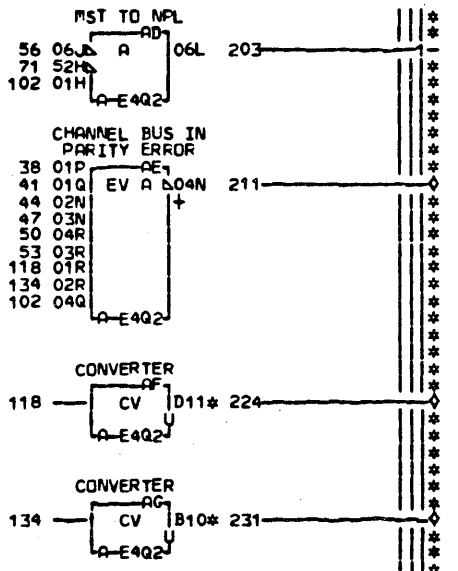
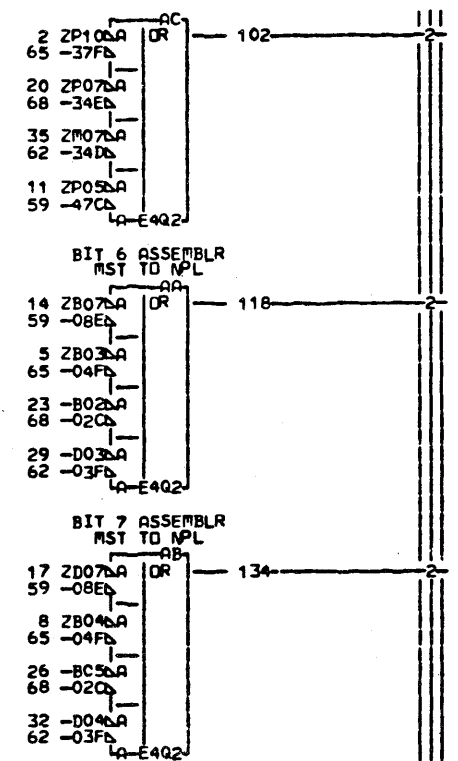
LDC. TYPE  
 A-E4Q2 CE27



- 000 PH102
- 103 + BUS IN BIT 3 ASSEMBLER—PH103-BB4
- 133 + BUS IN BIT 4 ASSEMBLED—PH103-BF4
- 118 + BUS IN BIT 5 ASSEMBLED—PH103-BK4
- 303 + NPL BIT 3 TO INTF A—PA014-EC4
- 218 + NPL BIT 4 TO INTF A—PA014-EG4
- 211 + NPL BIT 5 TO INTF A—PA014-EL4

CHANNEL DRIVERS BUS IN	
BITS 3 4 5	
E.C. HISTORY—314402	D. PACM-27RNB
FRAME 01	
DATE LAST EC 11-19-76 316677	IBR CORP. SDD PH102 P.N. 1755054 000

- LS BIT P TO DRVR OR INBUS — PB106DB4 — 2 —
- LOCAL STORE BIT 0•6 TO DRVR — PB106DJ4 — 5 —
- LOCAL STORE BIT 0•7 TO DRVR — PB106DK4 — 8 —
- SID ADR PTY 1•0-1•7 — PD106BB6 — 11 —
- SID ADR BIT 1•6 — PD106BL6 — 14 —
- SID ADR BIT 1•7 — PD106BM6 — 17 —
- LOCAL STORE BIT 1•P TO DRVR — PD107DB6 — 20 —
- LOCAL STORE BIT 1•6 TO DRVR — PD107DL6 — 23 —
- LOCAL STORE BIT 1•7 TO DRVR — PD107DM6 — 26 —
- UNIT CHECK STATUS TO DRIVER — PG101FJ6 — 29 —
- UNIT EXCEPT STATUS TO DRIVER — PG101FK6 — 32 —
- PARITY STATUS TO DRIVER — PG101GM4 — 35 —
- + BUS IN BIT 0 ASSEMBLED — PH101BB4 — 38 —
- + BUS IN BIT 1 ASSEMBLED — PH101BF4 — 41 —
- + BUS IN BIT 2 ASSEMBLED — PH101BK4 — 44 —
- + BUS IN BIT 3 ASSEMBLER — PH102BB4 — 47 —
- + BUS IN BIT 4 ASSEMBLED — PH102BF4 — 50 —
- + BUS IN BIT 5 ASSEMBLED — PH102BK4 — 53 —
- INTERFACE ENABLED AND OP IN — PH106BK6 — 56 —
- GATE ADDRESS TO CHANNEL — PH106CF6 — 59 —
- GATE STATUS TO CHAN — PH106CG6 — 62 —
- GATE DATA BYTE 1 TO CHANNEL — PH106CH6 — 65 —
- GATE DATA BYTE 2 TO CHANNEL — PH106CJ6 — 68 —
- + GEN CTRL UNIT BUSY STATUS — PH106GN2 — 71 —

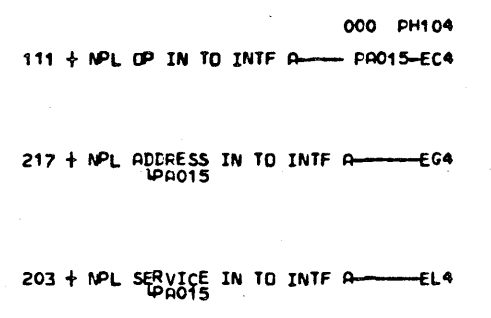
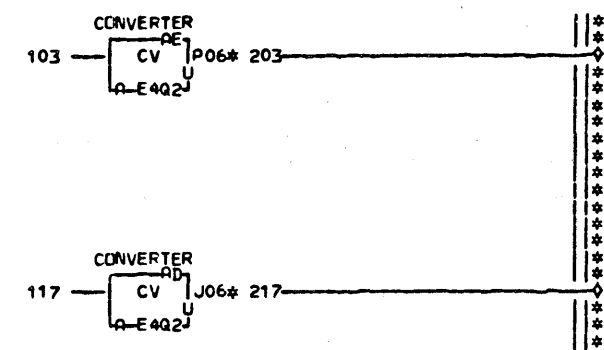
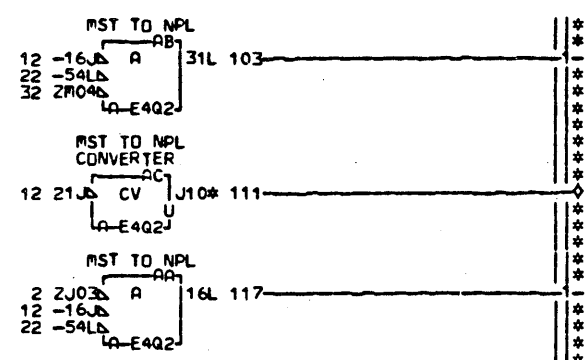
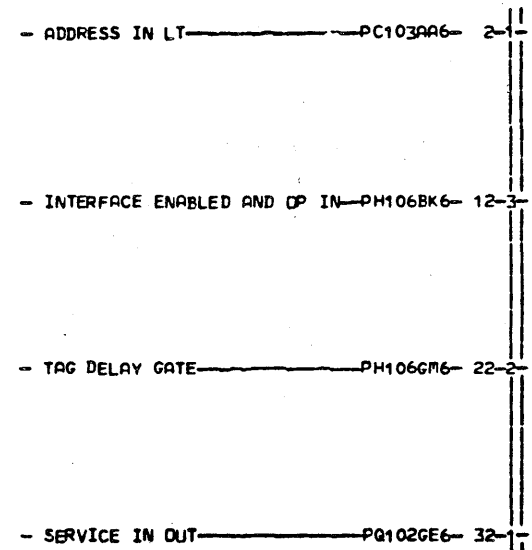


- 211 - CHANNEL BUS IN PARITY ERROR — DM2 LPH107
- 224 + MPL BIT 6 TO INTF A — PA014-EC4
- 231 + MPL BIT 7 TO INTF A — PA014-EG4
- 303 + MPL BIT P TO INTF A — PA014-EL4

EDGE CONN.  
224 A-E4V3D11  
231 A-F4V3B12  
303 A-F4V3R02

LDC. TYPE  
A-E4Q2 CE27

CHANNEL DRIVERS BUS IN		BITS 6 7 P	
E.C. HISTORY	314402	FRAM	01
DATE	11-19-76	IBN CORP. SDD	PH103
LAST EC	316677	P.No.	1755055 000



EDGE CONN.  
 111 A-E4V8B02  
 203 A-E4V4D06  
 217 A-E4V8B05

LOC. TYPE  
 A-E4Q2 CE27

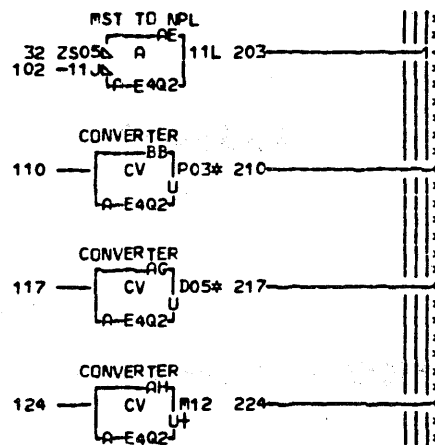
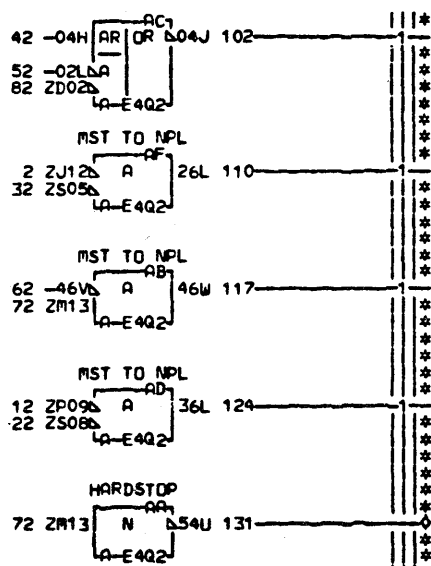
PH104  
 000

CHANNEL DRIVERS			
OP IN ADDR IN AND SFRVICE IN	E.C.—HISTORY—D	RACH.27RNN	
314402			
	FRAME	01	
	IBM CORP.SDD		PH104
DATE	LAST EC	P.N.	1755056 000
11-19-76	316677		

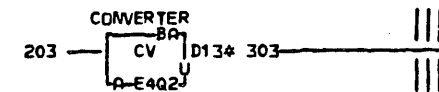
- REQUEST IN TAG TO CHIF—PC102AE6— 2—  
 - SELECT OUT OUTBOUND TO CHIF—PC102AJ5— 12—  
 - GATE INTF A SEL OUT DRVR RCV—PC106GE2— 22—  
 - ENABLE INTF A—PC106GH2— 32—  
 + STATUS IN FOR CTRL UNIT BUSY—PH106CE2— 42—  
 - TAG DELAY GATE—PH106GR6— 52—  
 - GATE HARDSTOP TO INTF DIS—PH107CE6— 62—  
 - ALLOW CHNLS ONLINE REPOWER—PR104DF6— 72—  
 - STATUS IN DIT—PR104FJ2— 82—

EDGE CONN.  
 210 A-E4VSD06  
 217 A-E4VSD11  
 303 A-E4V4D05

PH105  
 000

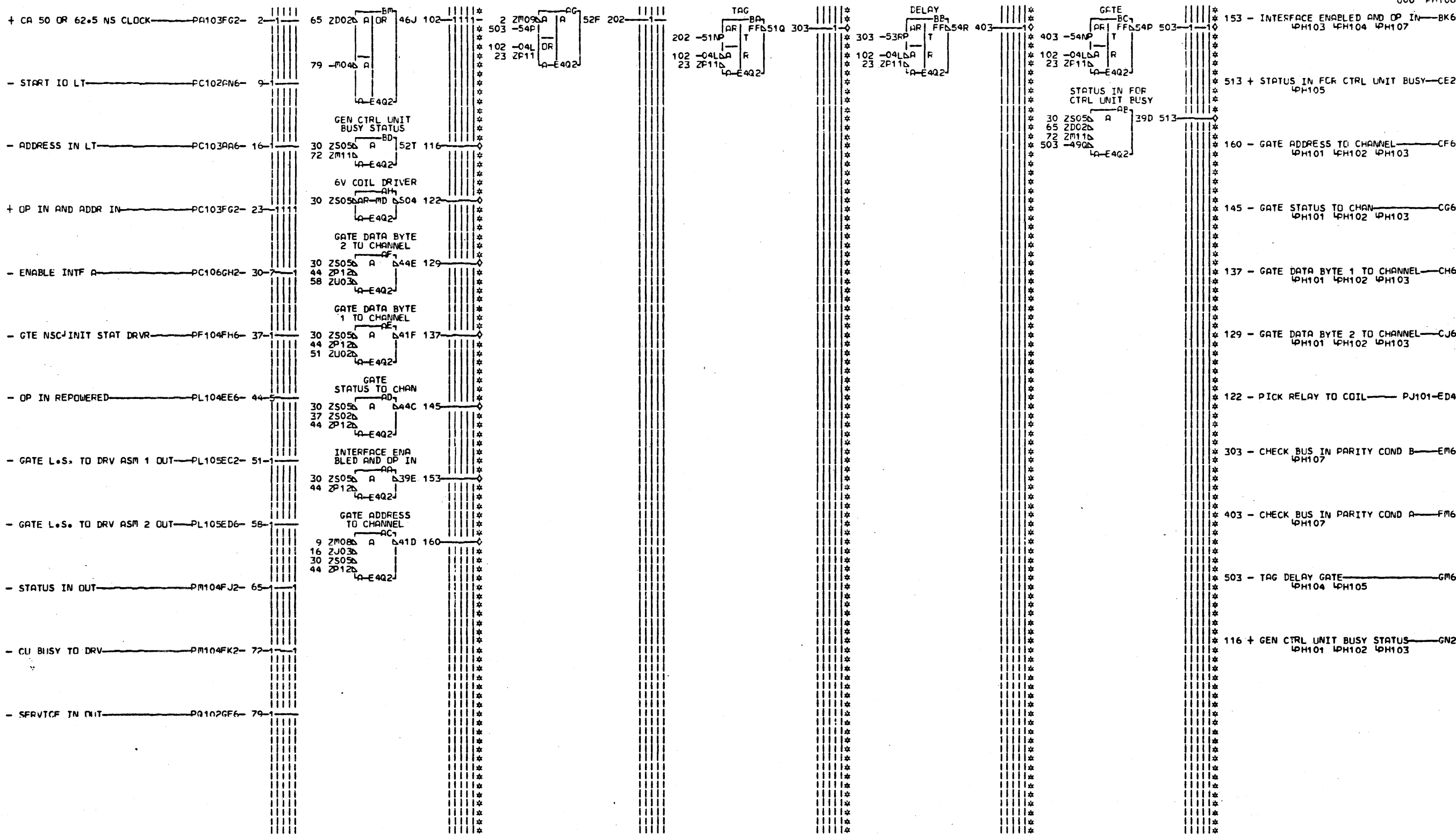


LOC. TYPE  
 A-E4Q2 CE27



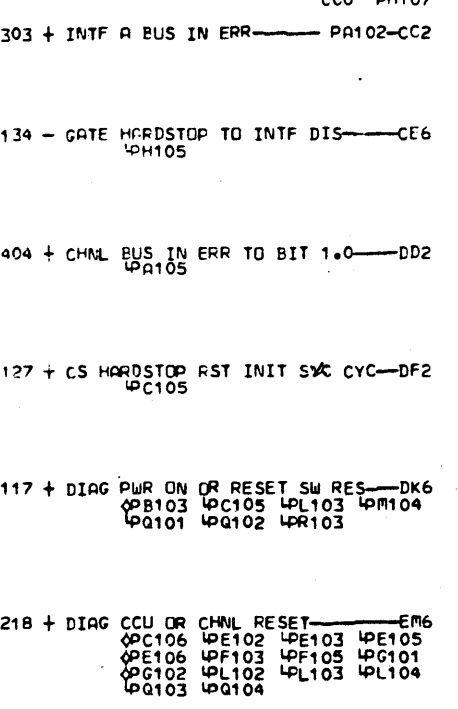
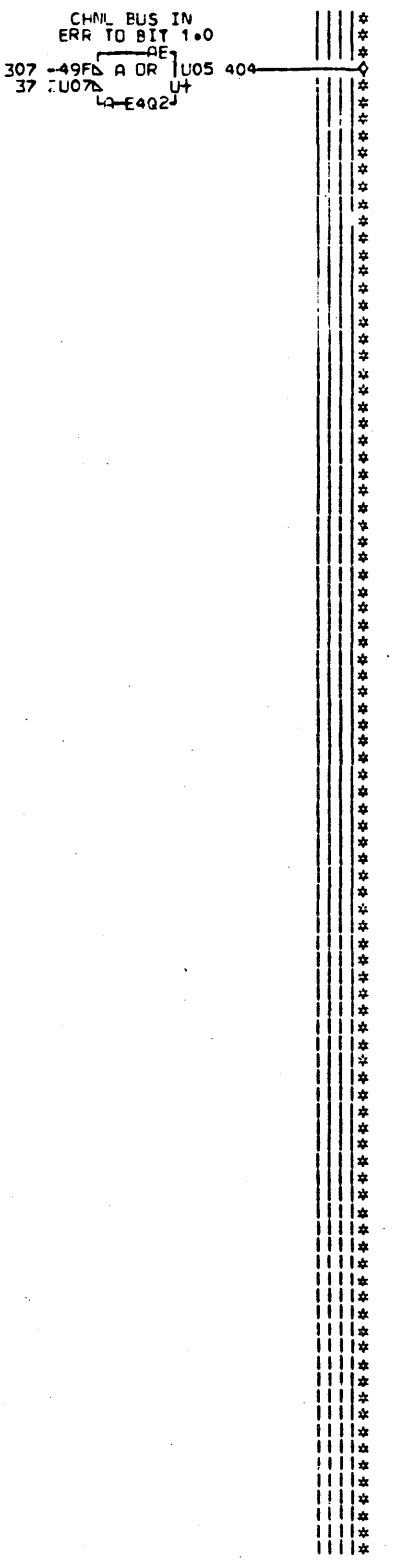
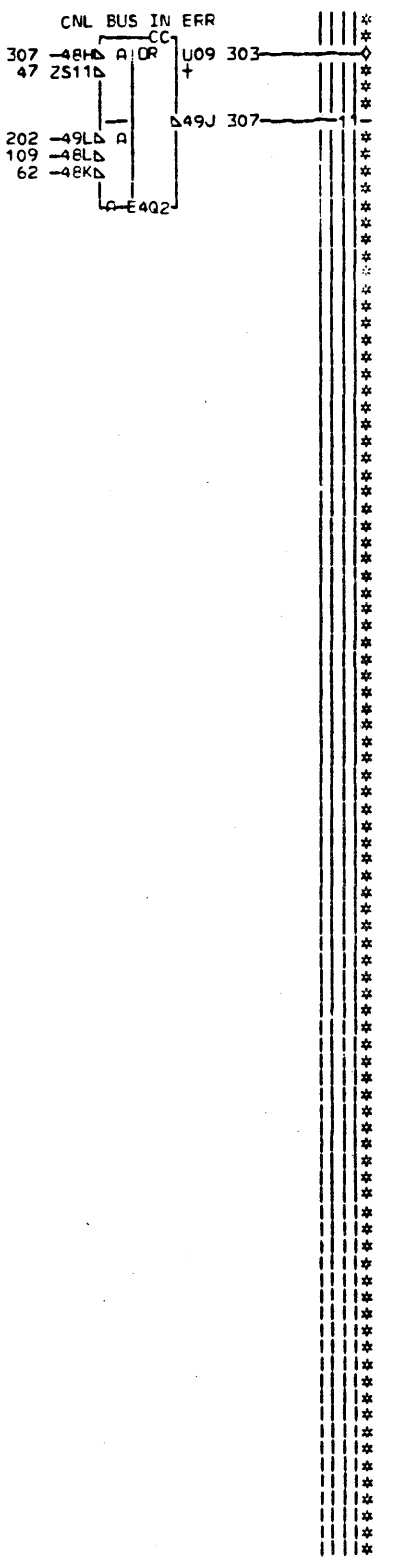
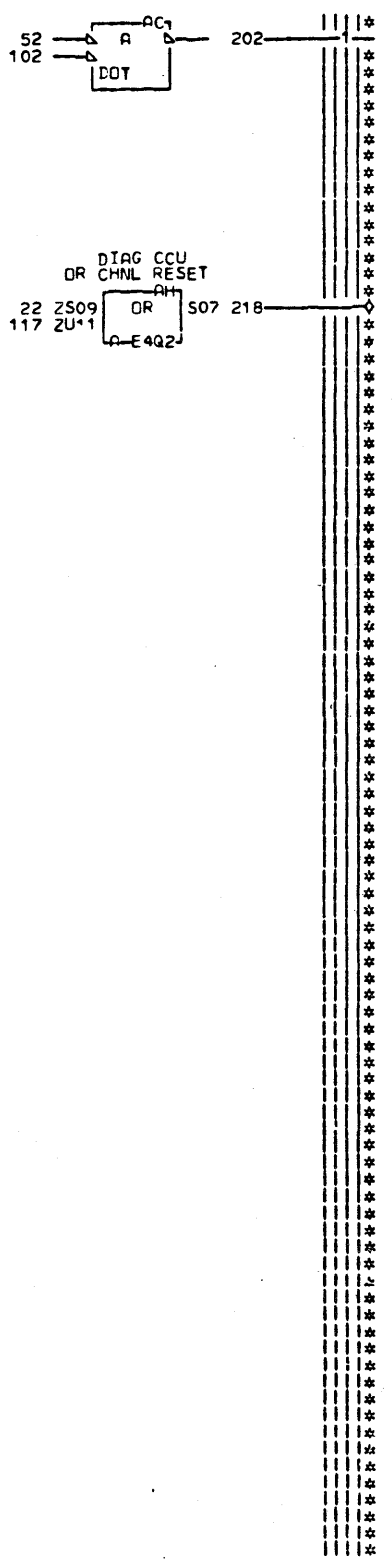
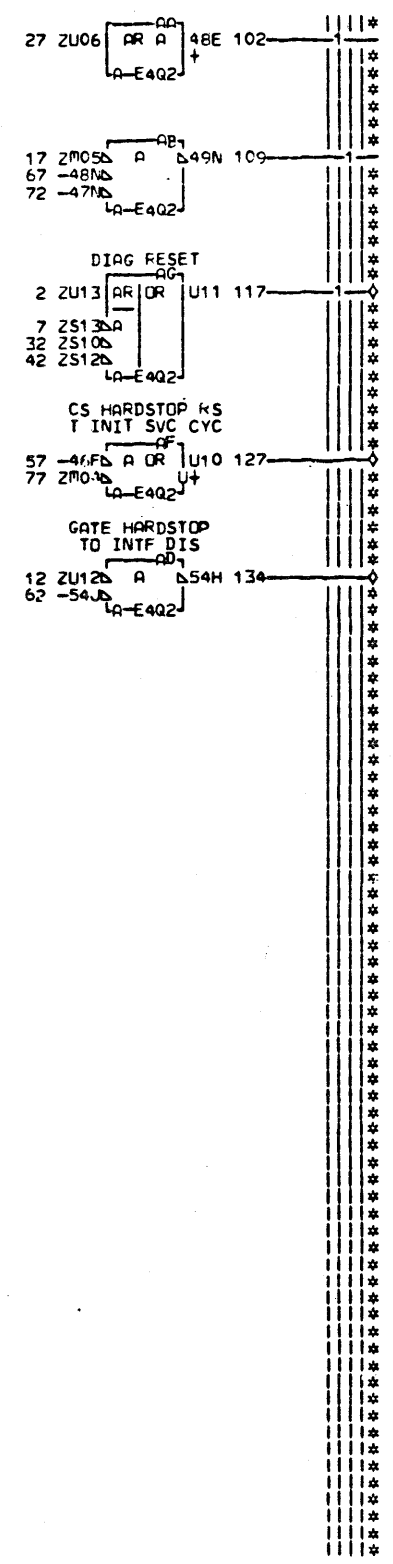
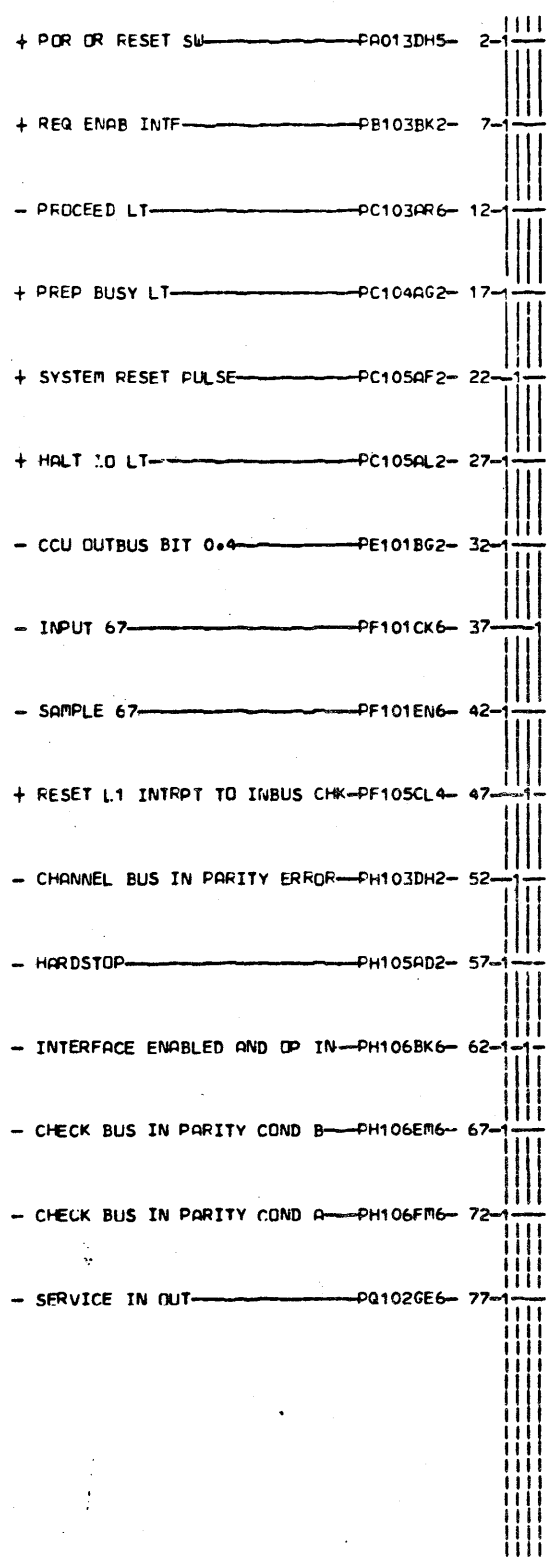
000 PH105  
 PH07-AD2  
 131 - HARDSTOP  
 217 + NPL DISCONNECT IN TO INTF A—DE4  
 PA015  
 224 + NPL SELECT OUT-IN TO INTF A—EC4  
 PJ101  
 303 + NPL STATUS IN TO INTF A PA015-EJ4  
 210 + NPL REQUEST IN TO INTF A—EN4  
 PA015

CHANNEL DRIVERS	
SELECT IN REQ TN AND STATUS IN	
E.C.-HISTORY—MACH.27RNB	
314402	FRAME 01
DATE LAST EC	IBM CORP.SDD PH105
11-19-76 316677	P.No. 1755057 000



LOC. TYPE  
E4Q2 CE27

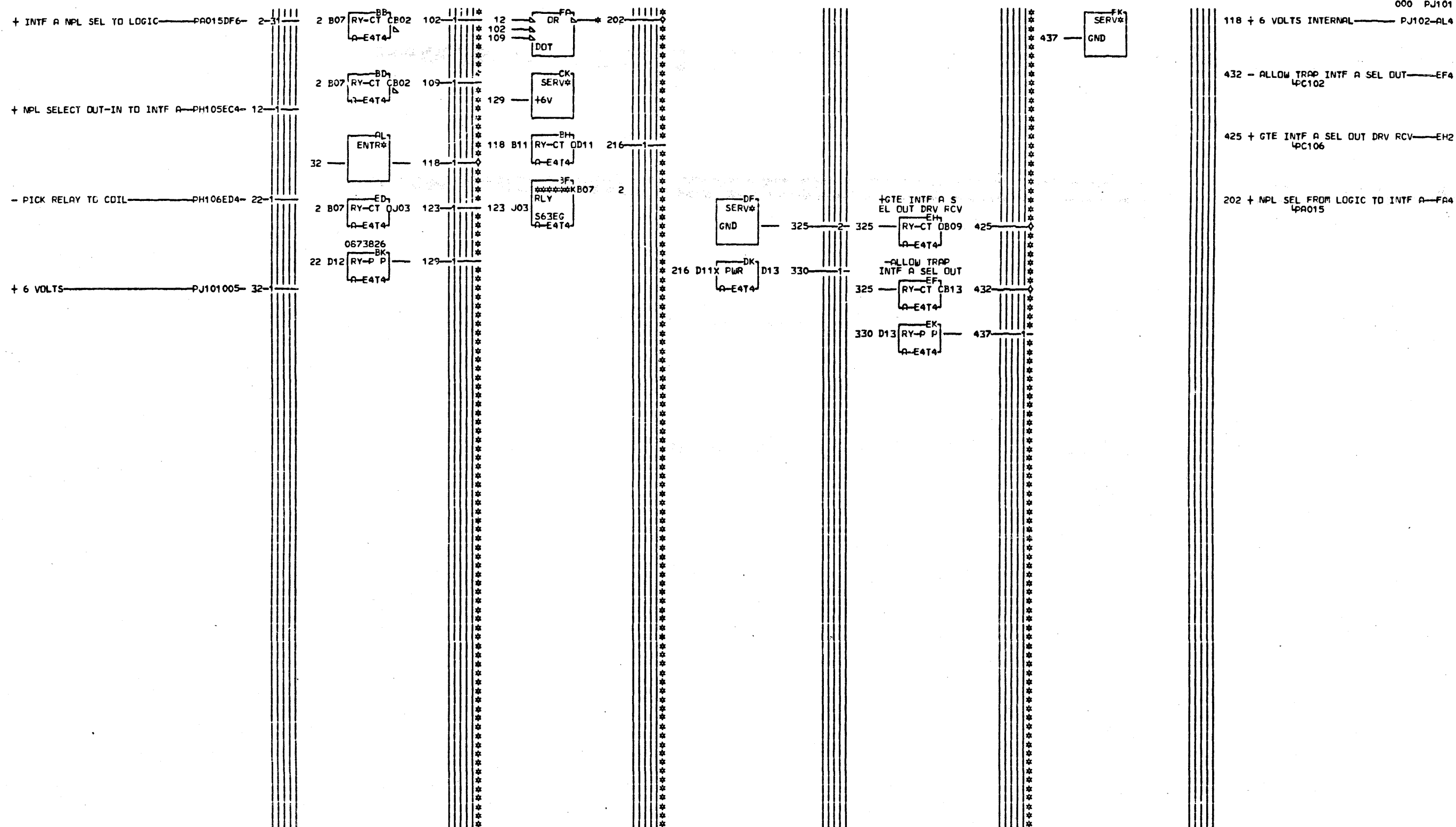
CHANNEL SELECT OUT RELAY DRIVER AND CONTROL GATING		
E.C. HISTORY - MACH. 27RNR		
314402	FRAMF	01
DATE	LAST EC	IAM CORP. SDD PH106
11-19-76	316677	P.N. 1755058 000



LOC. TYPE  
A-E4Q2 CE27

PH107  
000

BUS IN ERROR LATCH AND RESET GENERATION	
F.C. HISTORY	E. MACH. 27RNR
314402	FRAME 01
DATE LAST FC	IBM CORP. SDD PH107
11-19-76 316677	P.No. 1755059 000

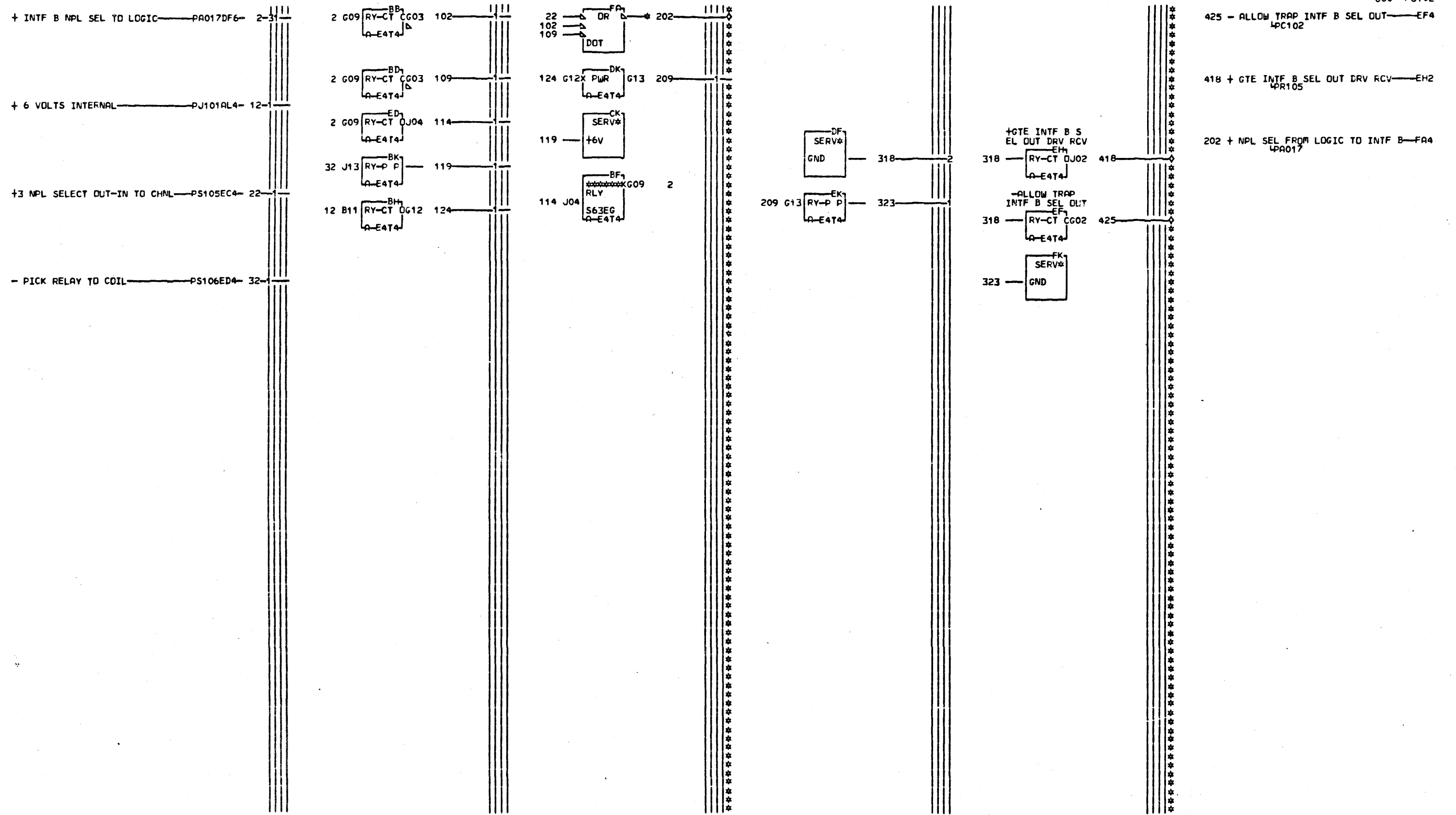


EDGE CONN.  
202 A-E4V4D09  
01T-A1A3D09

LOC. TYPE  
A-E4T4 N869

PJ101  
000

INTF A SELECT OUT RELAYS	
E-C-HISTORY	C-MACH-27RNB
DATE	LAST EC
02-23-76	314402
FRAME	01
IBM CORP. SDD	PJ101
P.N. 1755060	000



EDGE CONN.  
202 A-E4U4D09  
01U-A1A3D09

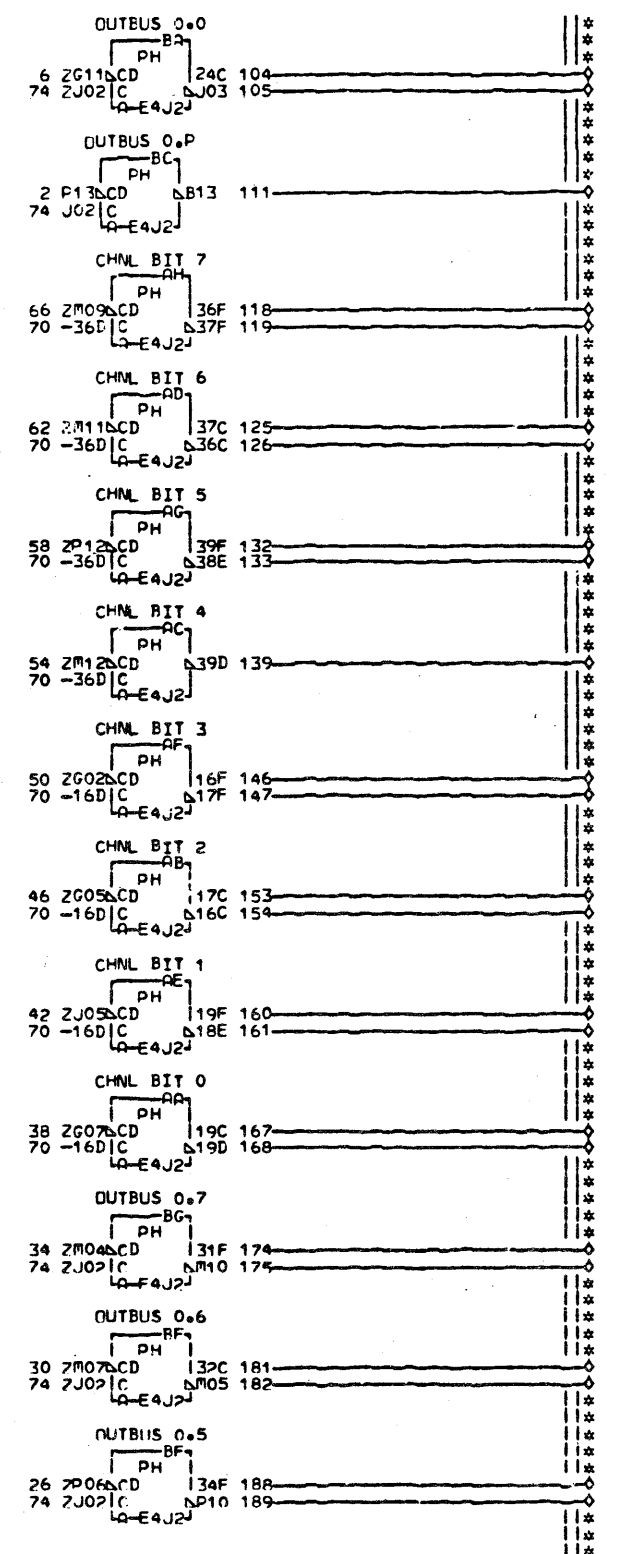
LOC TYPE  
A-E4T4 NB69

PJ102  
000

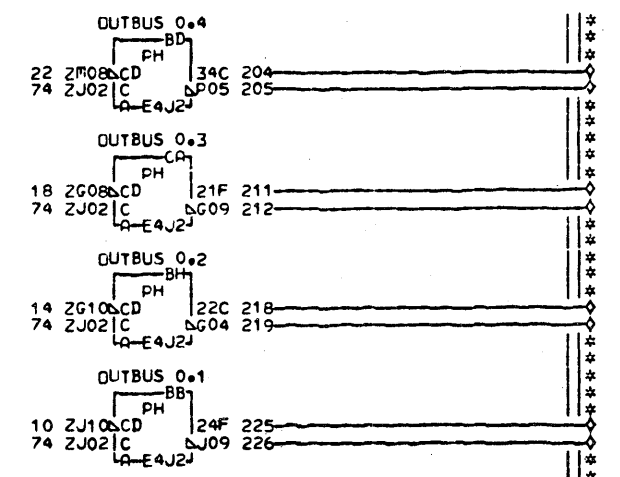
INTF B SELECT OUT RELAYS			
E.C.	HISTORY	C	MACH#27RNB
		FRAME	01
		IBM CURP.#	SDD PJ102
DATE	LAST EC	P.#	1755061 000
02-23-76	314402		



+ OUTBUS BIT 0.0 — PA011DH1 — 2  
 + OUTBUS BIT 0.0 — PA011DH3 — 6  
 + OUTBUS BIT 0.1 — PA011DH5 — 10  
 + OUTBUS BIT 0.2 — PA011DH7 — 14  
 + OUTBUS BIT 0.3 — PA011DJ2 — 18  
 + OUTBUS BIT 0.4 — PA011DJ4 — 22  
 + OUTBUS BIT 0.5 — PA011DJ6 — 26  
 + OUTBUS BIT 0.6 — PA011DK1 — 30  
 + OUTBUS BIT 0.7 — PA011DK3 — 34  
 + CHNL BUS OUT BIT 0 — PB102GB4 — 38  
 + CHNL BUS OUT BIT 1 — PB102GC4 — 42  
 + CHNL BUS OUT BIT 2 — PB102GE4 — 46  
 + CHNL BUS OUT BIT 3 — PB102GF4 — 50  
 + CHNL BUS OUT BIT 4 — PB102GH4 — 54  
 + CHNL BUS OUT BIT 5 — PB102GJ4 — 58  
 + CHNL BUS OUT BIT 6 — PB102GL4 — 62  
 + CHNL BUS OUT BIT 7 — PB102GM4 — 66  
 + TIE LP — PK106GH4 — 70  
 + SET CCI OUTBUS REG — PL103CF6 — 74



LOC. TYPE  
 A-E4J2 AC05

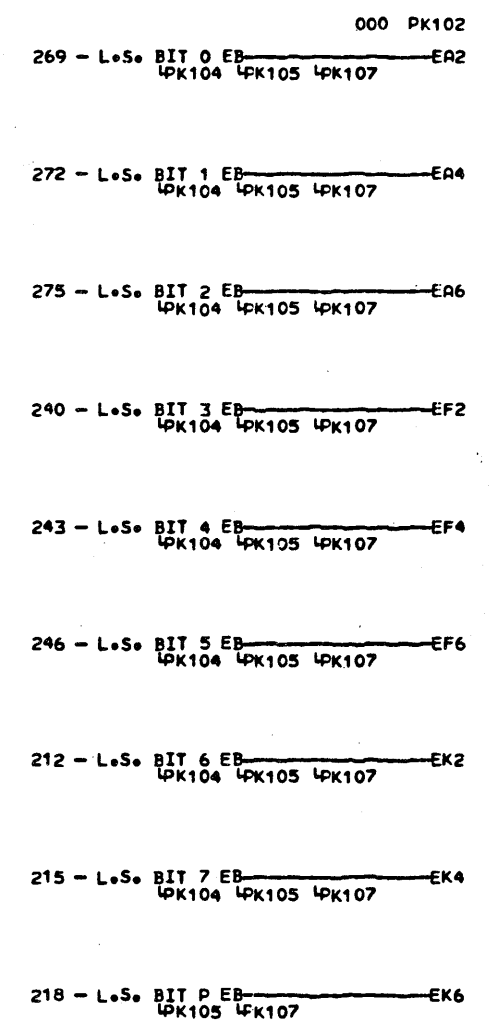
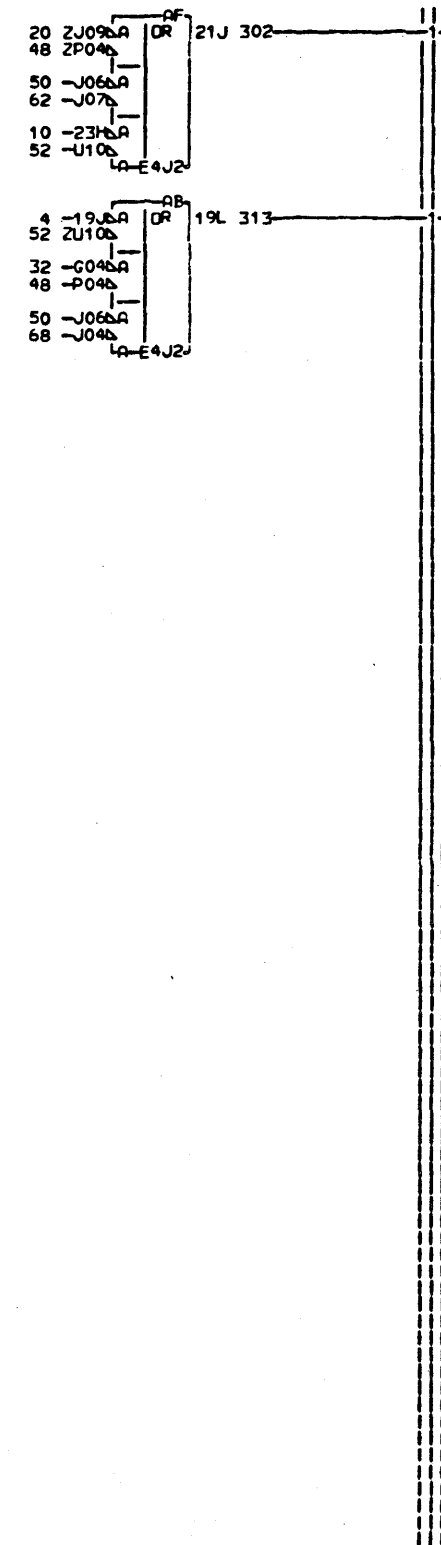
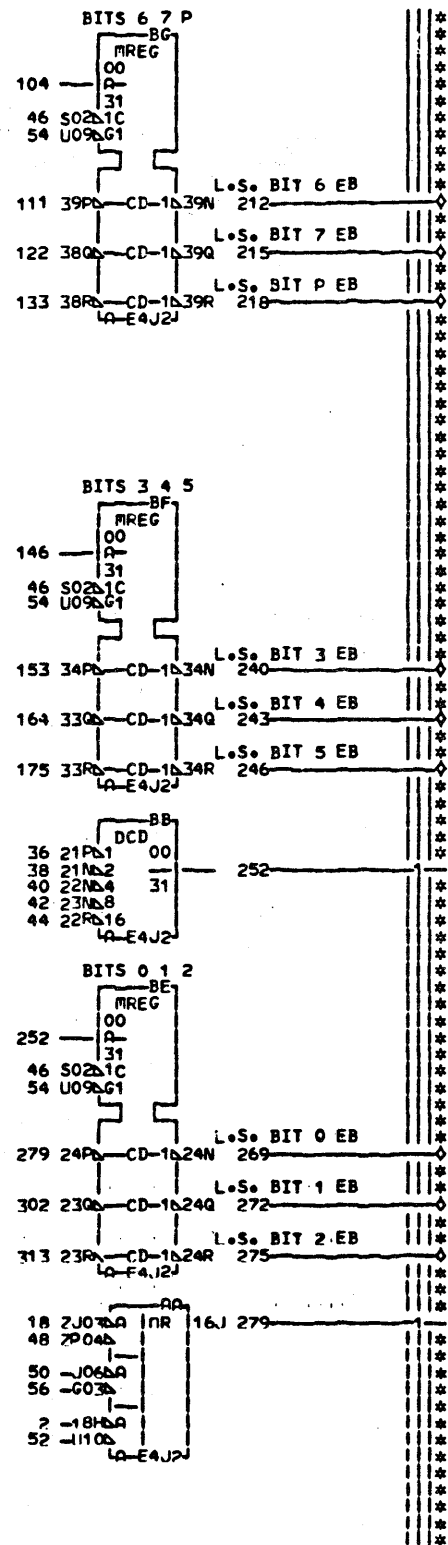
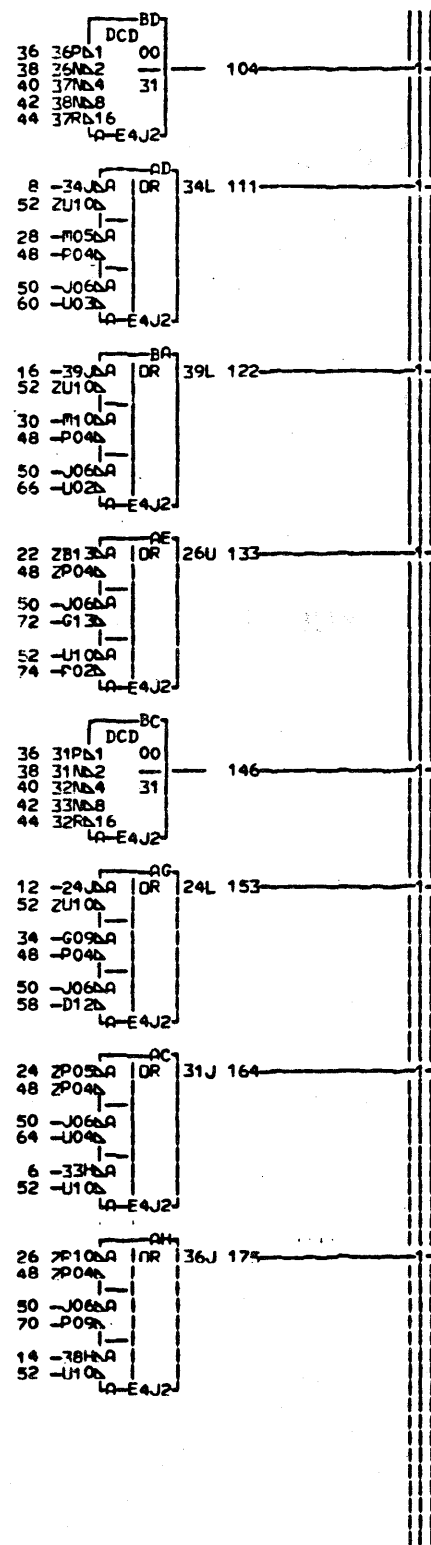
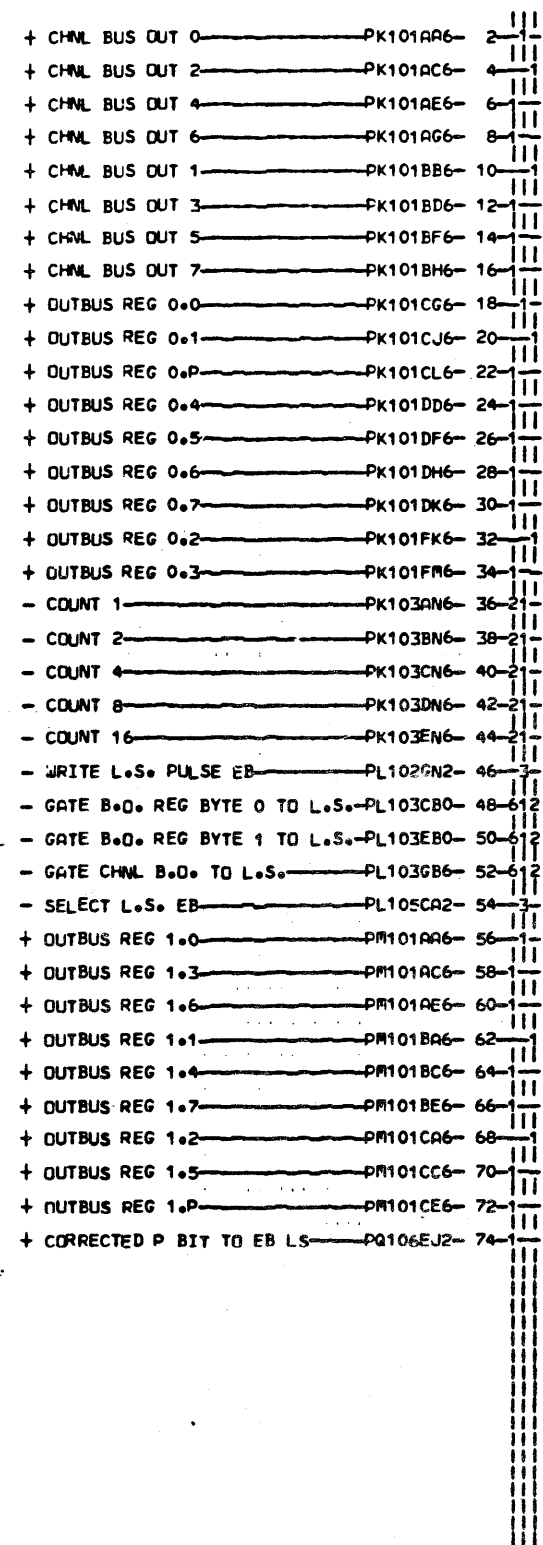


000 PK101

167 - CHNL BUS OUT 0 — PK105-AA2  
 168 + CHNL BUS OUT 0 — PK102-AA6  
 153 - CHNL BUS OUT 2 — PK105-AC2  
 154 + CHNL BUS OUT 2 — AC6  
 139 + CHNL BUS OUT 4 — AE6  
 125 - CHNL BUS OUT 6 — PK105-AG2  
 126 + CHNL BUS OUT 6 — PK102-AG6  
 160 - CHNL BUS OUT 1 — PK105-BB2  
 161 + CHNL BUS OUT 1 — BB6  
 146 - CHNL BUS OUT 3 — PK105-BD2  
 147 + CHNL BUS OUT 3 — BD6  
 132 - CHNL BUS OUT 5 — PK105-BF2  
 133 + CHNL BUS OUT 5 — BF6  
 118 - CHNL BUS OUT 7 — PK105-BH2  
 119 + CHNL BUS OUT 7 — PK102-BH6  
 104 - OUTBUS REG 0.0 — PK105-CG2  
 105 + OUTBUS REG 0.0 — CG6  
 225 - OUTBUS REG 0.1 — PK105-CJ2  
 226 + OUTBUS REG 0.1 — CJ6  
 111 + OUTBUS REG 0.P — CL6  
 204 - OUTBUS REG 0.4 — PK105-DD2  
 205 + OUTBUS REG 0.4 — DD6  
 188 - OUTBUS REG 0.5 — PK105-DF2  
 189 + OUTBUS REG 0.5 — DF6  
 181 - OUTBUS REG 0.6 — PK105-DH2  
 182 + OUTBUS REG 0.6 — DH6  
 174 - OUTBUS REG 0.7 — PK105-DK2  
 175 + OUTBUS REG 0.7 — DK6  
 218 - OUTBUS REG 0.2 — PK105-FK2  
 219 + OUTBUS REG 0.2 — FK6  
 211 - OUTBUS REG 0.3 — PK105-FM2  
 212 + OUTBUS REG 0.3 — FM6

PK101  
 000

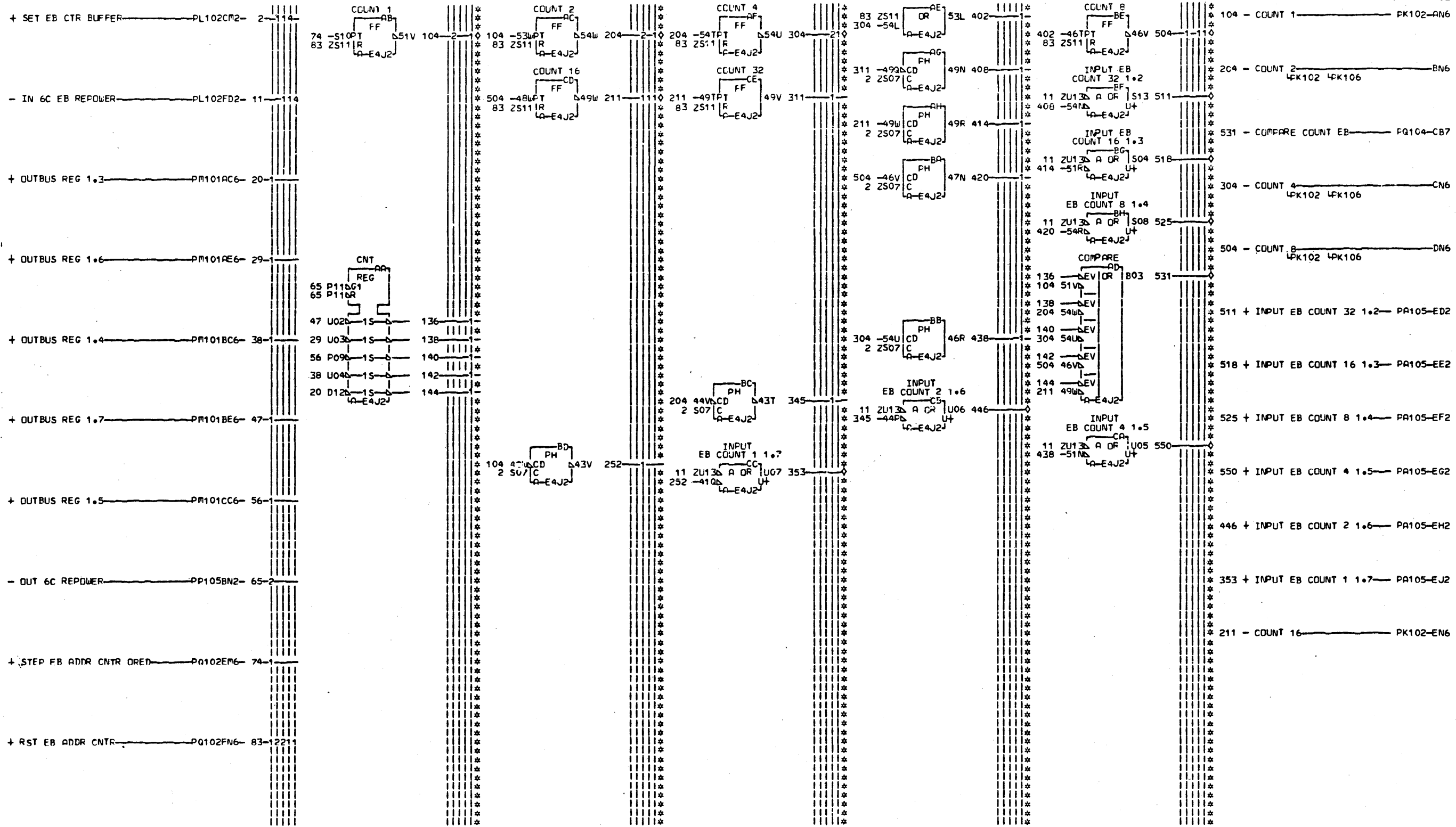
CCU OUTBUS REG AND CHANNEL BUS OUT REPAIRING			
F.C. HISTORY		F. MACH. 27RNR	
314402		FRAME	01
314424		IBN CORP. SCD	PK101
DATE	LAST EC	P.No.	1755062
11-19-76	316677		000



PK102  
000

LOC. TYPE  
A-E4J2 AC05

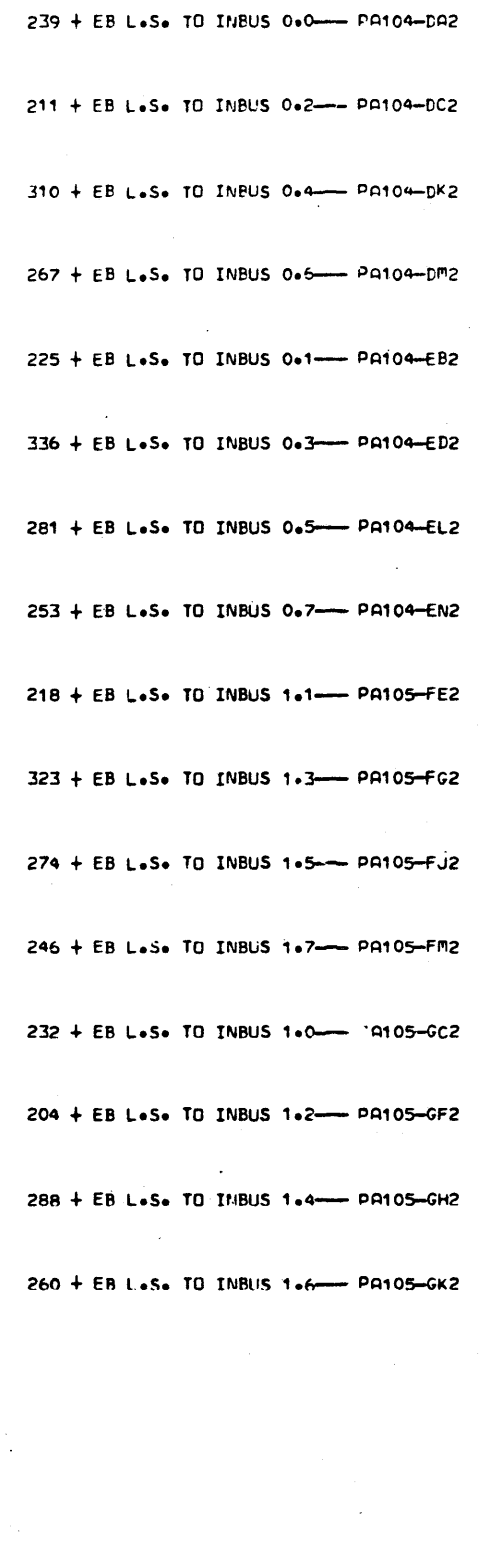
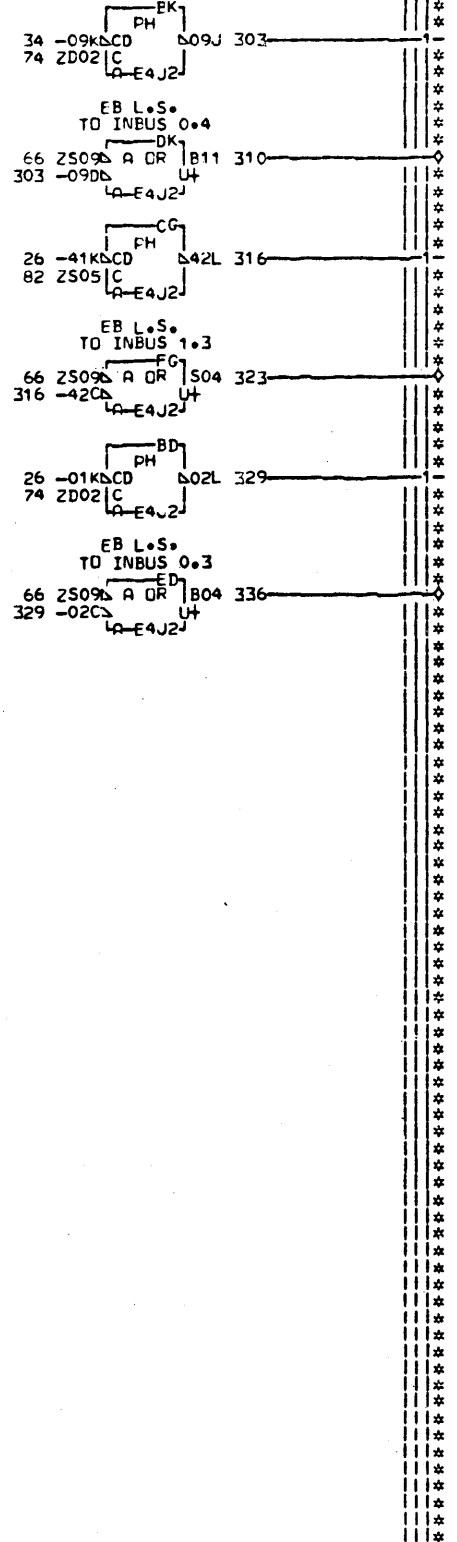
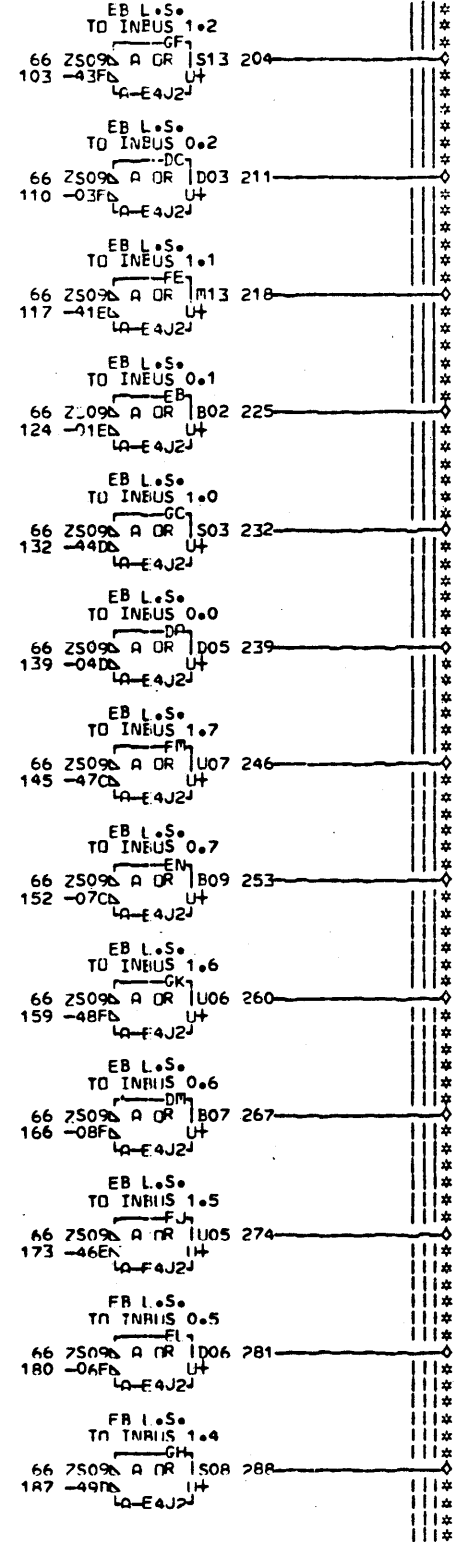
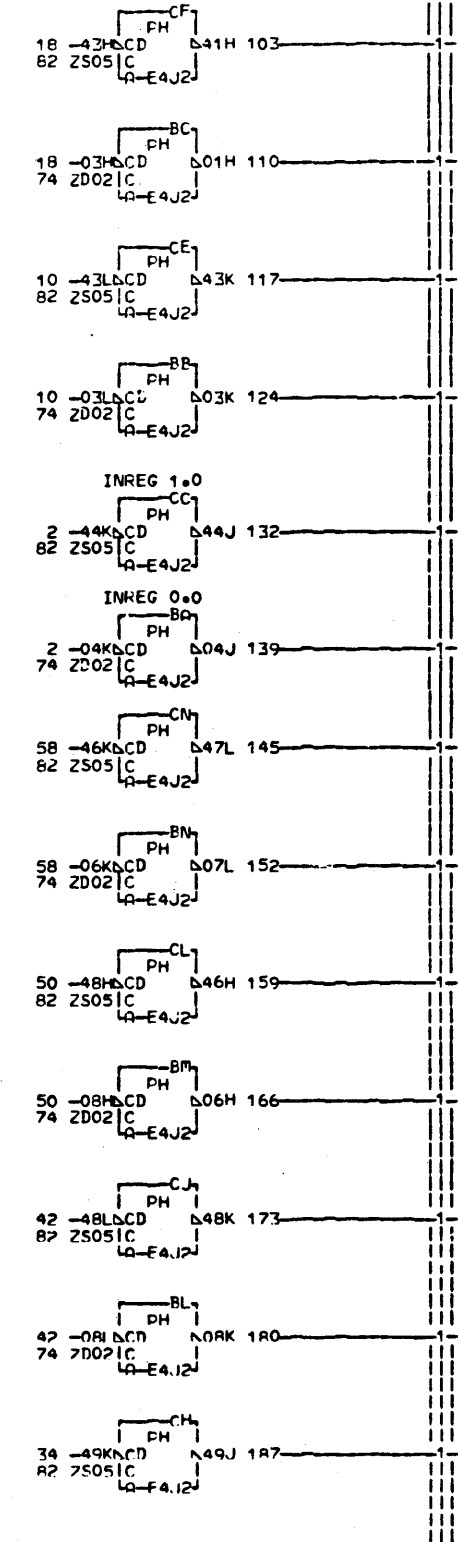
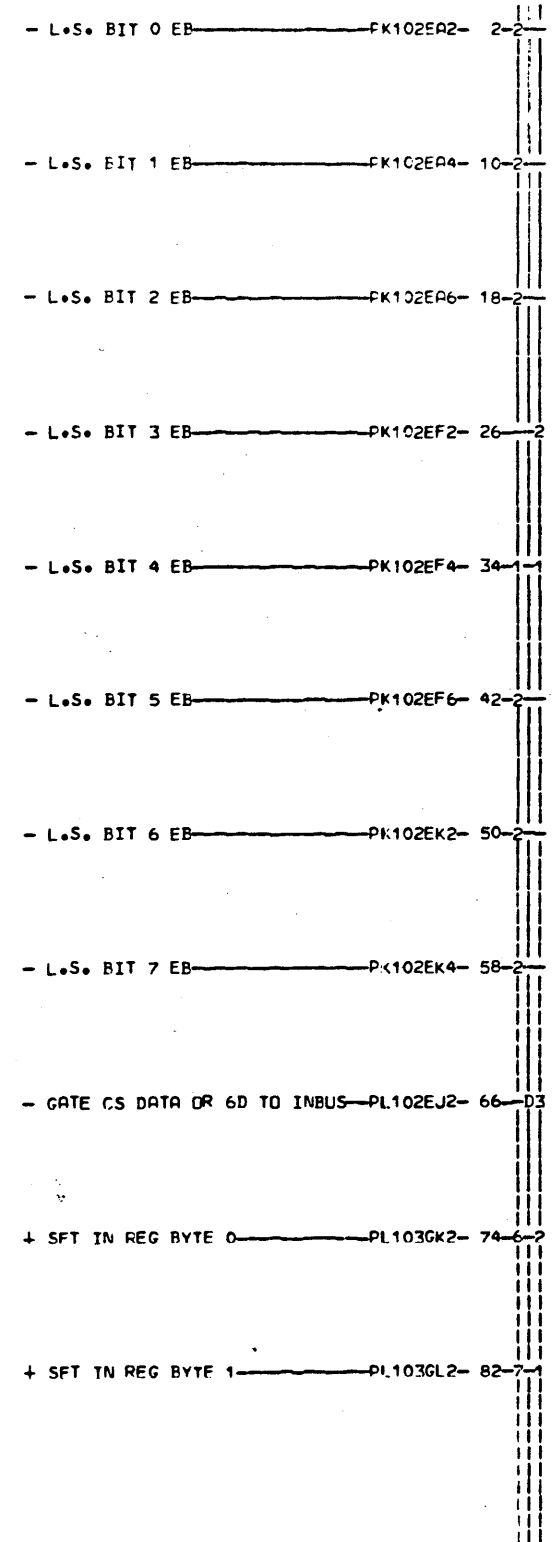
EXTENDED BUFFER LOCAL STORE  
 AND ASSEMBLER  
 -E.C.-HISTORY- MACH.27RNB  
 314402  
 FRAME 01  
 IBM CORP.SCD PK102  
 DATE LAST EC P.N. 1755063 000  
 11-19-76 316677



PK103  
000

LOC. TYPE  
A-E4J2 AC05

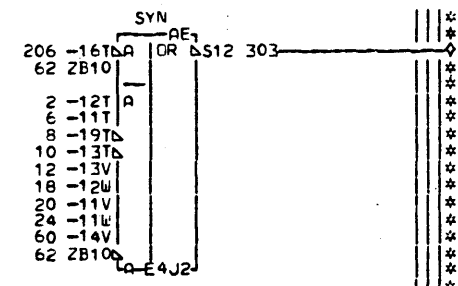
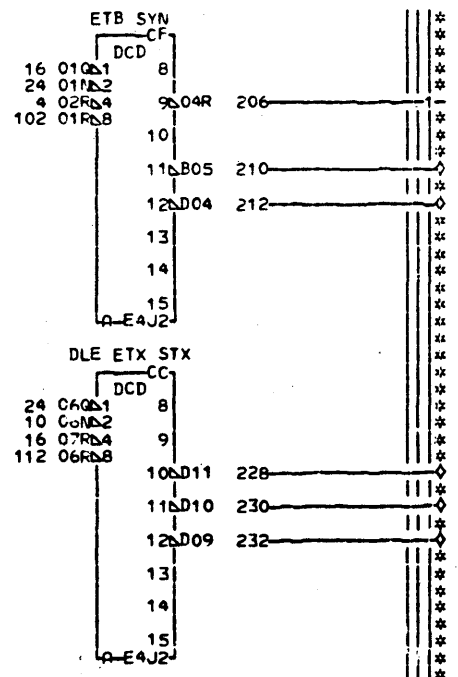
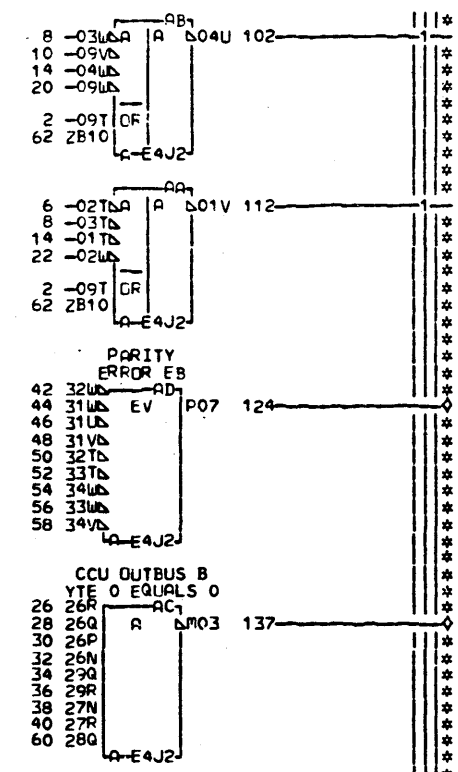
LOCAL STORE ADDRESS COUNTER AND COUNT CONTROL	
E.C. HISTORY	D. MACH. # 27FNR
314402	
DATE LAST EC	FRAME 01
11-19-76 316677	IRM CORP. SCD PK103
	P. N. 1755064 000



INC. TYPE A-E4J2 AC05

EXTENDED BUFFER L.S. TO INBUS	
F.C. HISTORY	MACH. 27RNB
314407	FRAME 01
DATE LAST EC	IBM CORP. SCD PK104
11-19-76 316677	P.N. 1755065 000

- CHNL BUS OUT 0 — PK101AA2- 2-2-1  
 - CHNL BUS OUT 2 — PK101AC2- 4-1-1  
 + CHNL BUS OUT 2 — PK101AC6- 6-1-1  
 + CHNL BUS OUT 4 — PK101AE6- 8-2-1  
 - CHNL BUS OUT 6 — PK101AG2- 10-1-1  
 - CHNL BUS OUT 1 — PK101BB2- 12-1-1  
 + CHNL BUS OUT 1 — PK101BB6- 14-2-1  
 - CHNL BUS OUT 3 — PK101BD2- 16-2-1  
 + CHNL BUS OUT 3 — PK101BD6- 18-1-1  
 - CHNL BUS OUT 5 — PK101BF2- 20-1-1  
 + CHNL BUS OUT 5 — PK101BF6- 22-1-1  
 - CHNL BUS OUT 7 — PK101BH2- 24-2-1  
 - OUTBUS REG 0.0 — PK101CG2- 26-1-1  
 - OUTBUS REG 0.1 — PK101CJ2- 28-1-1  
 - OUTBUS REG 0.4 — PK101DD2- 30-1-1  
 - OUTBUS REG 0.5 — PK101DF2- 32-1-1  
 - OUTBUS REG 0.6 — PK101DH2- 34-1-1  
 - OUTBUS REG 0.7 — PK101DK2- 36-1-1  
 - OUTBUS REG 0.2 — PK101FK2- 38-1-1  
 - OUTBUS REG 0.3 — PK101FM2- 40-1-1  
 - L.S. BIT 0 EB — PK102EA2- 42-1-1  
 - L.S. BIT 1 EB — PK102EA4- 44-1-1  
 - L.S. BIT 2 EB — PK102EA6- 46-1-1  
 - L.S. BIT 3 EB — PK102EF2- 48-1-1  
 - L.S. BIT 4 EB — PK102EF4- 50-1-1  
 - L.S. BIT 5 EB — PK102EF6- 52-1-1  
 - L.S. BIT 6 EB — PK102EK2- 54-1-1  
 - L.S. BIT 7 EB — PK102EK4- 56-1-1  
 - L.S. BIT P EB — PK102EK6- 58-1-1  
 + TIE UP — PK106GH4- 60-1-1  
 + USASCII MON MODE — PL101EJ2- 62-2-2



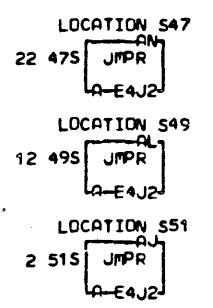
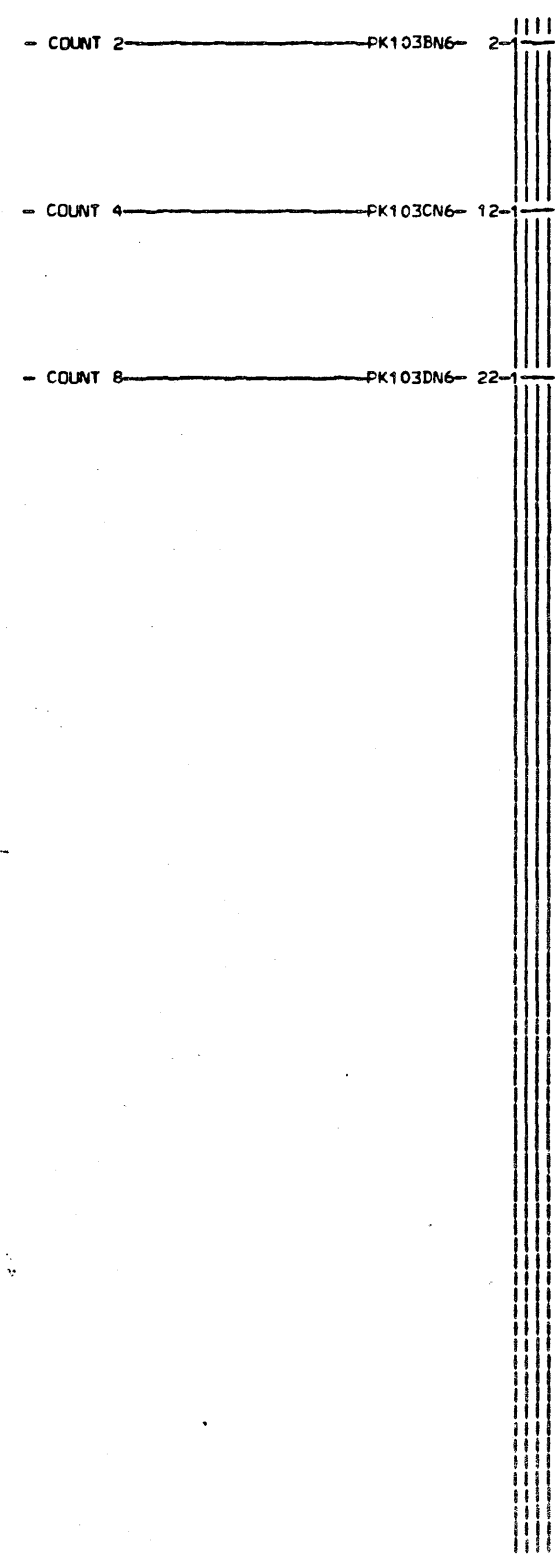
000 PK105

228 - DECODE STX — PL101-CC5  
 230 - DECODE ETX — PL101-CC6  
 232 - DECODE DLE — PL101-CC7  
 210 - DECODE USASCII ETB — PL101-CF6  
 212 - DECODE EBCDIC ETB — PL101-CF7  
 137 - CCU OUTBUS BYTE 0 EQUALS 0 — CK2  
 PM101  
 124 + PARITY ERROR EB — PL103-FM2  
 303 - SYN DECODE — PL105-GB2

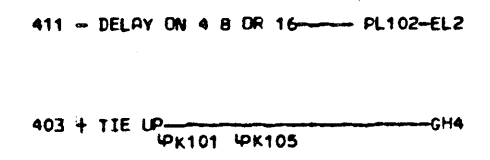
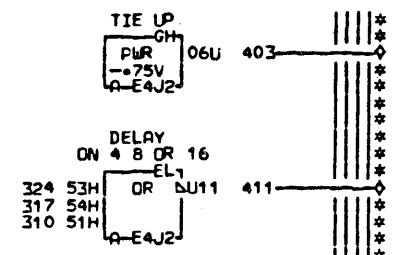
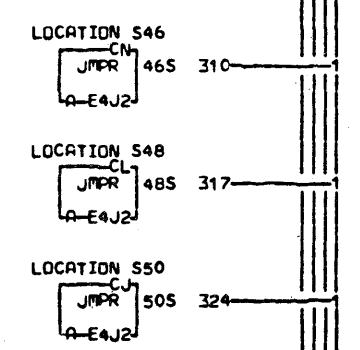
PK105  
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LOC. TYPE  
A-E4J2 AC05

BSC CNTRL CHARACTER RECOG AND  
 DETECT ALL ZERO CCU OUTBUS  
 F.C. HISTORY — MARCH 27 1976  
 314402  
 FRAME 01  
 IBM CORP. SCD PK105  
 DATE LAST FC P.N. 1755066 000  
 11-19-76 316677



-BLANK COLUMN-



NOTE 1 SEE PAGE PA049 NOTES FOR BYTE CHANNEL BURST LENGTH JUMPING

LOC. TYPE  
A-E4J2 AC05

PK106  
000

BURST LENGTH JUMPING			
E.C. HISTORY	D. MACH. 27RNR		
314402		FRAME	01
DATE	LAST FC	IBA CORP. SCD	PK106
11-19-76	316677	P.N. 1755067	000

- L.S. BIT 0 EB — PK102EA2 — 2  
 - L.S. BIT 1 EB — PK102EA4 — 12  
 - L.S. BIT 2 EB — PK102EA6 — 22  
 - L.S. BIT 3 EB — PK102EF2 — 32  
 - L.S. BIT 4 EB — PK102EF4 — 42  
 - L.S. BIT 5 EB — PK102EF6 — 52  
 - L.S. BIT 6 EB — PK102EK2 — 62  
 - L.S. BIT 7 EB — PK102EK4 — 72  
 - L.S. BIT P EB — PK102EK6 — 82

L.S. BIT  
 P EB TO DRV  
 — CN —  
 82 08V AR DR D007 104  
 — E4J2 —  
 L.S. BIT  
 7 EB TO DRV  
 — CL —  
 72 27H AR DR DP03 111  
 — E4J2 —  
 L.S. BIT  
 6 EB TO DRV  
 — BL —  
 62 26J AR DR DG12 118  
 — E4J2 —  
 L.S. BIT  
 5 EB TO DRV  
 — CJ —  
 52 27L AR DR DJ11 125  
 — E4J2 —  
 L.S. BIT  
 4 EB TO DRV  
 — BJ —  
 42 29K AR DR DM13 132  
 — E4J2 —  
 L.S. BIT  
 3 EB TO DRV  
 — CG —  
 32 27C AR DR DN02 139  
 — E4J2 —  
 L.S. BIT  
 2 EB TO DRV  
 — BG —  
 22 26D AR DR DJ13 146  
 — E4J2 —  
 L.S. BIT  
 1 EB TO DRV  
 — CE —  
 12 27F AR DR DJ12 153  
 — E4J2 —  
 L.S. BIT  
 0 EB TO DRV  
 — BE —  
 2 29E AR DR DB12 160  
 — E4J2 —

000 PK107

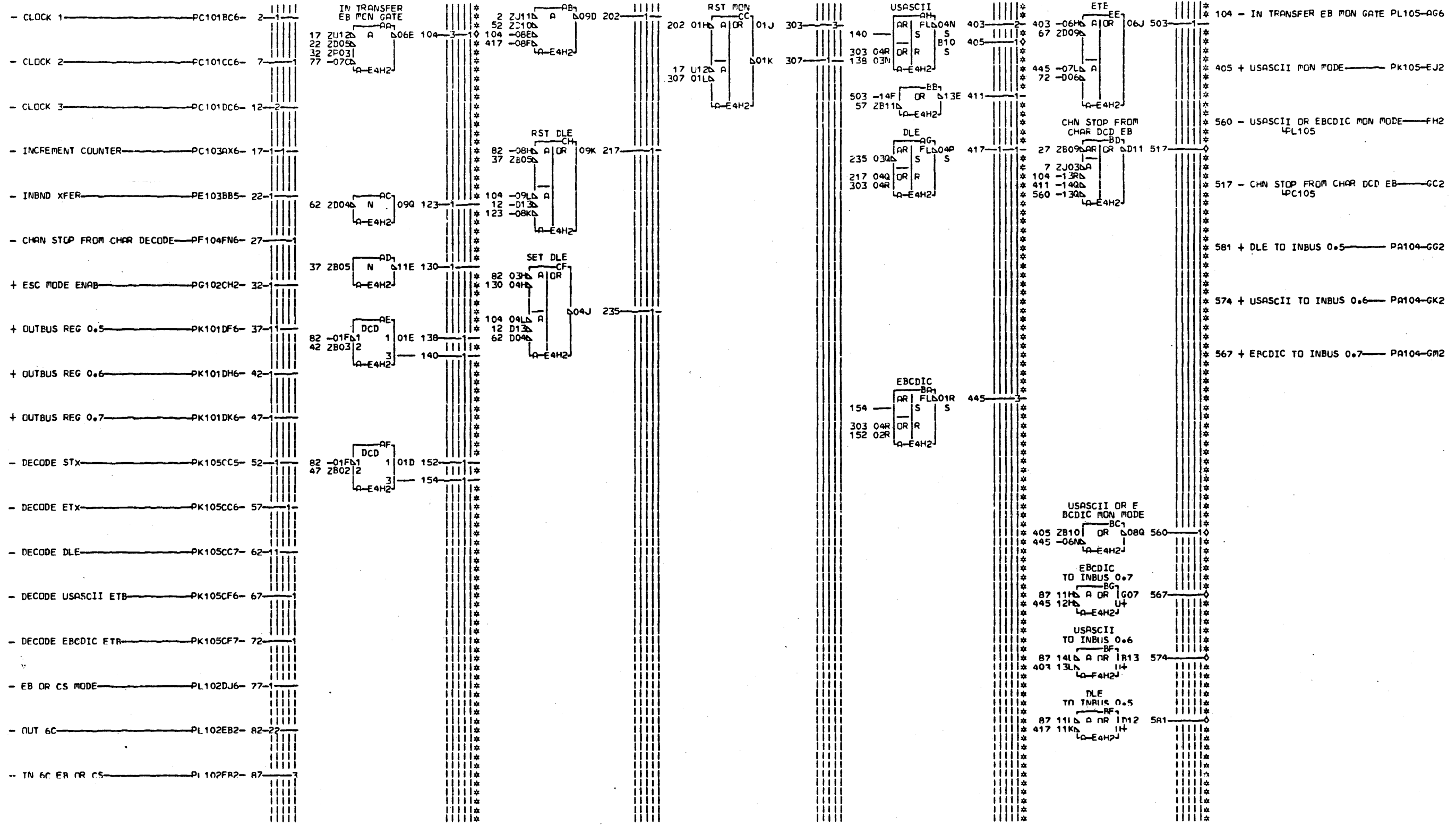
160 - L.S. BIT 0 EB TO DRV — PB106-BE6  
 146 - L.S. BIT 2 EB TO DRV — PB106-BG6  
 132 - L.S. BIT 4 EB TO DRV — PB106-BJ6  
 118 - L.S. BIT 6 EB TO DRV — PB106-BL6  
 153 - L.S. BIT 1 EB TO DRV — PB106-CE6  
 139 - L.S. BIT 3 EB TO DRV — PB106-CG6  
 125 - L.S. BIT 5 EB TO DRV — PB106-CJ6  
 111 - L.S. BIT 7 EB TO DRV — PB106-CL6  
 104 - L.S. BIT P EB TO DRV — PB106-CN6

LDC. TYPE  
A-E4J2 AC05

PK107  
000

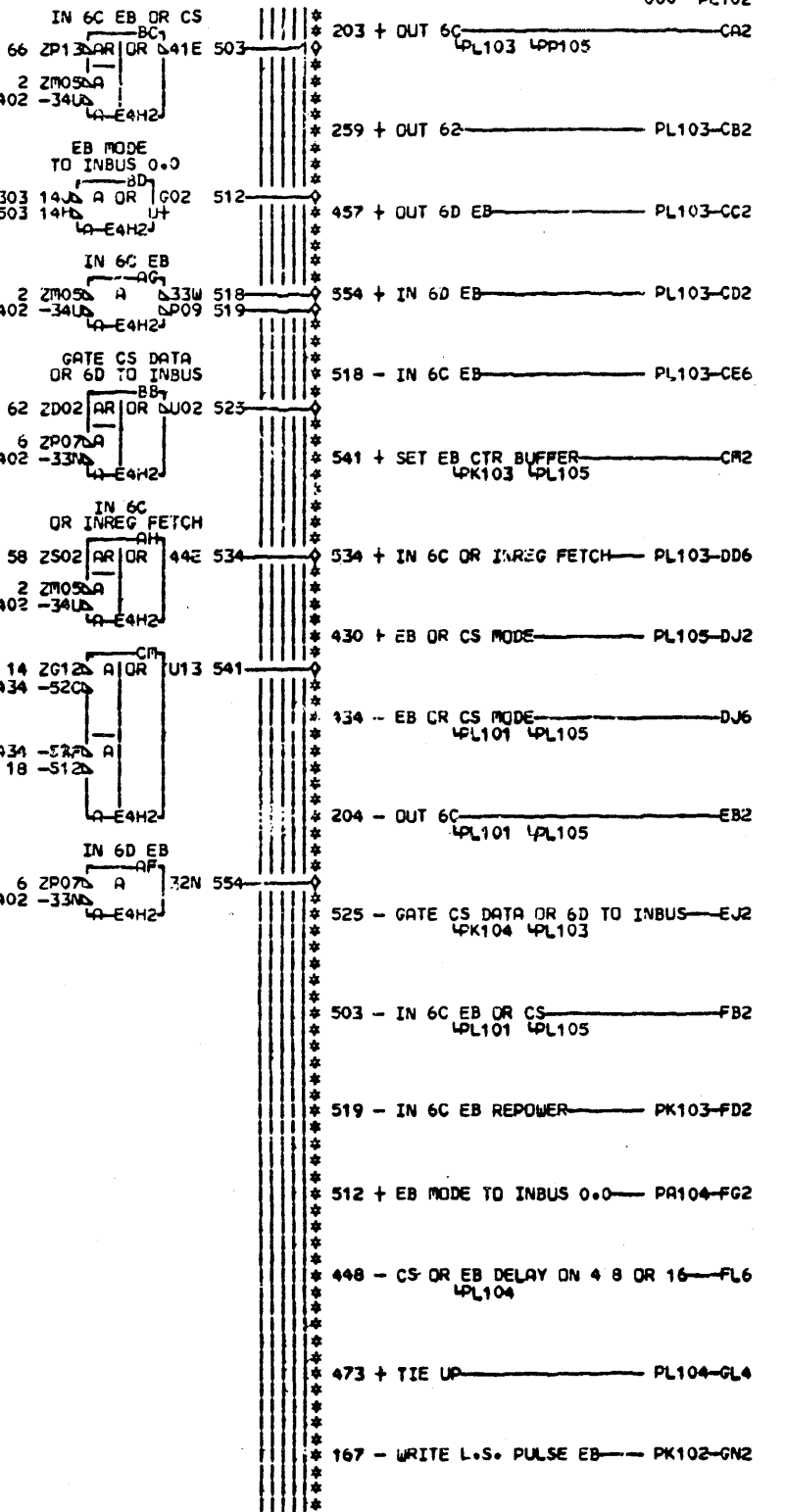
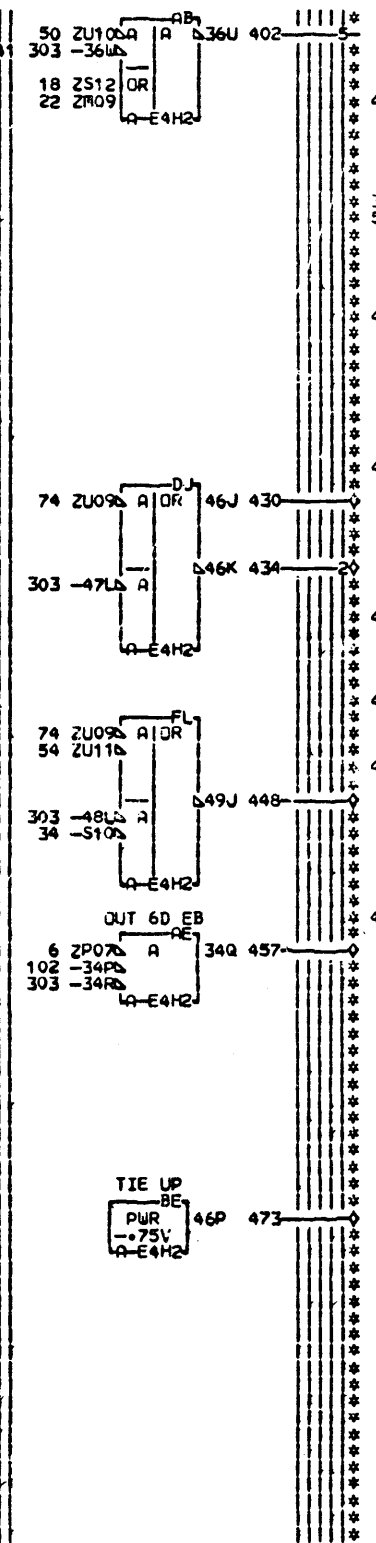
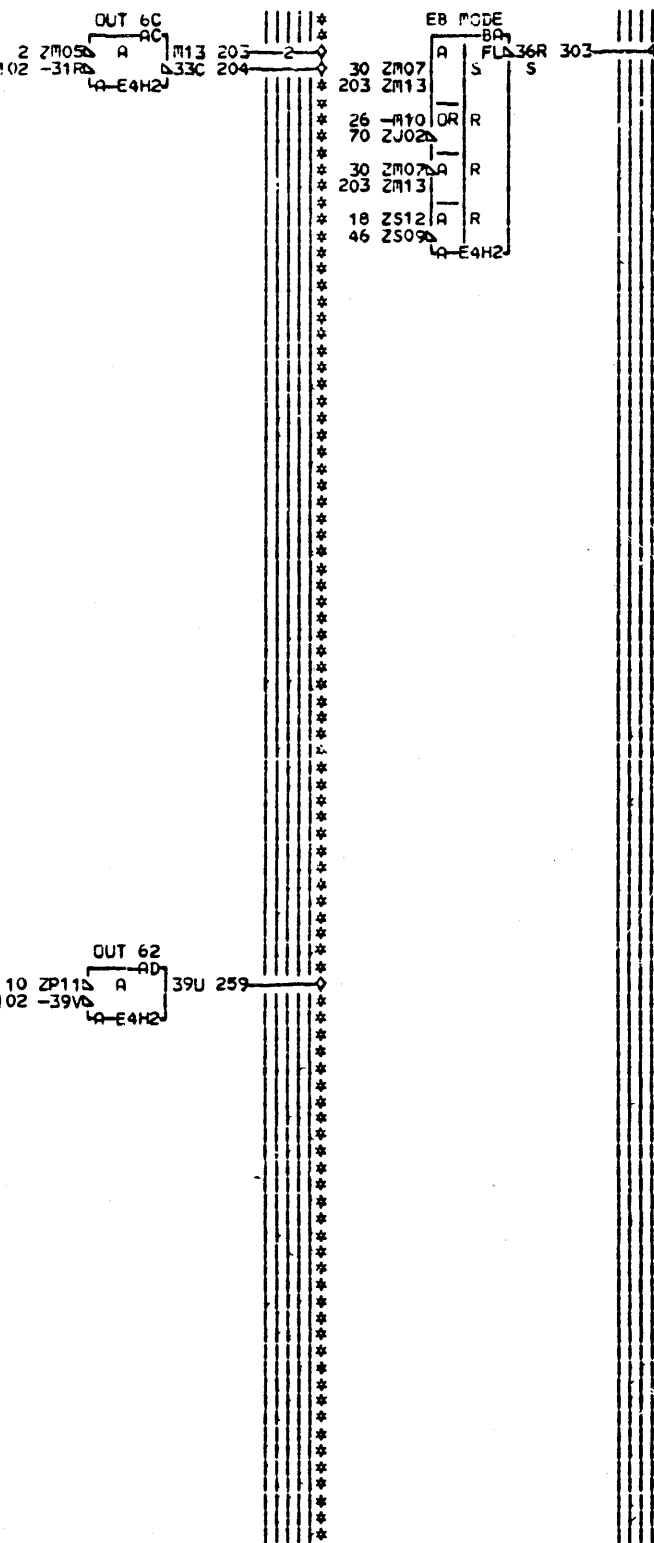
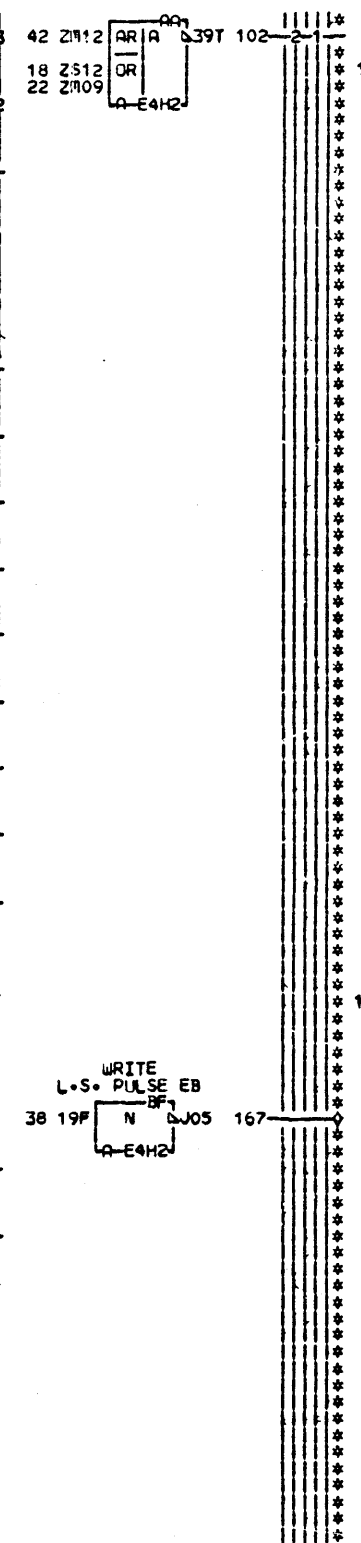
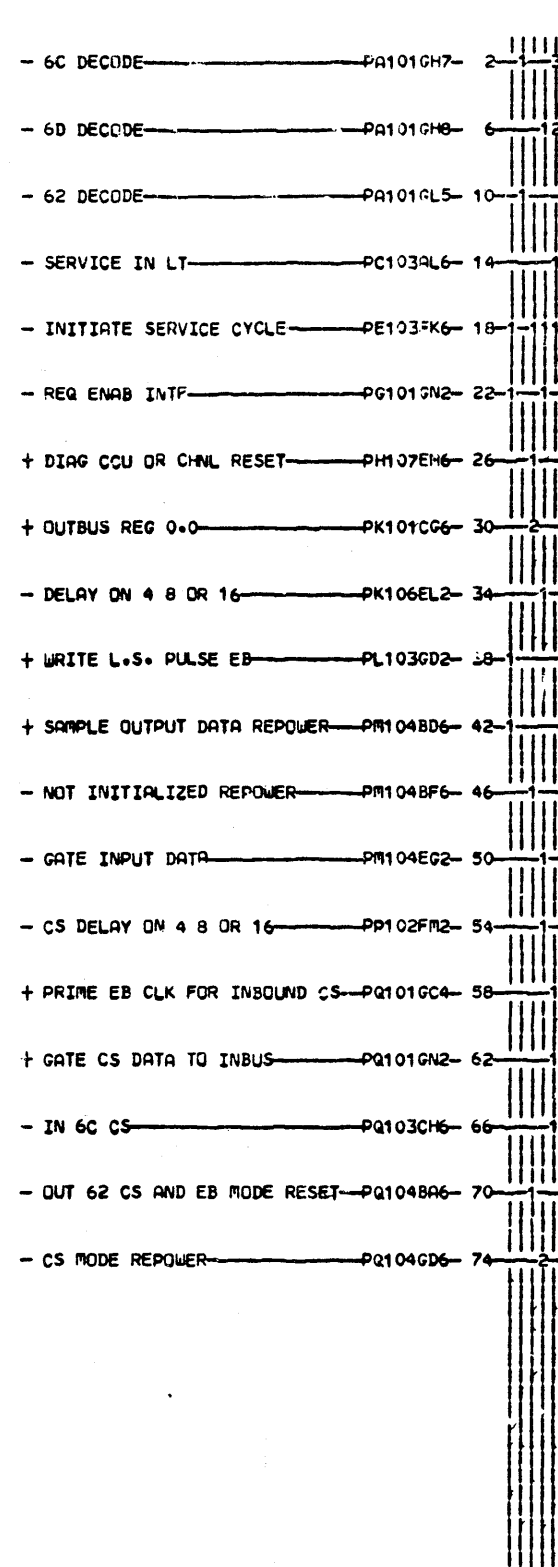
EB L.S. TO DRV	
F.C. HISTORY 314402	D. MACH. 27RNB FRAME 01
DATE LAST EC 11-19-76 316677	IBR CORP. SCD P.N. 1755068

PK107  
000



LOC. TYPE  
A-E4H2 CE28

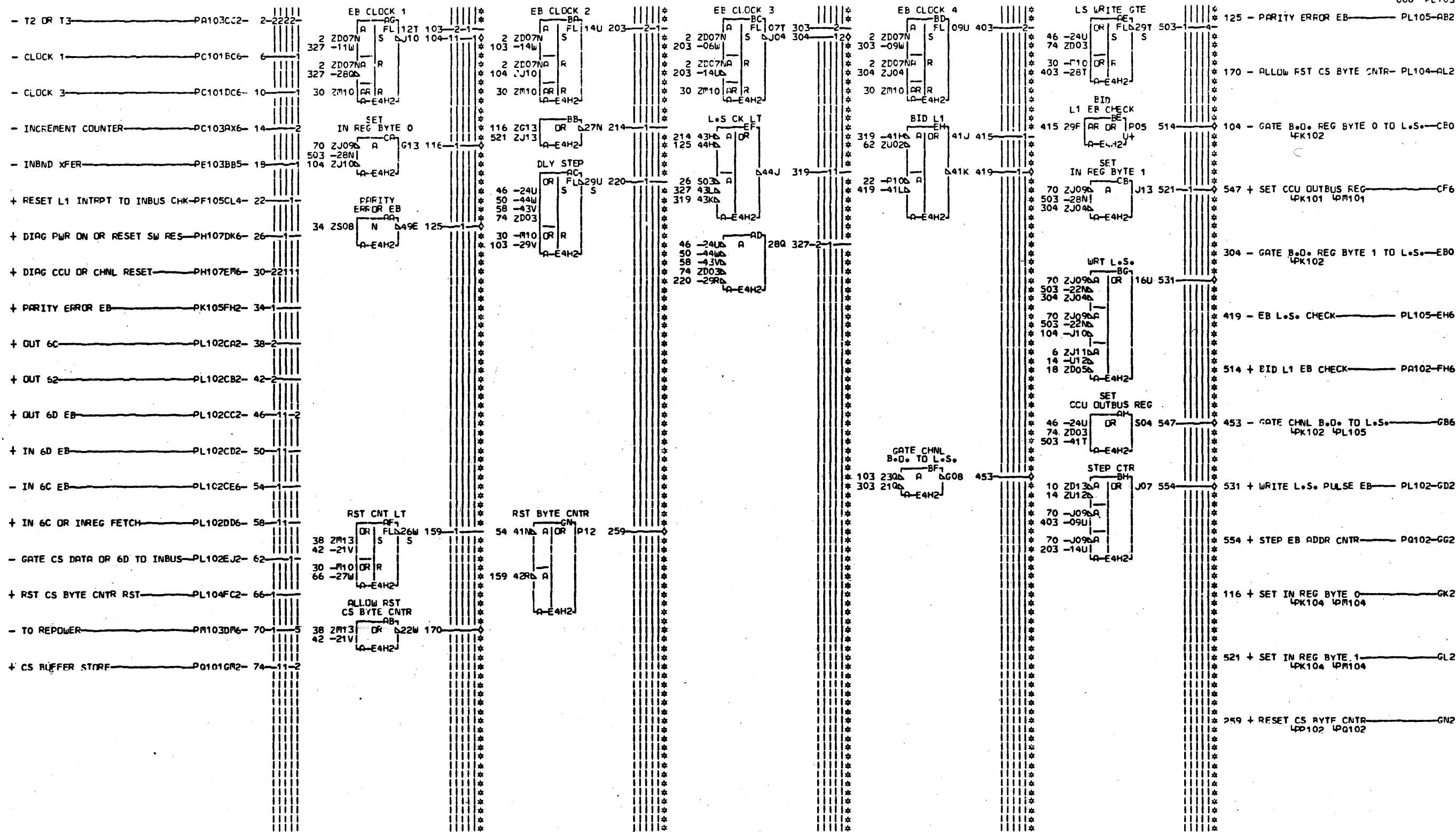




PL102  
000

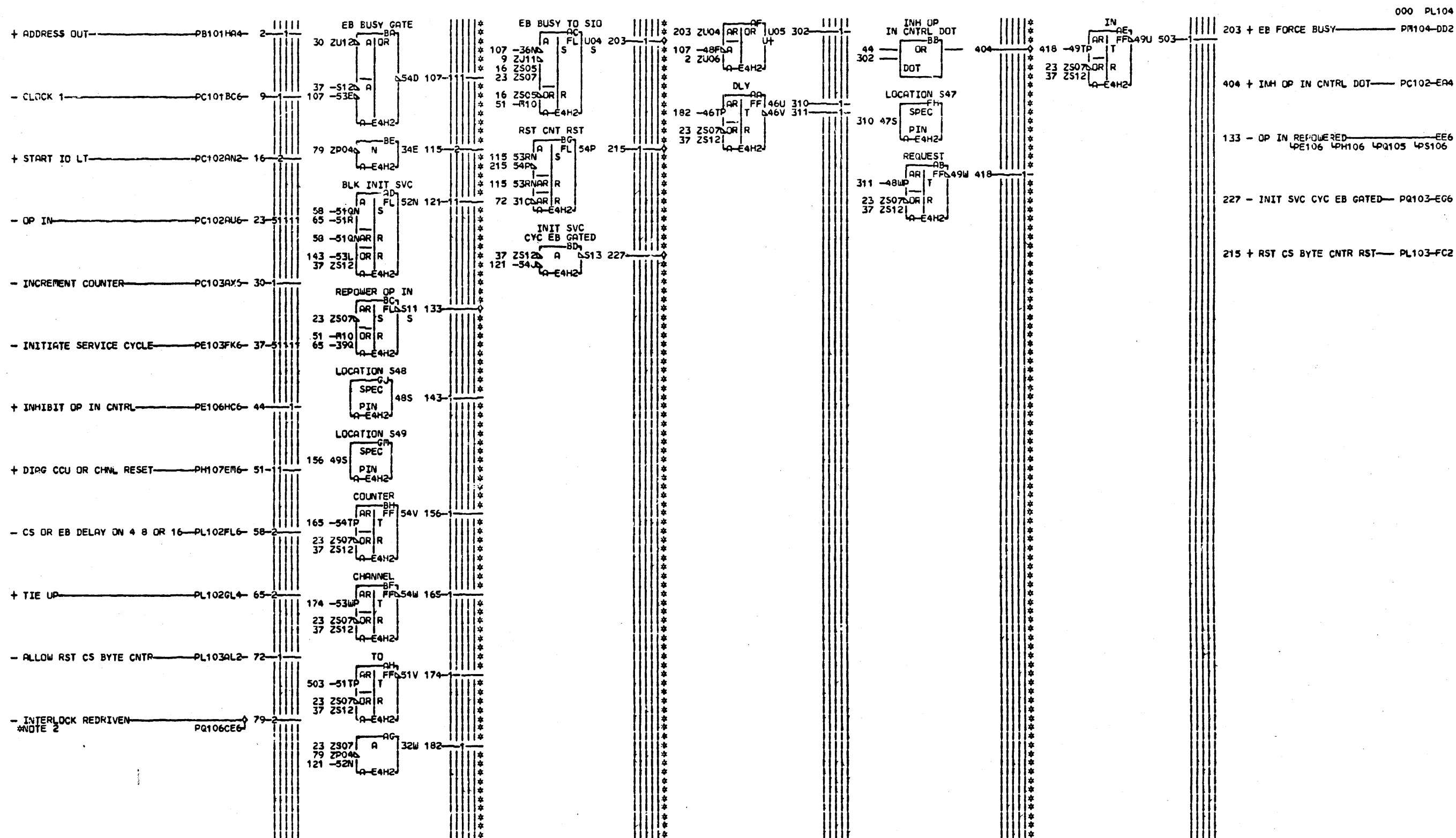
LOC. TYPE  
E4H2 CE28

INPUT OUTPUT DECODES AND EB LATCH			
E.C. HISTORY	E. RACH. 27RNB		
314402		FRAME	01
314424		IBM CORP. SCD	PL102
315620		P.N. 1755070	000
DATE	LAST EC		
01-09-78	318552		



INC. TYPE  
A-E4H2 CE28

EXTENDED BUFFER CONTROLS	
E.C. HISTORY	D. MACH. PARTNR
314402	FRAME 01
314424	
DATE 11-19-76	IBM CORP. SCD PL103
LAST FC 316677	P.N. 1755071 000

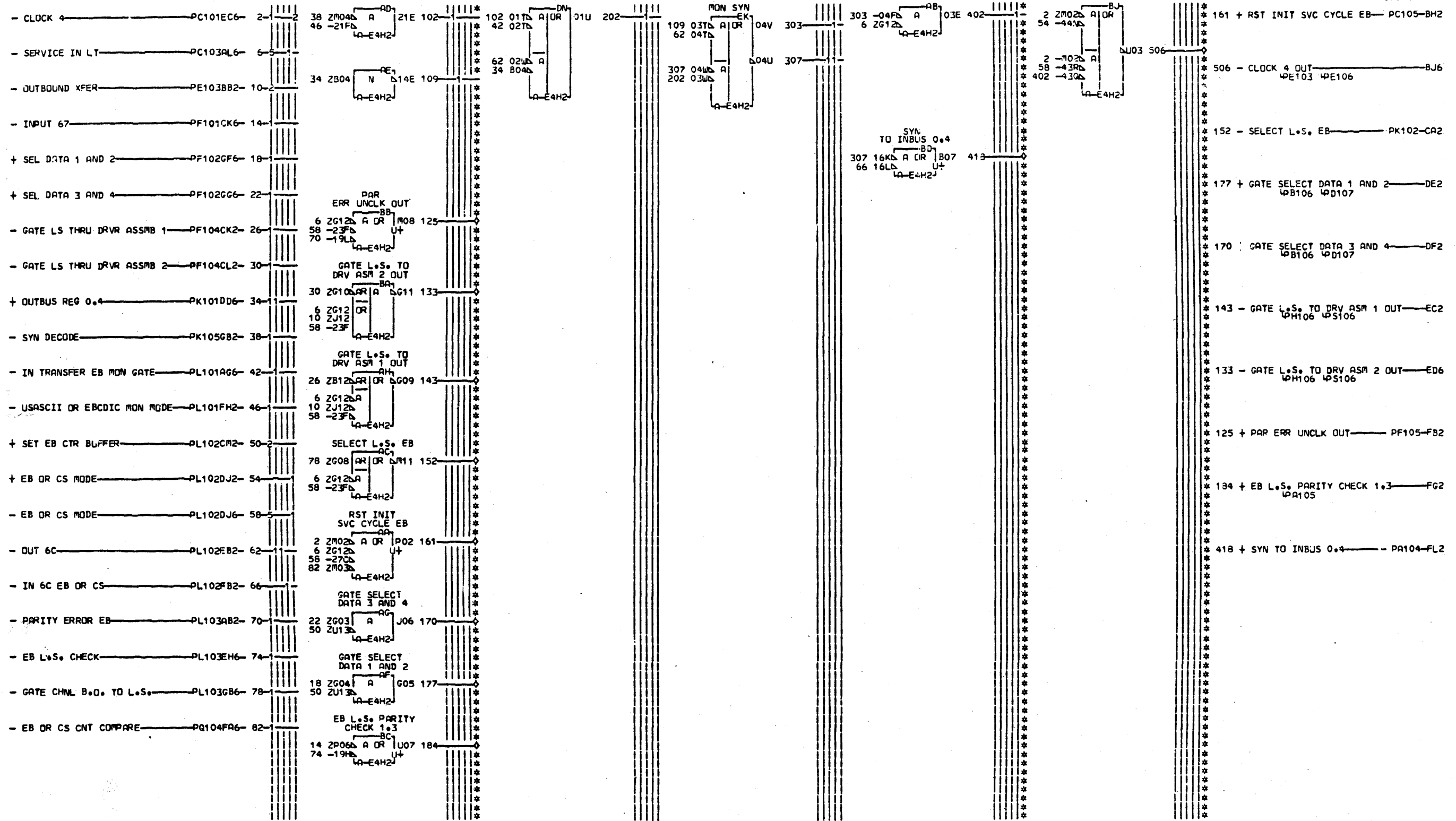


NOTE 1 SEE PAGE PA049 NOTES FOR DURATION OF DELAY BETWEEN BURSTS ON BYTE CHANNEL JUMPERING  
 NOTE 2 IF BOARD IS AT EC LEVEL 3156200 SOURCE FOR - INTERLOCK IS PM101FB6

PL104  
 000

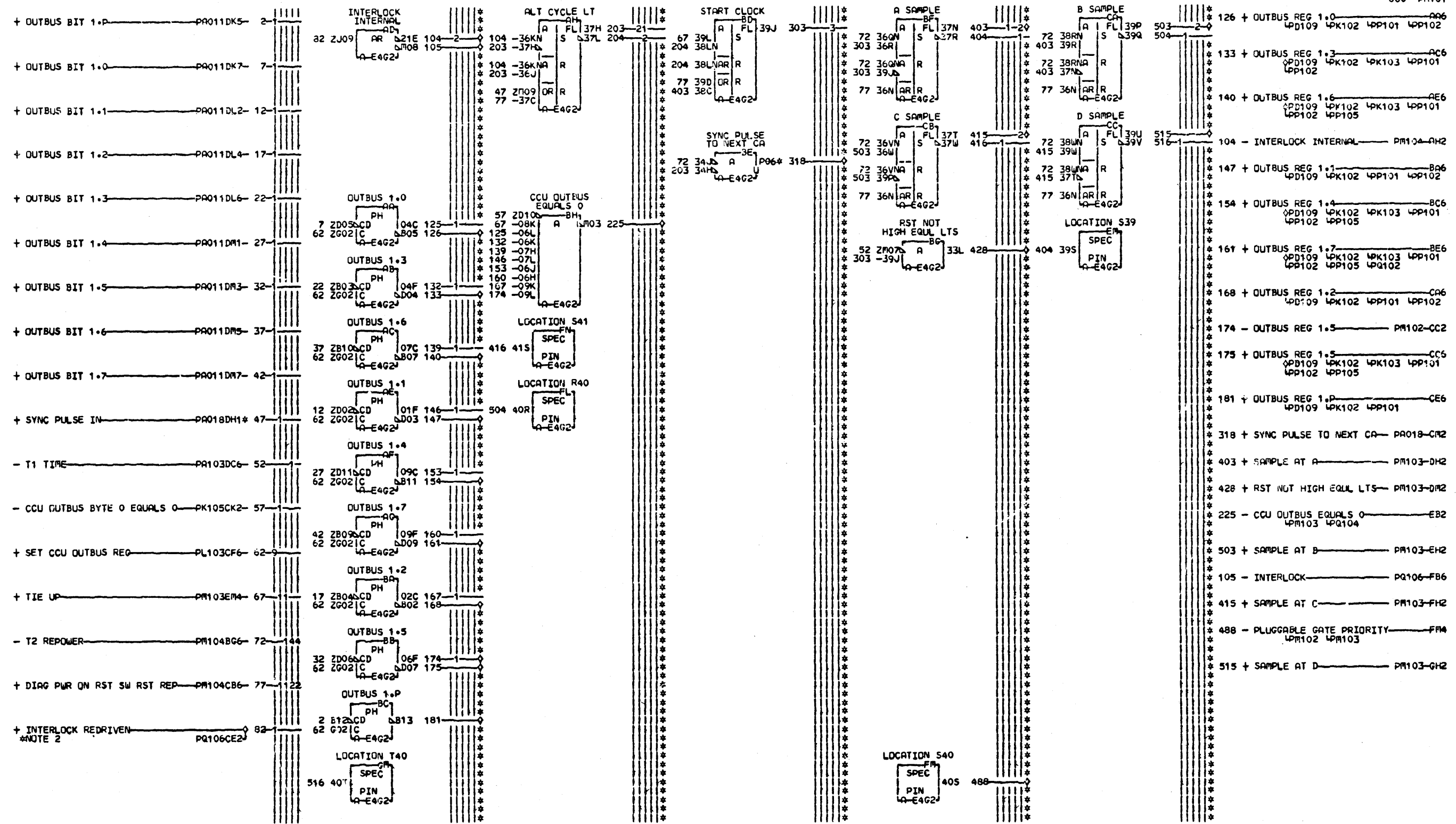
LOC. TYPE  
 A-E4H2 CE28

BURST LENGTH CONTROLS AND FORCE SHORT CU BUSY  
 -E-C-HISTORY-E-MACH-27RNB  
 314402  
 314424 FRAME 01  
 316677 IBM CORP.SCD PL104  
 DATE LAST EC P.N. 1755072 000  
 01-09-78 318552



LOC. TYPE  
A-E4H2 CE28

BOTH LOCAL STORE GATE CONTROLS AND I.S. CYCLE RESET		MACH. 27RNB	
E.C. - HISTORY	314402	FRAME	01
DATE	07-14-76	IBM CORP. SCD	PL105
LAST EC	315620	P.N.	1755073 000

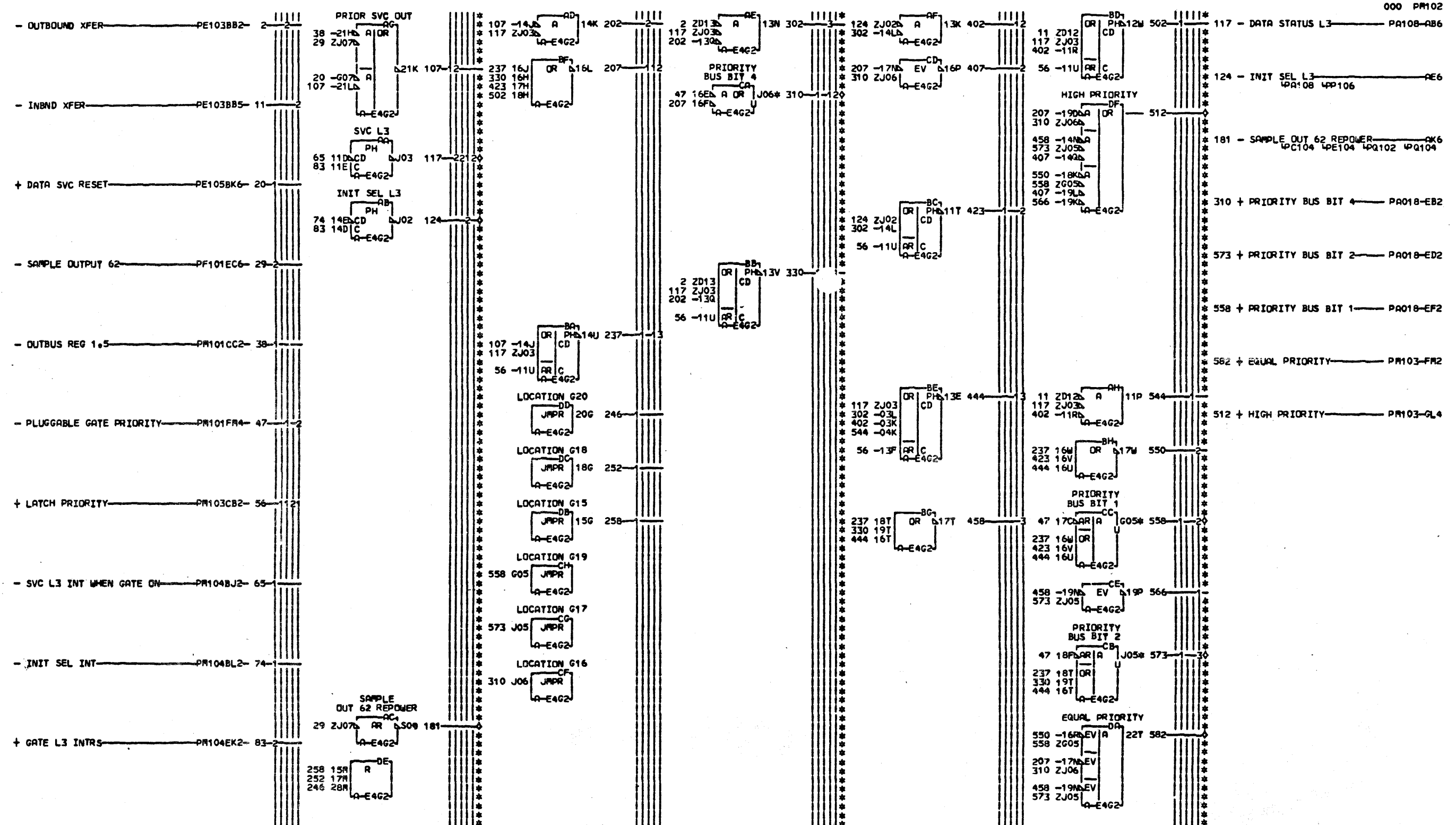


NOTE 1 SEE PAGE PROSS NOTES FOR MULTIPLE CA SAMPLE TIME JUMPERING  
 NOTE 2 IF BOARD IS AT EC LEVEL 3156200 SOURCE FOR INTERLOCK IS PA013DH7

EDGE CONN.  
 47 RESISTOR  
 A-E4G2M09  
 318 A-E4G6A02

LOC. TYPE  
 A-E4G2 CE29

CCU OUTBUS BYTE 1 REPOWER AND 0 DET AND PRIOR SAMPLE GEN		
E-C-HISTORY	E-MACH	#27RNB
314402	FRAME	01
314424		
315620		
316677	IBM CORP.	SCD PM101
DATE	LAST EC	
01-09-78	318552	P.N. 1755074 000

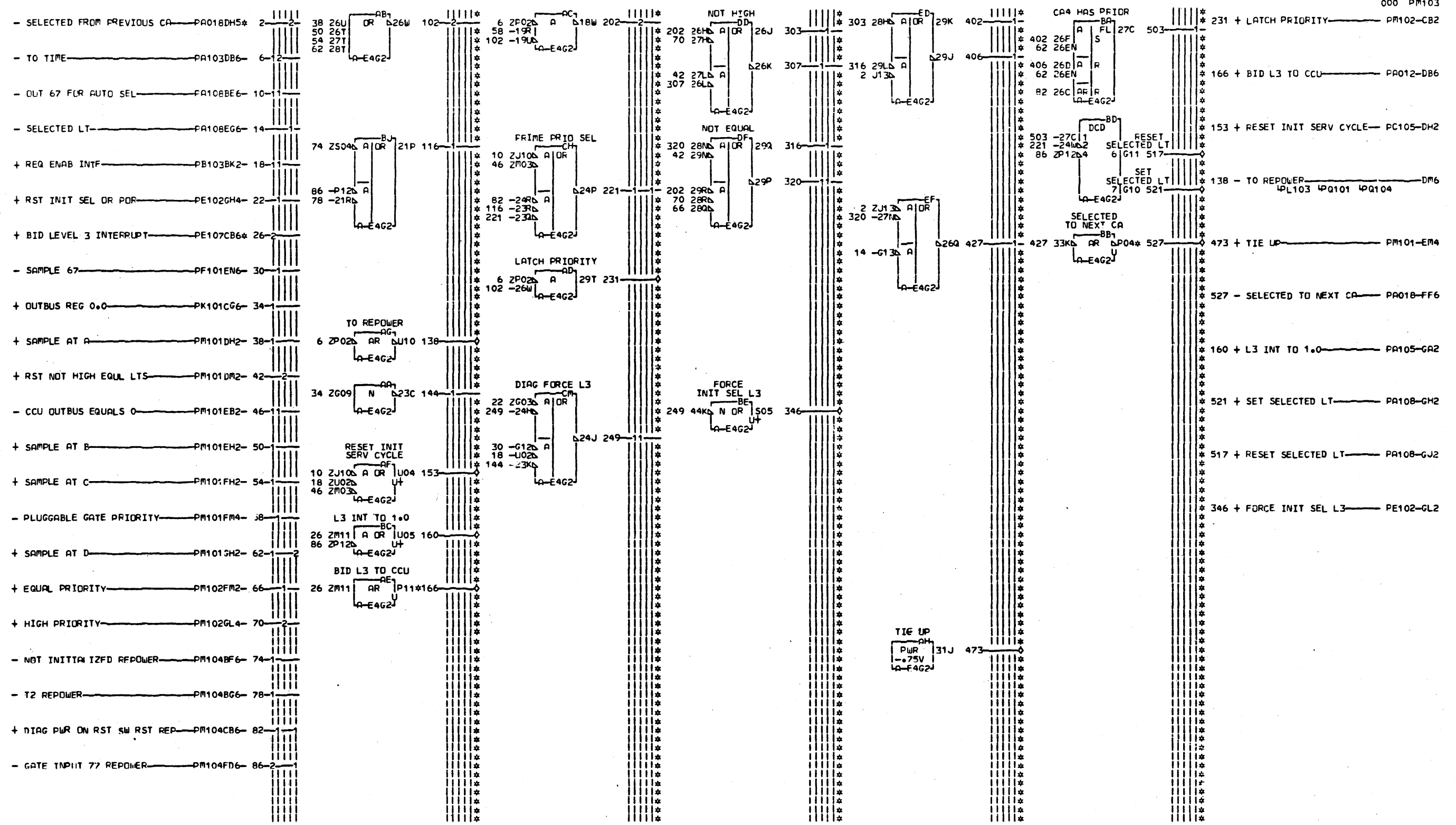


NOTE 1 SEE PAGE PROSS NOTES  
FOR PRIORITY BUS TERMINATION  
JUMPING

EDGE COM.  
310 A-E4N1D13  
01A-E4F6E04  
558 A-E4N1B11  
01A-E4F6C02  
573 A-E4N1C11  
01A-E4F6D02

LOC. TYPE  
A-E4G2 CE29

CA L3 PRIORITY DETERMINATION AND GATING PRIOR TO NEXT CA	
E.C. HISTORY	D. PACH. 27RNB
314402	
DATE	LAST EC
07-14-76	315620
FRAME	01
IBR CORP. SCD	PM102
P.N. 1755075	000



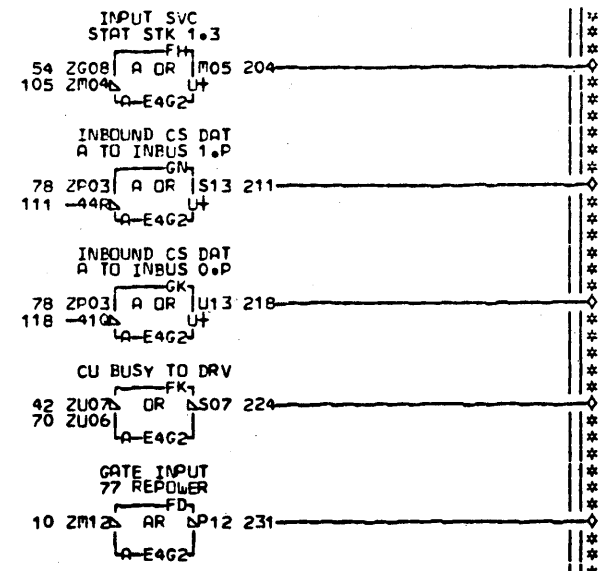
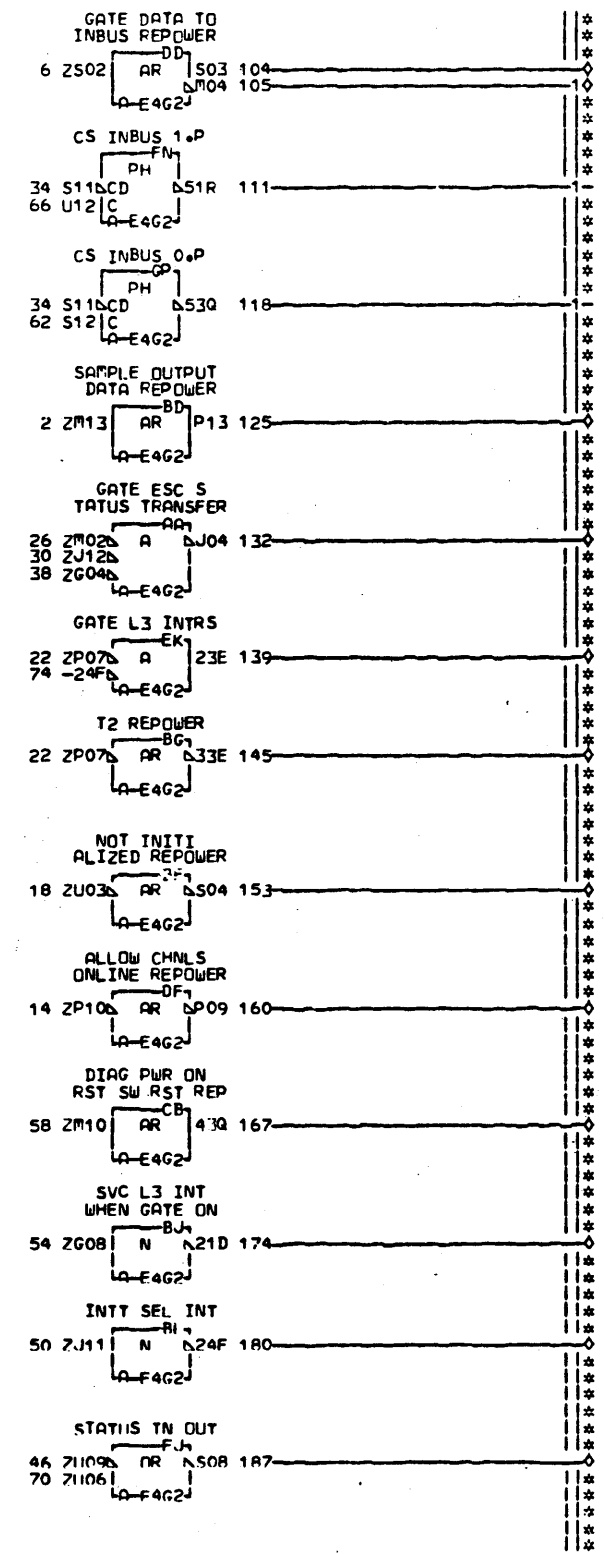
EDGE CNW.  
 2 RESISTOR  
 A-E4G2J17  
 26 RESISTOR  
 A-E4G2M11  
 166 A-E4C2D13  
 01A-E4C4D13  
 527 A-E4F6C04

LOC. TYPE  
 A-E4G2 CE29

PRIORITY CONTROL LOGIC			
F.C. HISTORY	D. MACH.	27RNR	
314402	FRAME	01	
314424	IBM CORP. SCD		PM103
315620	P.N.	1755076	000
DATE	LAST FC		
11-19-76	316677		



- + SAMPLE OUTPUT DATA ON OUTBUS-PA012DD5- 2-1
- + GATE INPUT DATA ON INBUS-PA012DD7- 6-1
- GATE INPUT 77-PA012DE4- 10-1
- ALLOW CHANNELS ON LINE-PA013DH1- 14-1
- NOT INITIALIZED-PA013DH3- 18-1
- T2 TIME-PA103DD6- 22-2
- + COMMAND OUT-PB101HC4- 26-1
- + SERVICE OUT-PB101HD4- 30-1
- LS BIT P TO DRV OR INBUS-PB106DB4- 34-2
- ESC STATUS XFER-PE103BB8- 38-1
- CONTROL UNIT BUSY TO DRIVER-PE106DG6- 42-1
- STATUS IN LT-PE106HB2- 46-1
- + INIT SEL L3 INT-PE107CC2\*- 50-1
- + SVC STAT STK OR SVC L3 INT-PE107EE4\*- 54-1
- + DIAG PWR ON OR RESET SW RES-PH107DK6- 58-1
- + SET IN REG BYTE 0-PL103GK2- 62-1
- + SET IN REG BYTE 1-PL103GL2- 66-1
- + EB FORCE BUSY-PL104DD2- 70-1
- INTERLOCK INTERNAL-PM101AH2- 74-1
- + GATE CS DATA TO INBUS-PQ101GN2- 78-2



- 132 - GATE ESC STATUS TRANSFER-AA6  
LPF102
- 125 + SAMPLE OUTPUT DATA REPOWER-BD6  
LP108 LPF101 LP102 LPQ103
- 153 - NOT INITIALIZED REPOWER-BF6  
LPG102 LP102 LPM103 LPQ104
- 145 - T2 REPOWER-BG6  
LP101 LPM103
- 174 - SVC L3 INT WHEN GATE ON PM102-BJ2
- 180 - INIT SEL INT-PM102-BL2
- 167 + DIAG PWR ON RST SW RST REP-CB6  
LP101 LPM103
- 104 + GATE DATA TO INBUS REPOWER-DD6  
LP103 LP105 LPF101
- 160 - ALLOW CHNLS ONLINE REPOWER-DF6  
LPB103 L105 LPG102 LPH105  
LPQ102 L105 LPS105
- 105 - GATE INPUT DATA-EG2  
LPD106 LP102 LPQ103
- 139 + GATE L3 INTR-PM102-EK2
- 231 - GATE INPUT 77 REPOWER-FD6  
LP108 LPM103
- 204 + INPUT SVC STAT STK 1.3-PA105-FH2
- 187 - STATUS IN OUT-FJ2  
LPH105 LPH106 LPS105 LPS106
- 224 - CU BUSY TO DRV-FK2  
LPH106 LPS106
- 21A + INBOUND CS DATA TO INBUS 0.0P-GK2  
LP106
- 211 + INBOUND CS DATA TO INBUS 1.0P-GN2  
LP107

EDGE CONN.  
50 RESISTOR  
A-E4G2J11  
54 RESISTOR  
A-E4G2G08

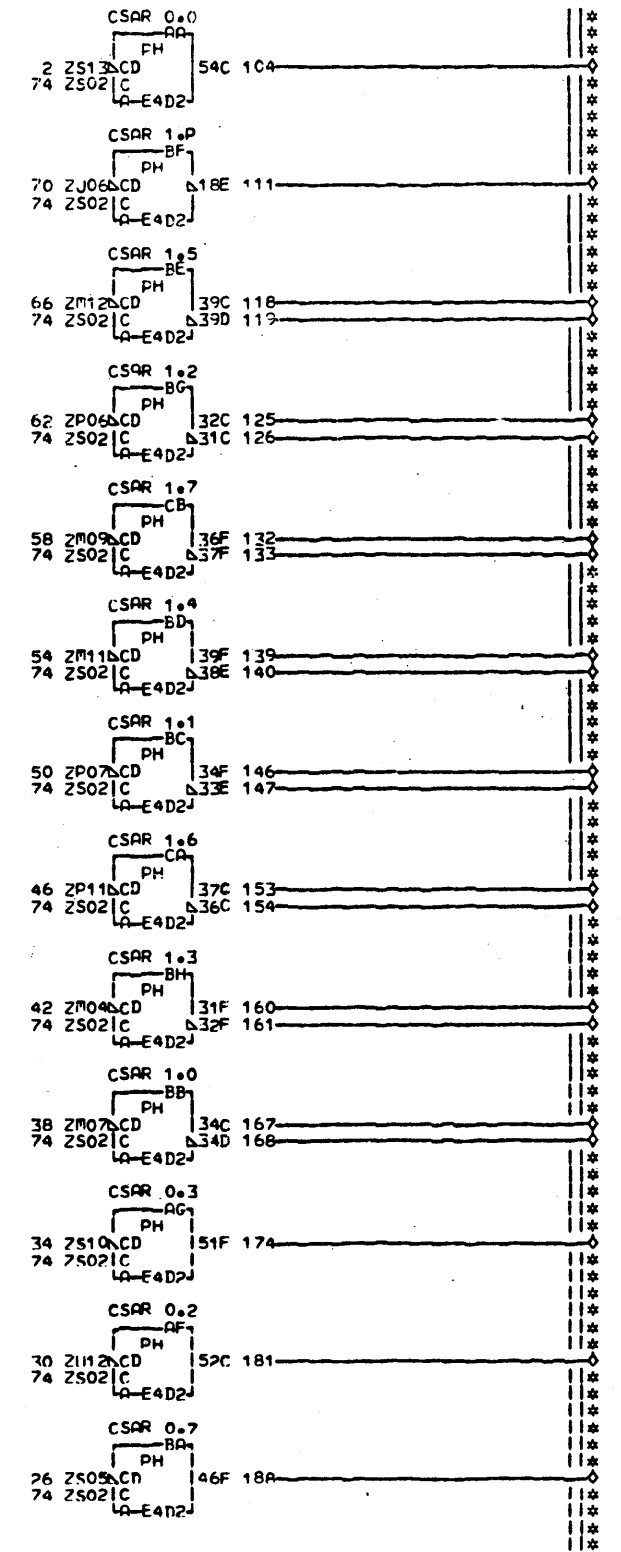
LDC. TYPE  
A-E4G2 CE29

PM104  
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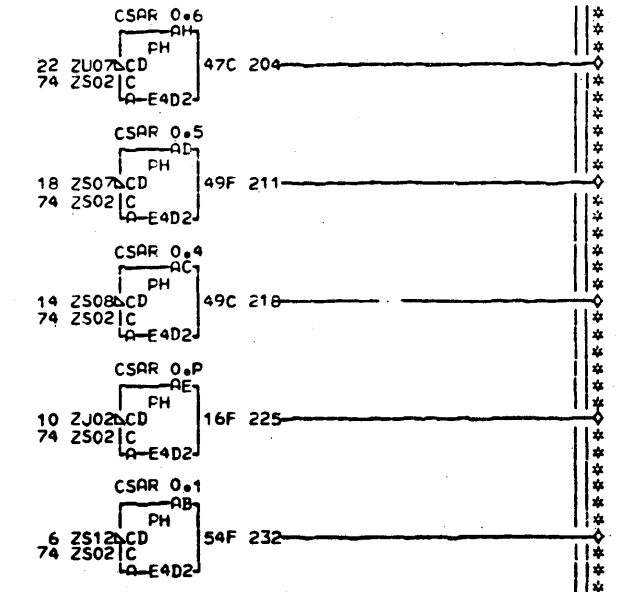
REPOWERING			
F.C. HISTORY	MACH.27RNB		
314402		FRAME	01
314424		IRM CORP.SCD	PM104
DATE	LAST EC	P.N.	1755077 000
11-19-76	316677		



+ OUTBUS REG 0.0 — FK101CG6— 2—  
 + OUTBUS REG 0.1 — PK101CJ6— 6—  
 + OUTBUS REG 0.P — PK101CL6— 10—  
 + OUTBUS REG 0.4 — FK101DD6— 14—  
 + OUTBUS REG 0.5 — PK101DF6— 18—  
 + OUTBUS REG 0.6 — PK101DH6— 22—  
 + OUTBUS REG 0.7 — PK101DK6— 26—  
 + OUTBUS REG 0.2 — PK101FK6— 30—  
 + OUTBUS REG 0.3 — PK101FM6— 34—  
 + OUTBUS REG 1.0 — PM101AR6— 38—  
 + OUTBUS REG 1.3 — PM101AC6— 42—  
 + OUTBUS REG 1.6 — PM101AE6— 46—  
 + OUTBUS REG 1.1 — PM101BA6— 50—  
 + OUTBUS REG 1.4 — PM101BC6— 54—  
 + OUTBUS REG 1.7 — PM101BE6— 58—  
 + OUTBUS REG 1.2 — PM101CA6— 62—  
 + OUTBUS REG 1.5 — PM101CC6— 66—  
 + OUTBUS REG 1.P — PM101CE6— 70—  
 + SET CSAR BYTE 0 AND 1 — PQ103FC6— 74-75



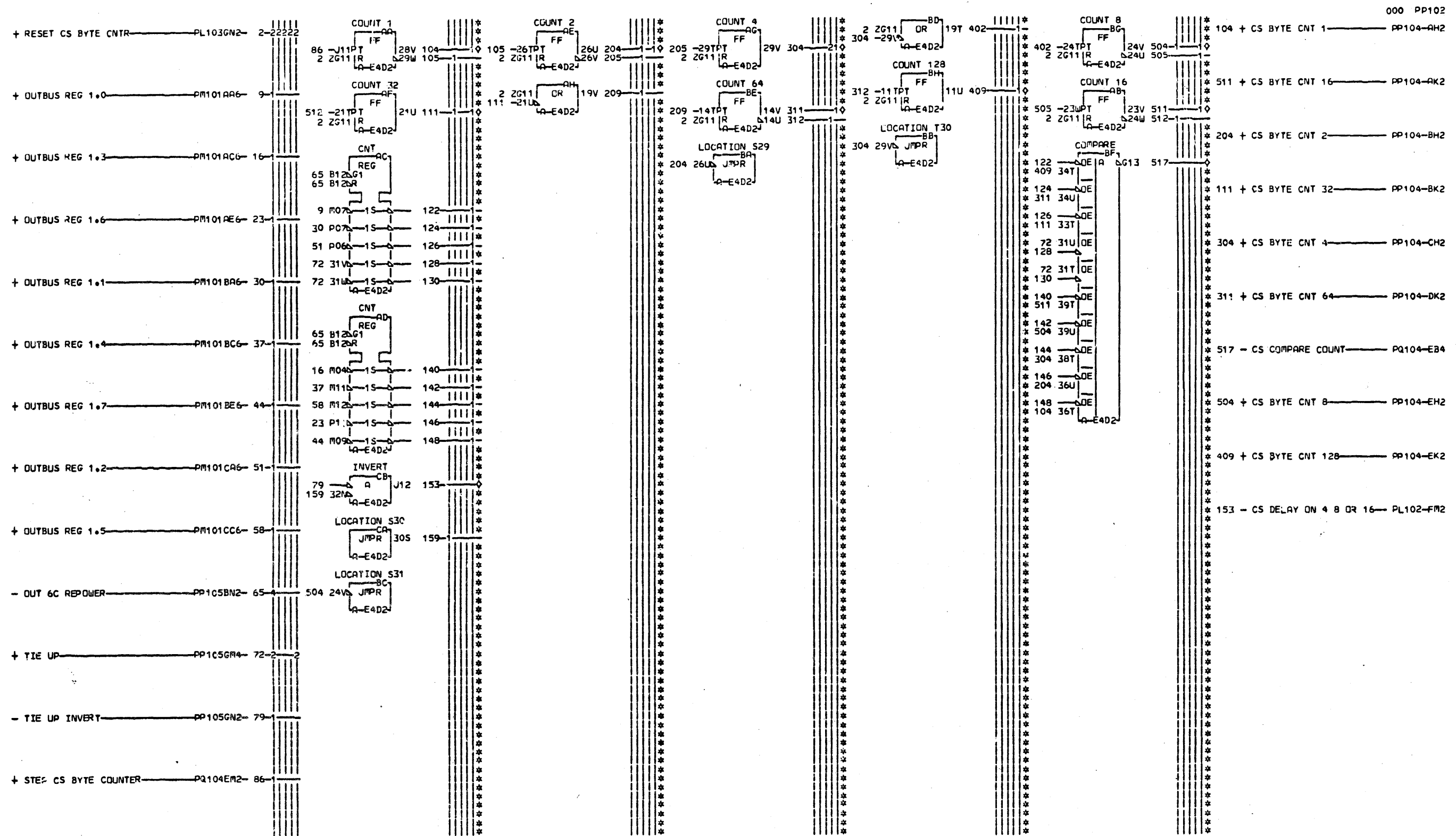
LOC. TYPE  
 A-E4D2 CE24



000 PP101  
 104 - CSAR BIT 0.0 — AA2  
 232 - CSAR BIT 0.1 — AB2  
 218 - CSAR BIT 0.4 — AG2  
 211 - CSAR BIT 0.5 — AH2  
 225 - CSAR BIT 0.P — PP103-AN2  
 181 - CSAR BIT 0.2 — BD2  
 174 - CSAR BIT 0.3 — BE2  
 204 - CSAR BIT 0.6 — BK2  
 188 - CSAR BIT 0.7 — BL2  
 167 - CSAR BIT 1.0 — PP103-DA2  
 168 + CSAR BIT 1.0 — PP104-DA6  
 146 - CSAR BIT 1.1 — PP103-DB2  
 147 + CSAR BIT 1.1 — PP104-DB6  
 139 - CSAR BIT 1.4 — PP103-DG2  
 140 + CSAR BIT 1.4 — PP104-DG6  
 118 - CSAR BIT 1.5 — PP103-DH2  
 119 + CSAR BIT 1.5 — PP104-DH6  
 111 + CSAR BIT 1.P — PP105-DN6  
 125 - CSAR BIT 1.2 — PP103-ED2  
 126 + CSAR BIT 1.2 — PP104-ED6  
 160 - CSAR BIT 1.3 — PP103-EE2  
 161 + CSAR BIT 1.3 — PP104-EE6  
 153 - CSAR BIT 1.6 — PP103-EK2  
 154 + CSAR BIT 1.6 — PP104-EK6  
 132 - CSAR BIT 1.7 — PP105-EL2  
 133 + CSAR BIT 1.7 — PP104-EL6

PP101  
 000

CS ADDRESS REG	
F.C. HISTORY 314402	MACH. 27RNR FRAME 01
DATE LAST FC 11-19-76 316677	IRM CORP. SNO PP101 P. No. 1755078 000



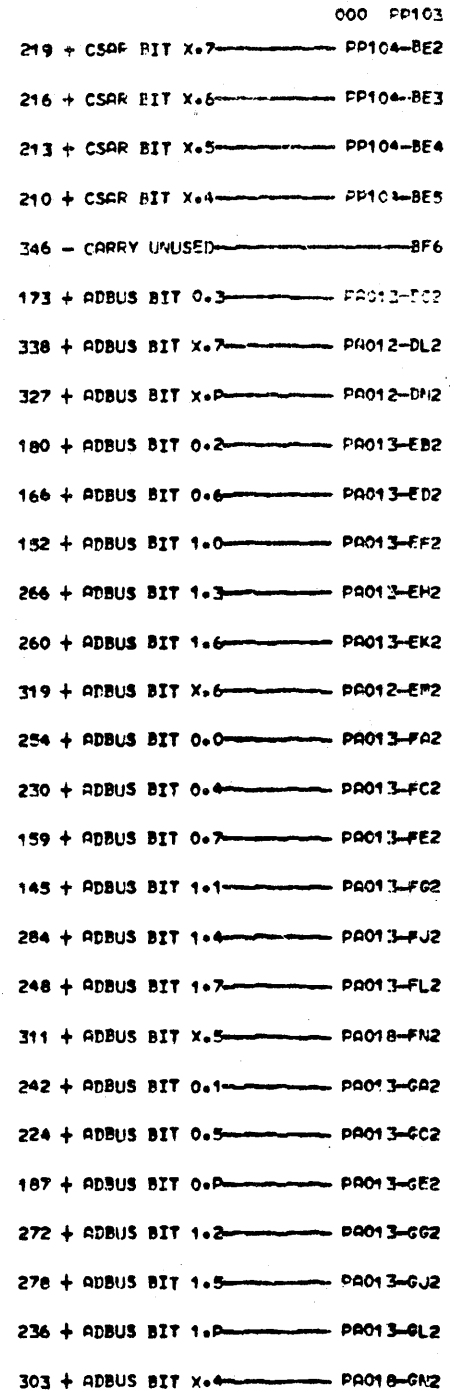
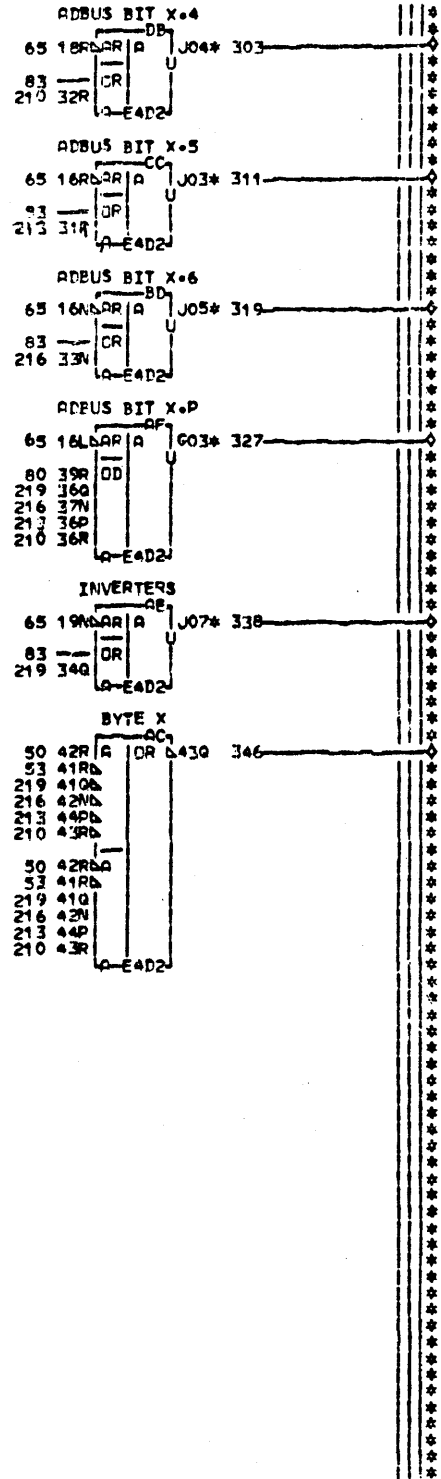
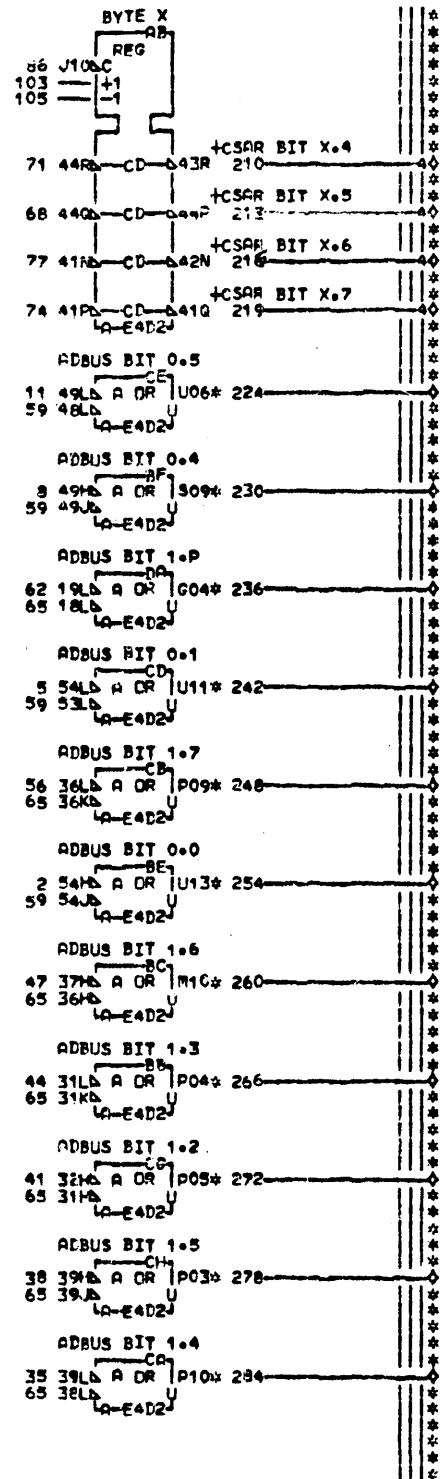
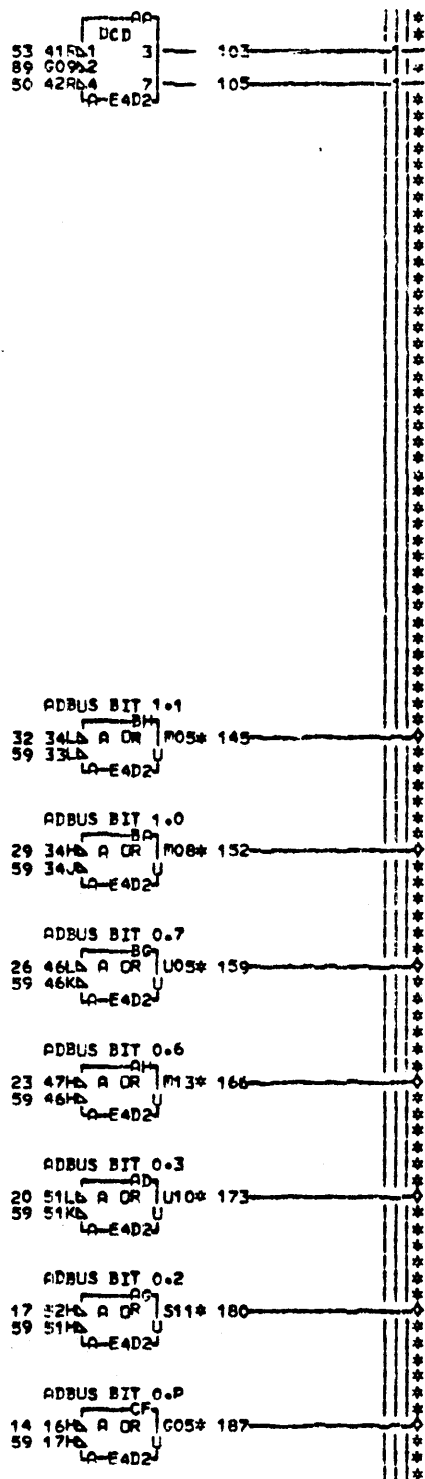
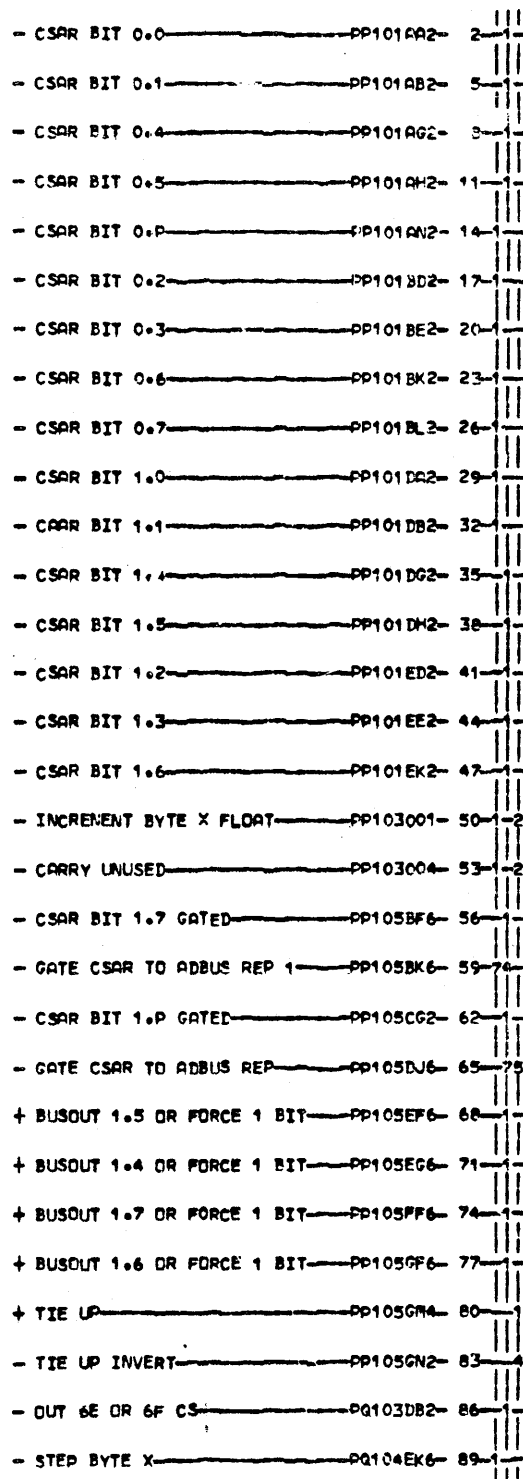
NOTE 1 SEE PAGE PA043 NOTES  
FOR BYTE CHANNEL BURST LENGTH  
OPTION JUMPERING

LOC. TYPE  
A-E4D2 CE24

CS COUNTER AND COUNT	
COMPARE	
E-C-HISTORY	D-MACH-27RNR
314402	FRAME 01
DATE LAST EC	IBM CORP. SDD
07-14-76 315620	P.N. 1755079

PP102  
000

PP102  
000



EDGE CONN.

145	A-E4D205	180	A-E4D205	242	A-E4D205	272	A-E4D206	319	A-E4D211
01A	E4D205	01A	E4D206	01A	E4D205	01A	E4D206	01A	E4D211
152	A-E4D203	187	A-E4D202	248	A-E4D213	278	A-E4D210	327	A-E4D210
01A	E4D203	01A	E4D202	01A	E4D213	01A	E4D210	01A	E4D210
159	A-E4D213	224	A-E4D210	254	A-E4D204	284	A-E4D209	338	A-E4D213
01A	E4D213	01A	E4D210	01A	E4D204	01A	E4D209	01A	E4D213
166	A-E4D212	230	A-E4D209	260	A-E4D211	303	A-E4D213		
01A	E4D212	01A	E4D209	01A	E4D211	01A	E4D213		
173	A-E4D208	236	A-E4D202	266	A-E4D207	311	A-E4D213		
01A	E4D208	01A	E4D202	01A	E4D207	01A	E4D213		

LOC. TYPE  
P-E4D2 CE24

CS BYTE X AND ADBUS GATING

E.C. HISTORY	E. MACH. 27RMB
314402	FRAP 01
316677	
DATE	LAST EC
01-24-79	318589
P.N.	1755080
000	

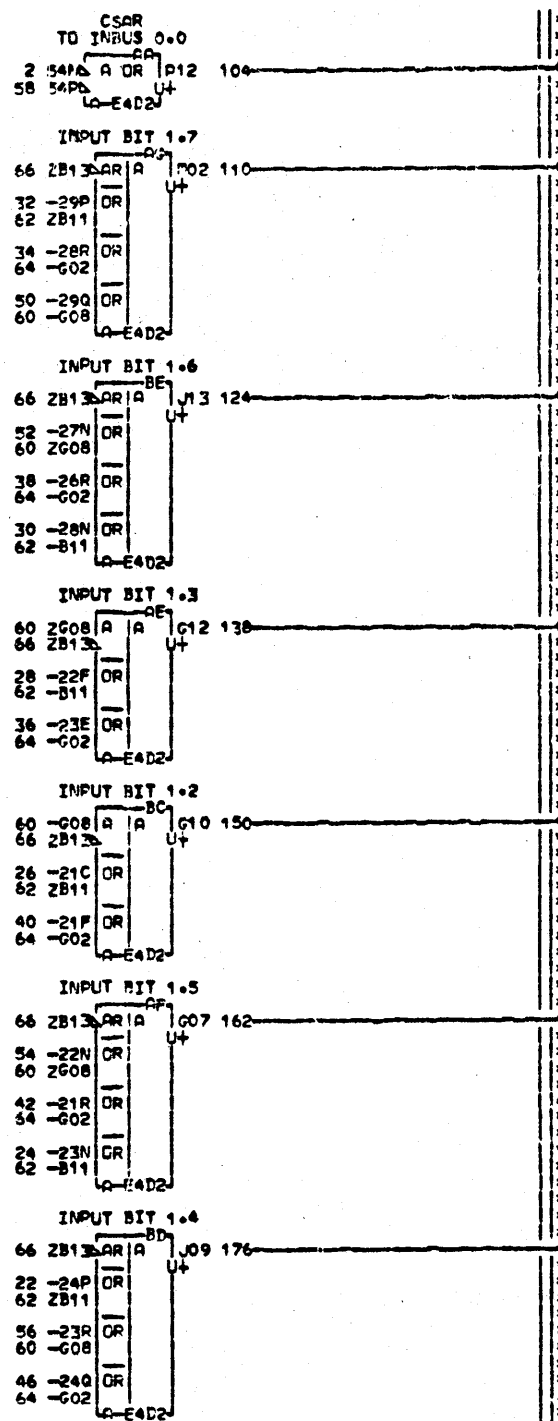
000 PP103

PP103

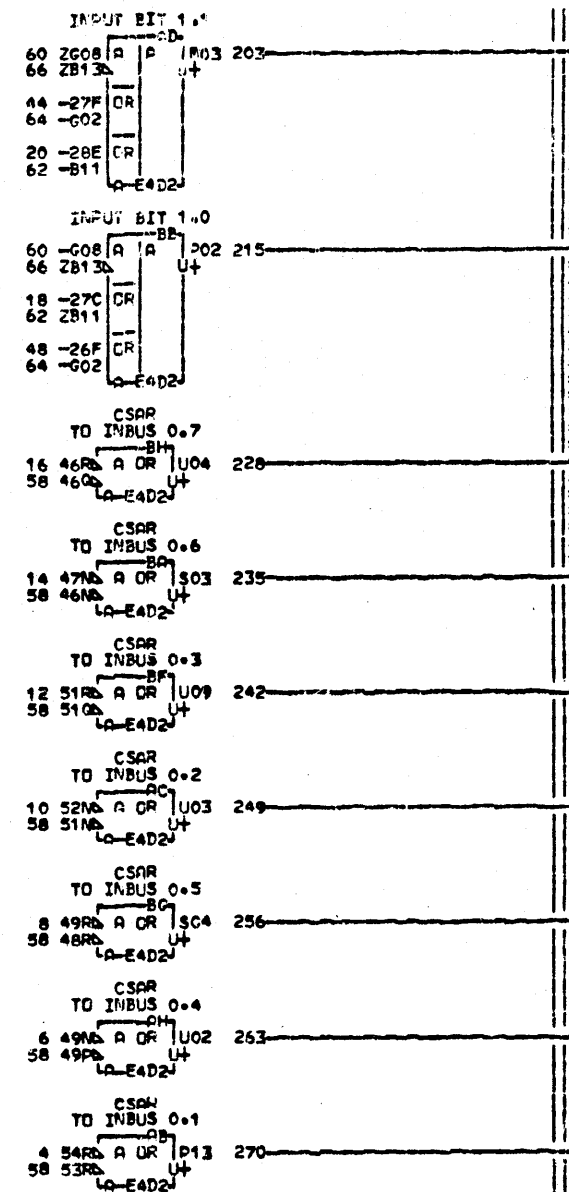
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38996

- CSAR BIT 0.0 ----- PP101AA2- 2-1  
 - CSAR BIT 0.1 ----- PP101AB2- 4-1  
 - CSAR BIT 0.4 ----- PP101AG2- 6-1  
 - CSAR BIT 0.5 ----- PP101AH2- 8-1  
 - CSAR BIT 0.2 ----- PP101BD2- 10-1  
 - CSAR BIT 0.3 ----- PP101BE2- 12-1  
 - CSAR BIT 0.6 ----- PP101BK2- 14-1  
 - CSAR BIT 0.7 ----- PP101BL2- 16-1  
 + CSAR BIT 1.0 ----- PP101DA6- 18-1  
 + CSAR BIT 1.1 ----- PP101DB6- 20-1  
 + CSAR BIT 1.4 ----- PP101DC6- 22-1  
 + CSAR BIT 1.5 ----- PP101DH6- 24-1  
 + CSAR BIT 1.2 ----- PP101ED6- 26-1  
 + CSAR BIT 1.3 ----- PP101EE6- 28-1  
 + CSAR BIT 1.6 ----- PP101EK6- 30-1  
 + CSAR BIT 1.7 ----- PP101EL6- 32-1  
 + CS BYTE CNT 1 ----- PP102AK2- 34-1  
 + CS BYTE CNT 16 ----- PP102BK2- 36-1  
 + CS BYTE CNT 2 ----- PP102BK2- 38-1  
 + CS BYTE CNT 32 ----- PP102BK2- 40-1  
 + CS BYTE CNT 4 ----- PP102CK2- 42-1  
 + CS BYTE CNT 64 ----- PP102DK2- 44-1  
 + CS BYTE CNT 8 ----- PP102EK2- 46-1  
 + CS BYTE CNT 128 ----- PP102EK2- 48-1  
 + CSAR BIT X.7 ----- PP103BE2- 50-1  
 + CSAR BIT X.6 ----- PP103BE3- 52-1  
 + CSAR BIT X.5 ----- PP103BE4- 54-1  
 + CSAR BIT X.4 ----- PP103BE5- 56-1  
 - IN 6F REPOWER ----- PP105BL6- 58-17  
 - IN 6E CS ----- PP103CF6- 60-62  
 - IN 6F CS ----- PP103CG6- 62-62  
 - IN 6C CS ----- PP103CH6- 64-62  
 - IN 6C OR 6E OR 6F CS ----- PP103CE2- 66-62



LOC. TYPE  
A-E4D2 CE24



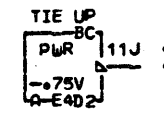
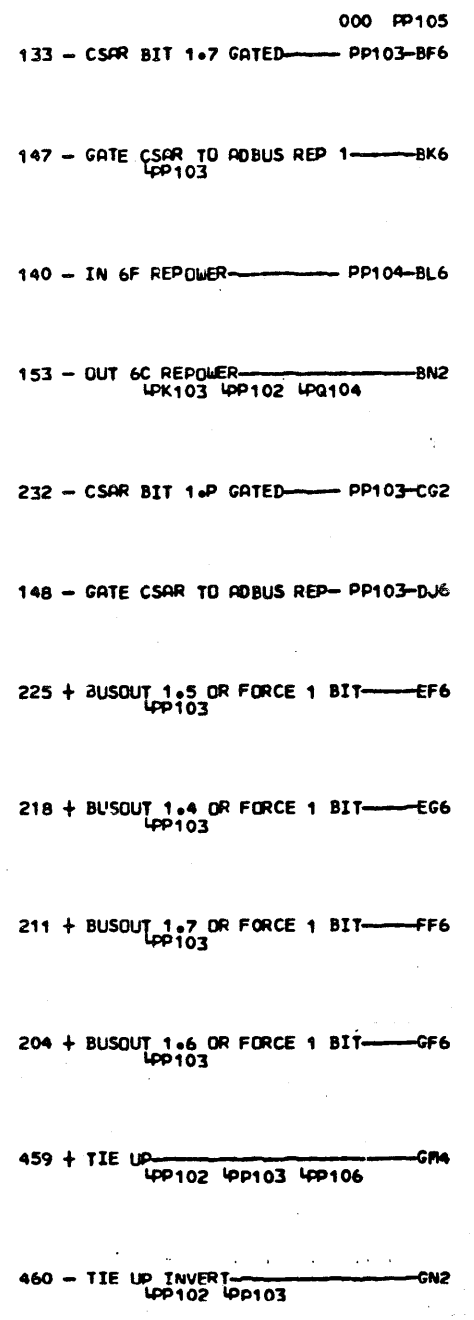
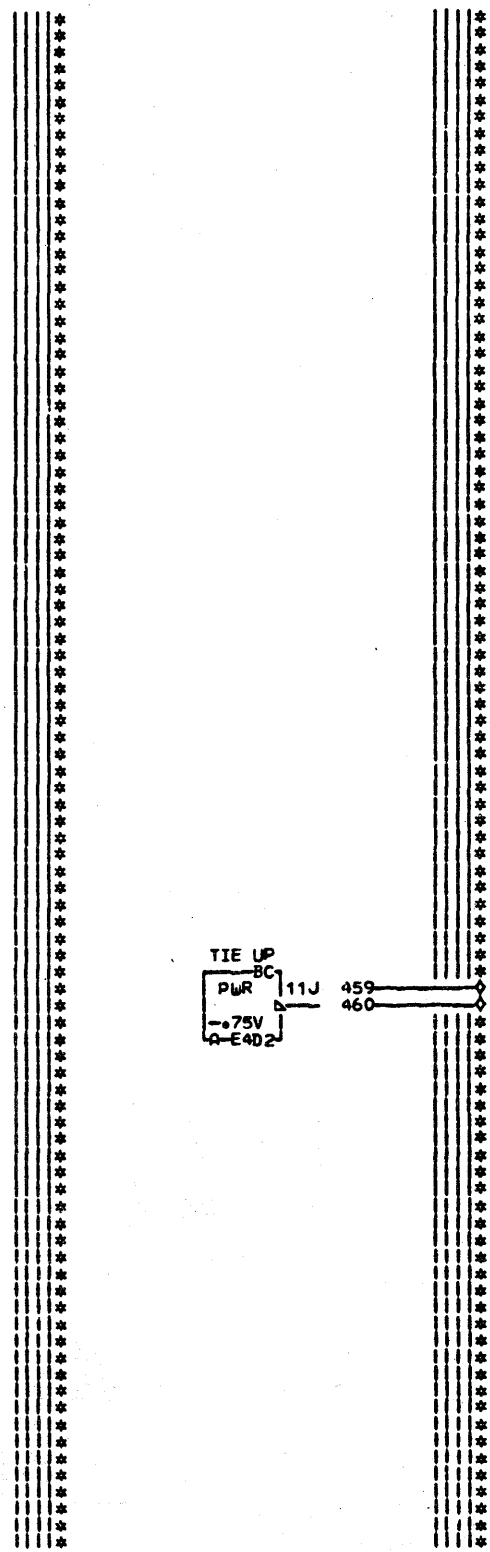
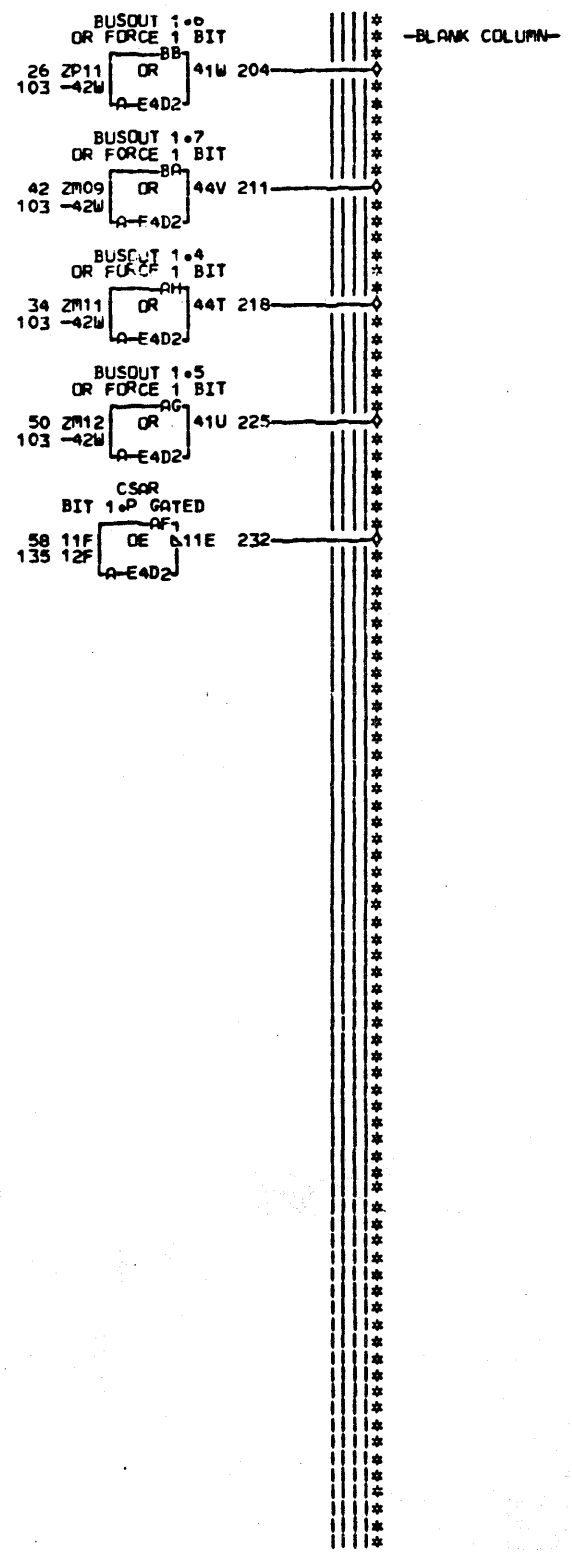
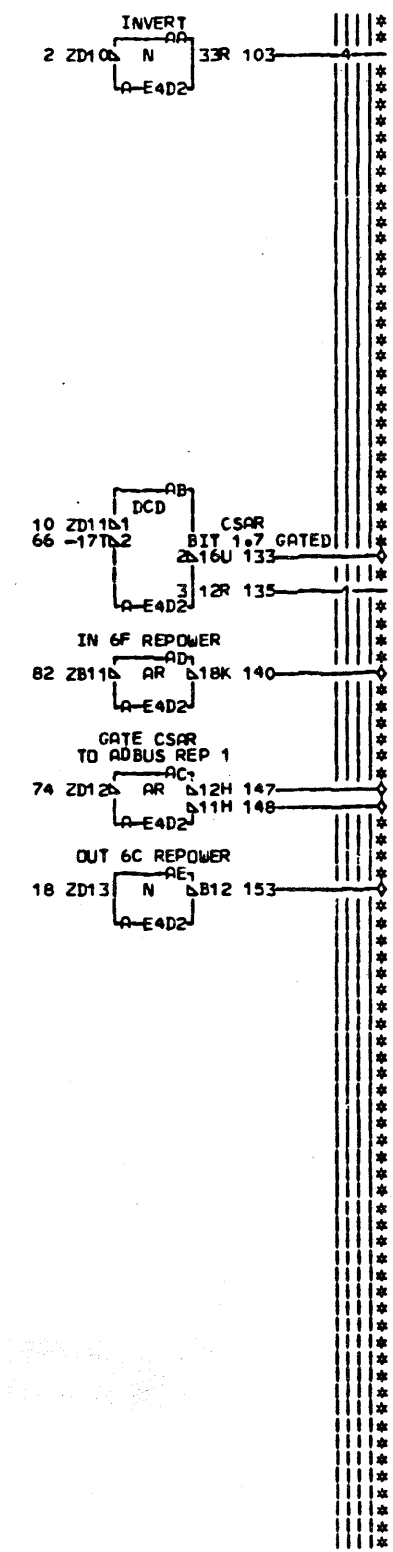
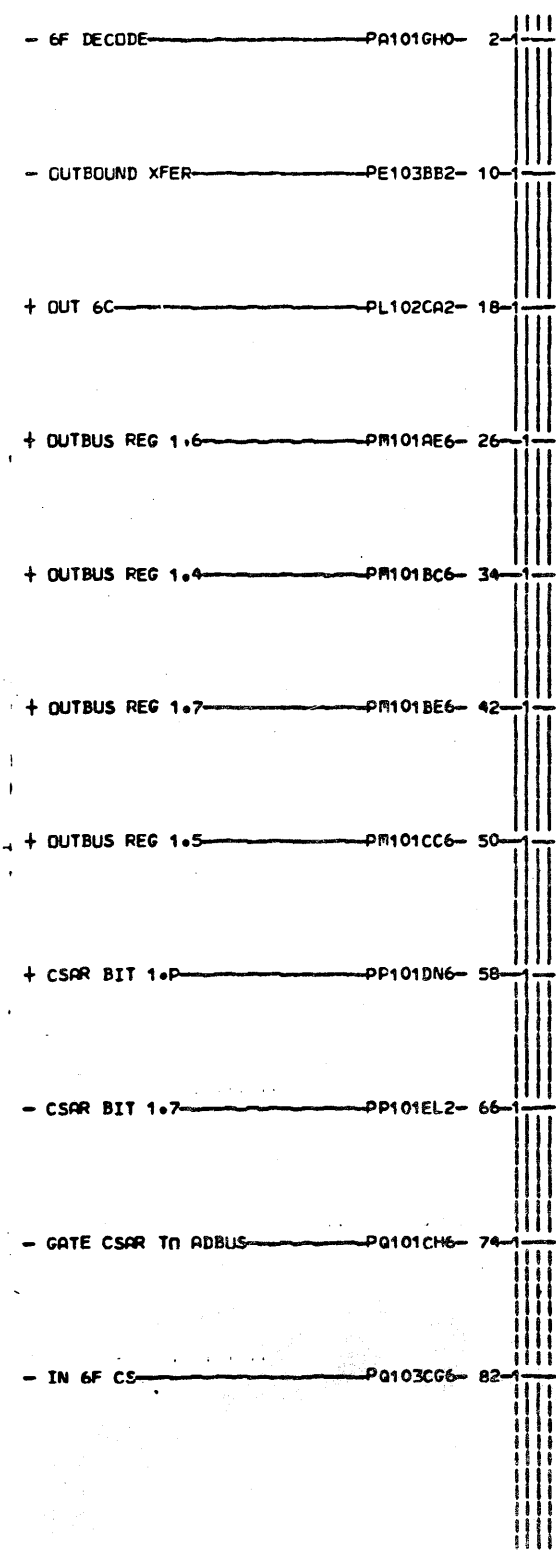
000 PP104

104 + CSAR TO INBUS 0.0 ----- PA104-GI2  
 270 + CSAR TO INBUS 0.1 ----- PA104-DM2  
 249 + CSAR TO INBUS 0.2 ----- PA104-EN2  
 203 + CS INPUTS TO INBUS 1.1- PA105-FE2  
 138 + CS INPUTS TO INBUS 1.3- PA105-FD2  
 162 + CS INPUTS TO INBUS 1.5- PA105-FF2  
 110 + CS INPUTS TO INBUS 1.7- PA105-FH2  
 263 + CSAR TO INBUS 0.4 ----- PA104-FK2  
 235 + CSAR TO INBUS 0.6 ----- PA104-FF2  
 215 + CS INPUTS TO INBUS 1.0- PA105-GA2  
 150 + CS INPUTS TO INBUS 1.2- PA105-GC2  
 176 + CS INPUTS TO INBUS 1.4- PA105-GE2  
 124 + CS INPUTS TO INBUS 1.6- PA105-GG2  
 242 + CSAR TO INBUS 0.3 ----- PA104-GJ2  
 256 + CSAR TO INBUS 0.5 ----- PA104-GL2  
 228 + CSAR TO INBUS 0.7 ----- PA104-GN2

PP104  
000

CS BYTE 0 AND 1 GATING TO INBUS		E-C-HISTORY-E		PCH-27RNB	
314402		FRAME		01	
316677		IBM CORP.		PP104	
DATE LAST EC		P.N.		1755081 C00	
01-24-79 318589					

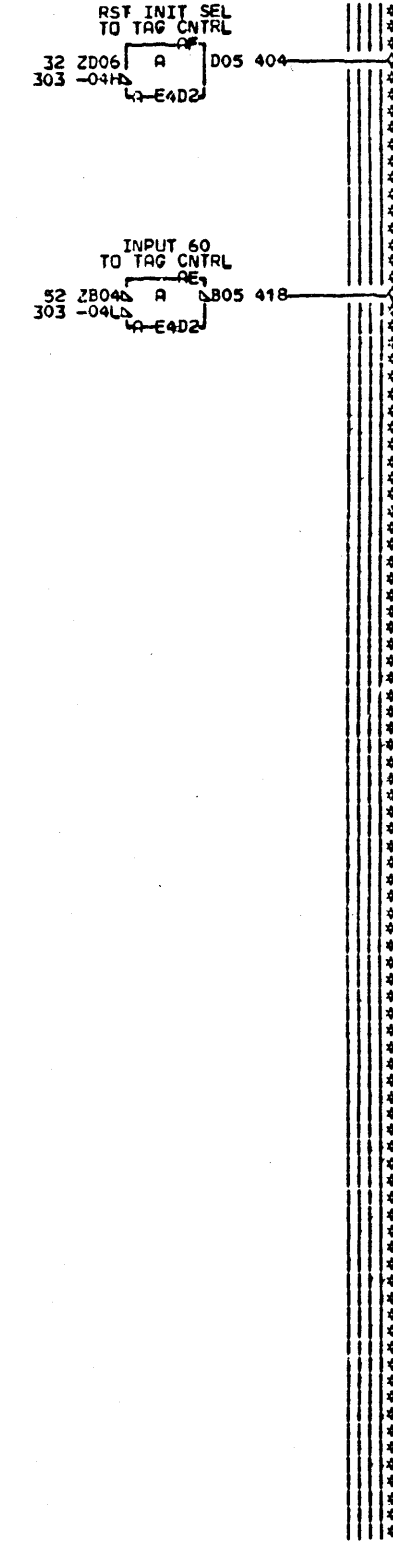
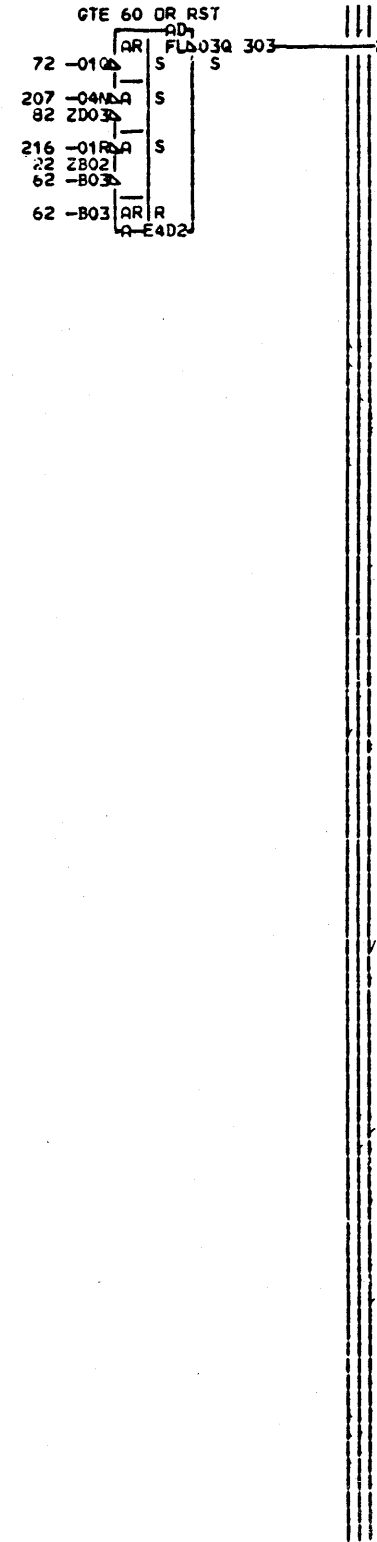
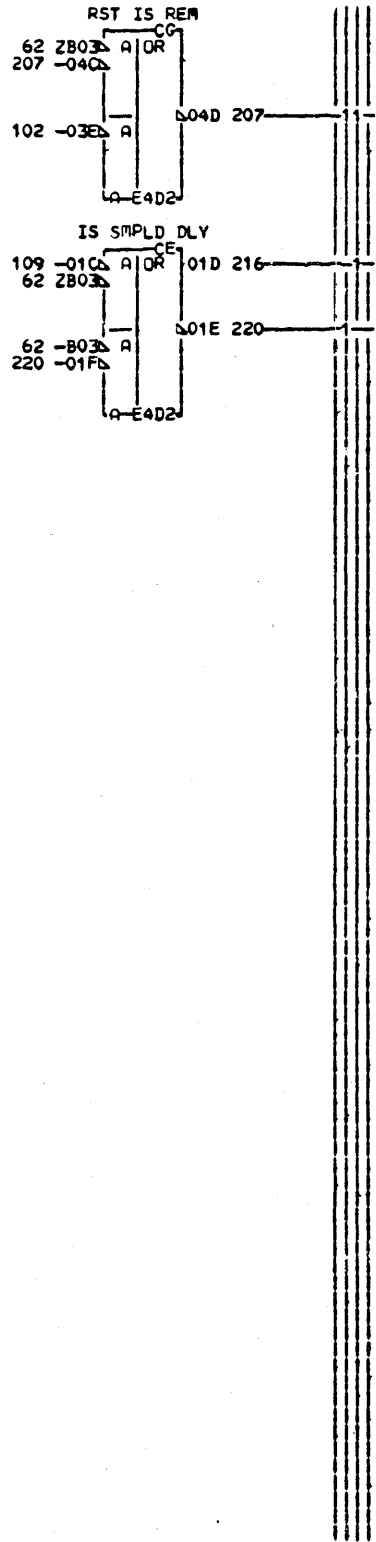
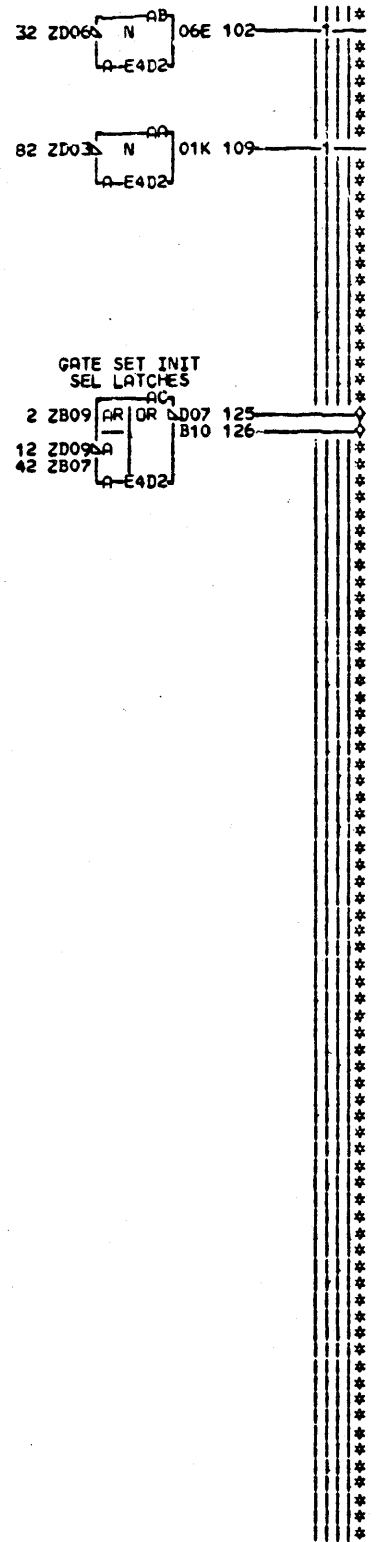
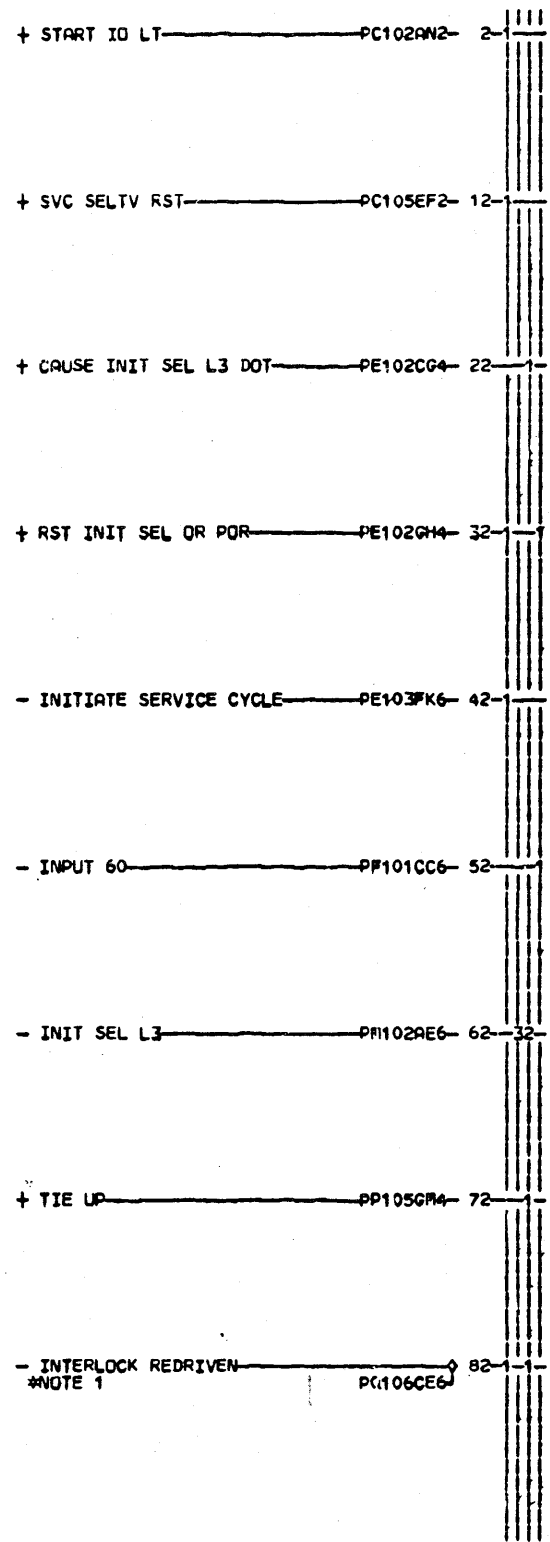
38596



LOC. TYPE  
A-E4D2 CE24

PP105  
000

FORCE 1 TO BYTE X AND REPOWERING	
E.C. HISTORY	MACH. 27RNB
314402	
DATE LAST EC	IBN CORP. SDD PP105
11-19-76 316677	P.No. 1755082 000



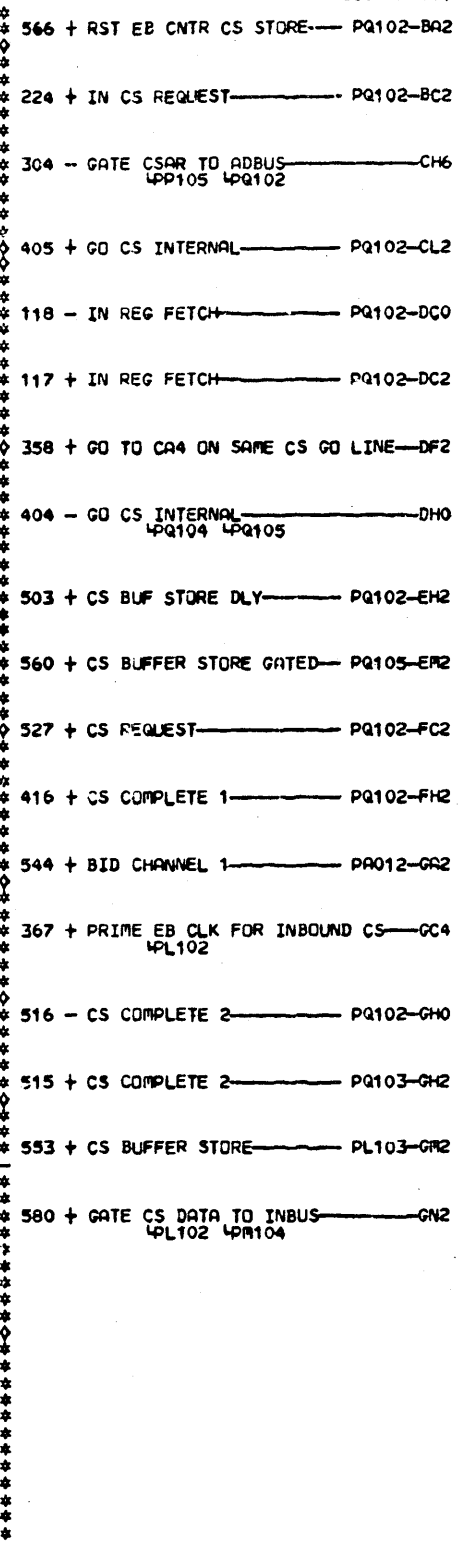
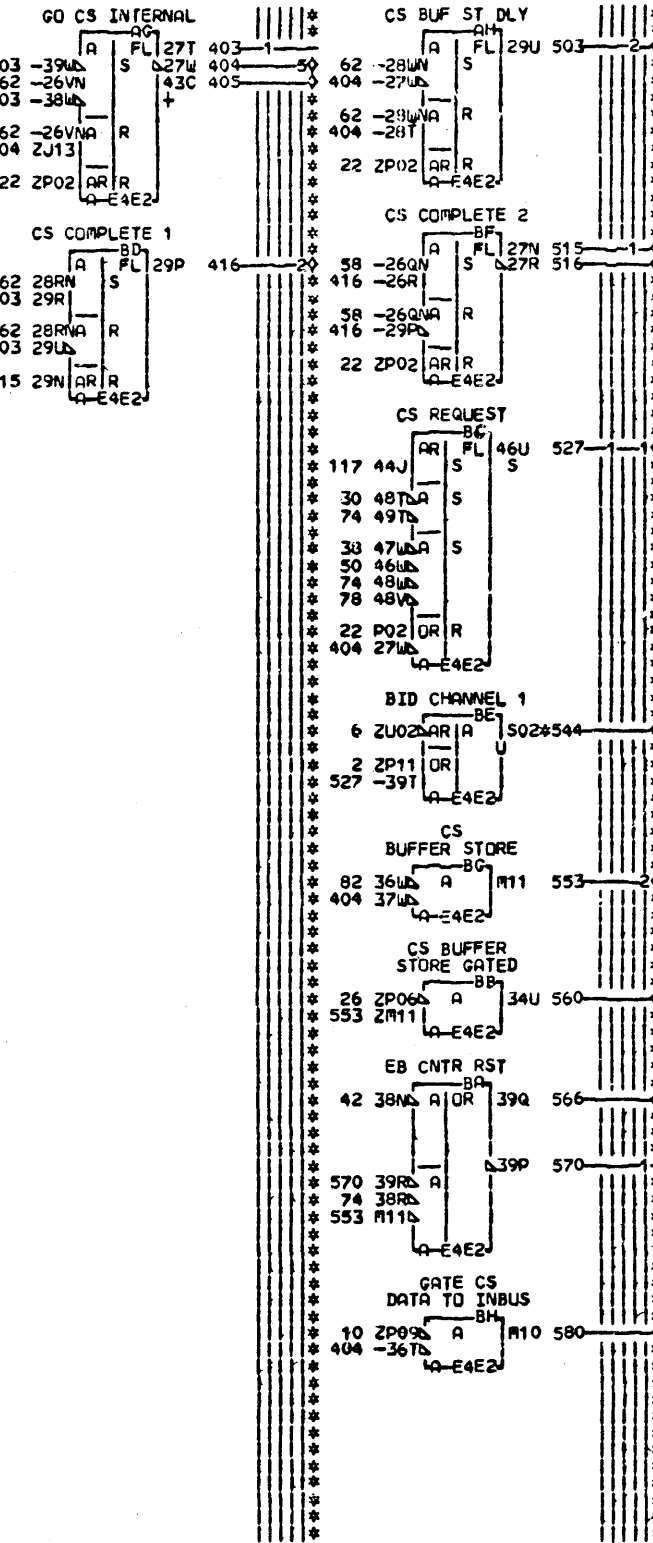
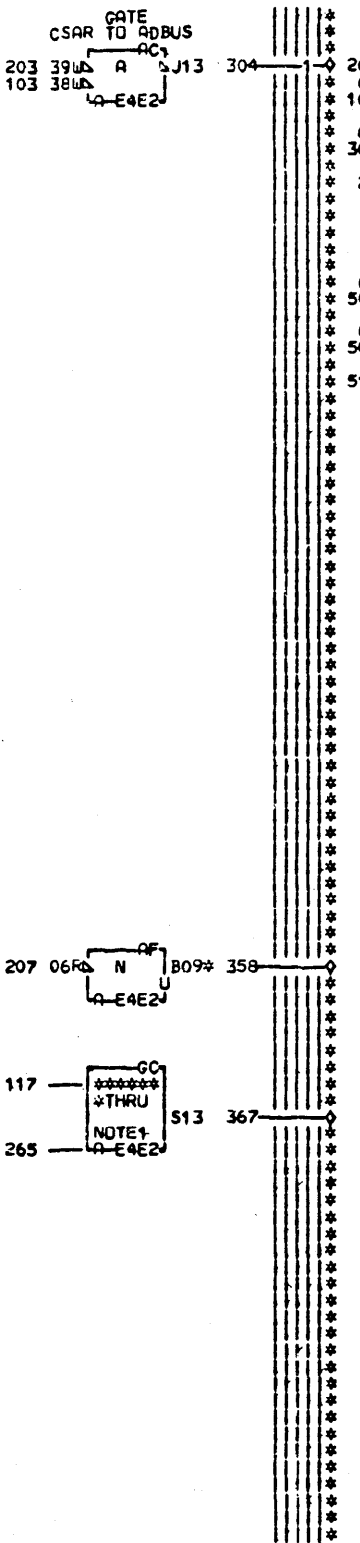
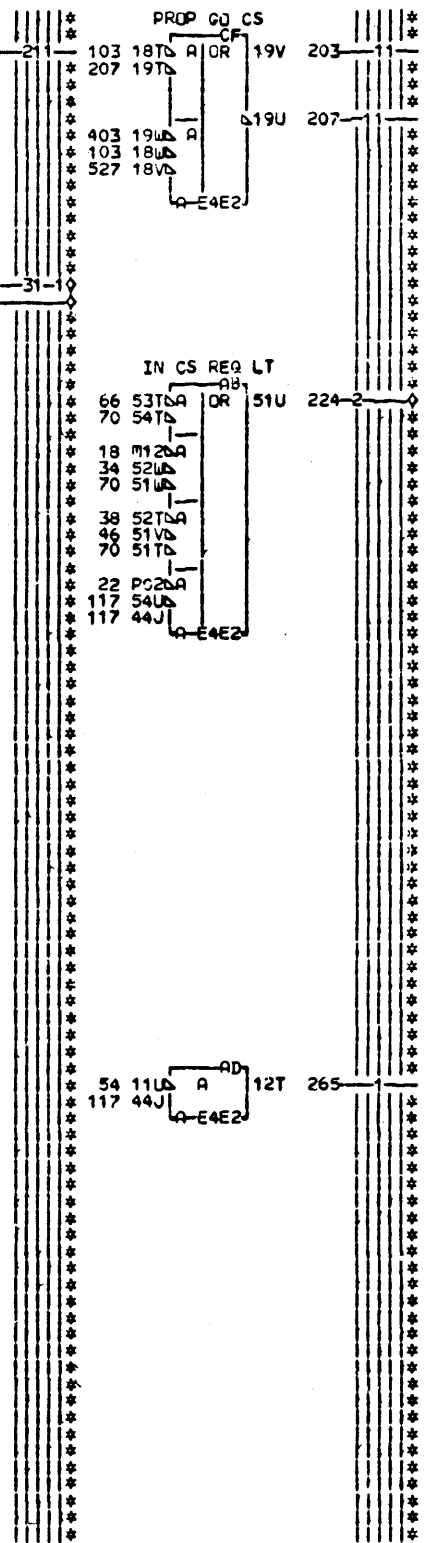
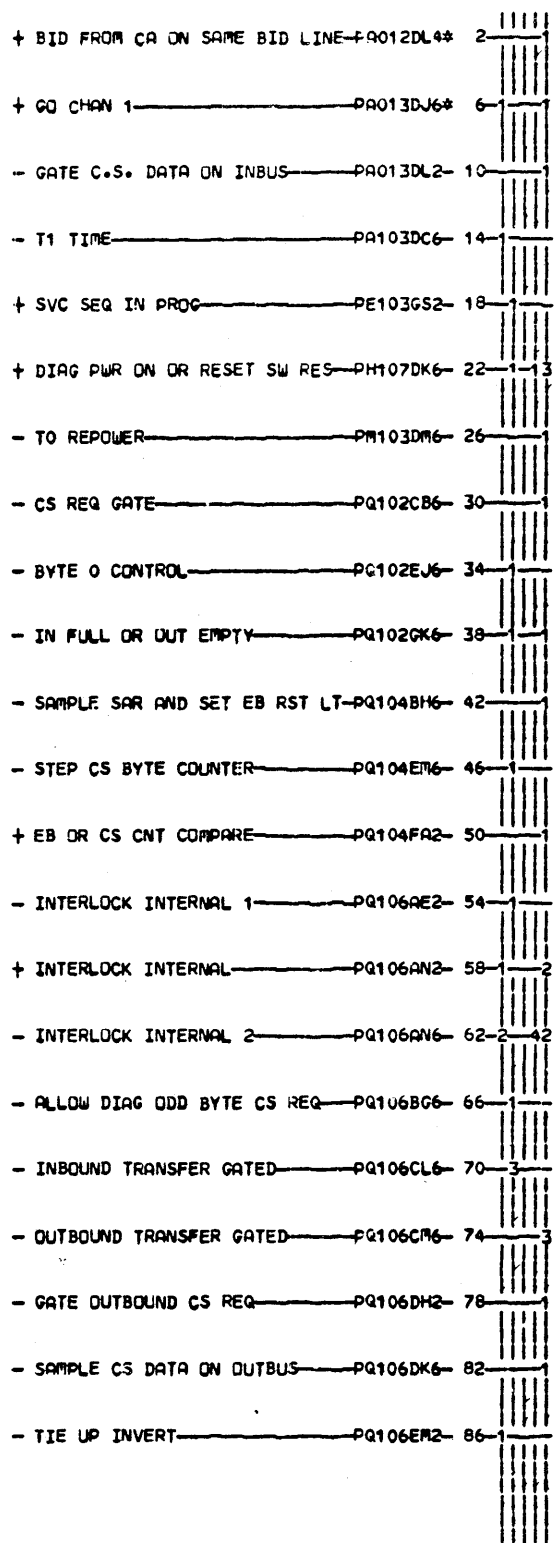
- 000 PP106
- 125 - GATE SET INIT SEL LATCHES—DB2  
PC105
  - 126 + BLK SET OF SVC SEL RST LT—DB6  
PC105
  - 418 - INPUT 60 TO TAG CNTRL—PC105-FF6
  - 404 + RST INIT SEL TO TAG CNTRL—FG2  
PC105

NOTE 1. IF BOARD IS AT EC  
LEVEL 3156200 SOURCE FOR  
- INTERLOCK IS PP101FB6

PP106  
000

LOC. TYPE  
A-E4D2 CE24

INITIAL SEL RST CONTROL AND SVC SEL RST CONTROL	
E-C-HISTORY—314402	E-MACH-27RNB
316677	FRAME 01
DATE LAST EC 01-09-78 318552	IBM CORP.SCD-PP106 P.N. 1755083 000

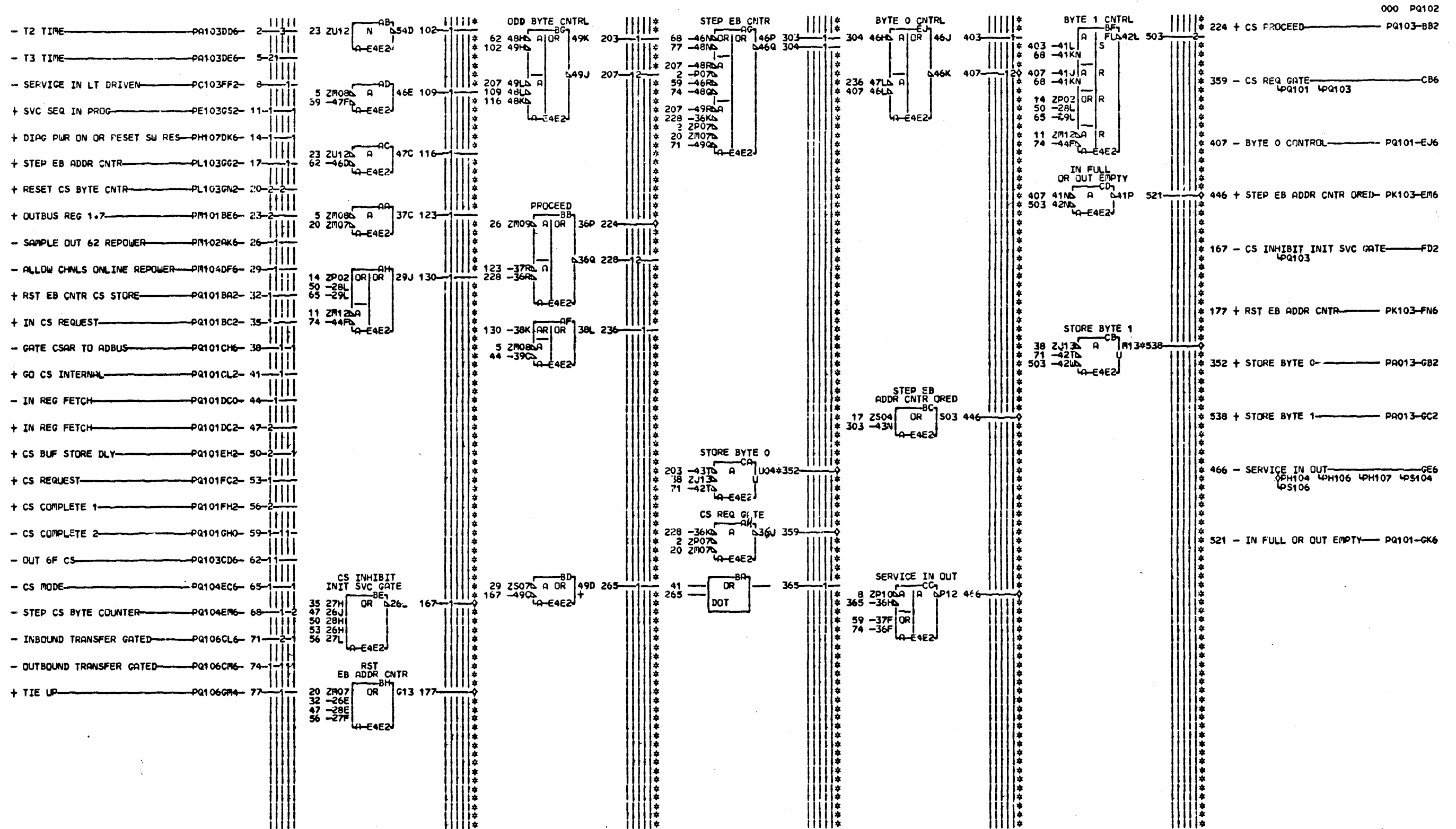


NOTE 1. THRU BLK VS CARD PN  
 TOP INPUT TO OUTPUT CONN  
 PN 8251998  
 BOTTOM INPUT TO OUTPUT CONN  
 PN 8254562

EDGE CONN.  
 2 RESISTOR  
 A-E4E2P11  
 6 RESISTOR  
 A-E4E2U02  
 358 A-E4B5B12  
 544 A-E4C3D06

LOC. TYPE  
 A-E4E2 CE25

CS SEQUENCING CONTROL	
E.C.—HISTORY—E-MACH.27RNB	
314402	FRAME 01
314424	
316677	
DATE LAST EC	IBM CORP.SCD PQ101
01-09-78 318552	P.N. 1755084 000

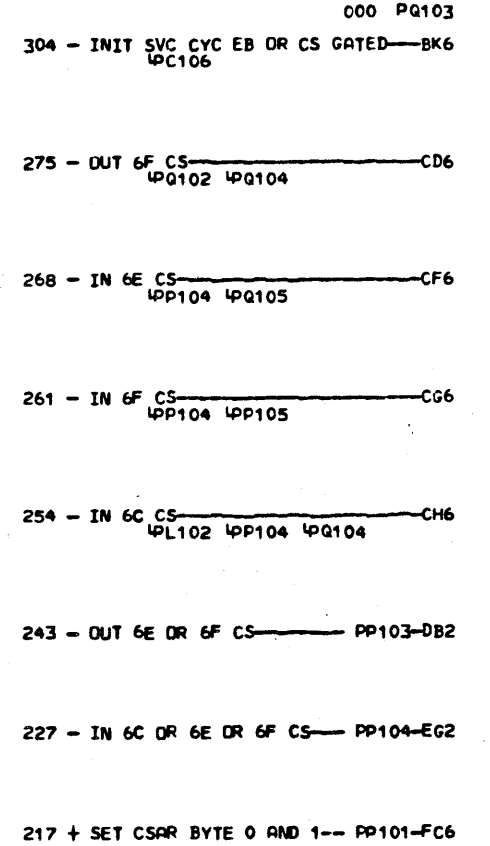
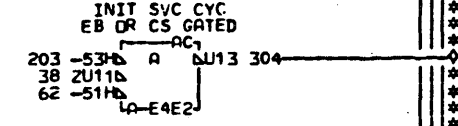
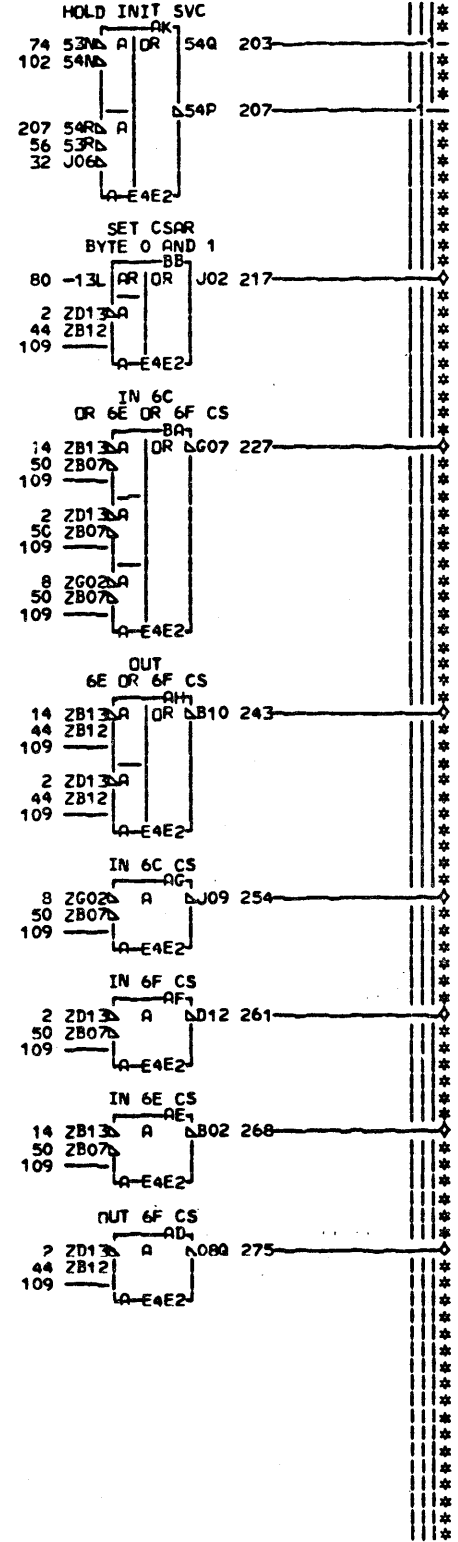
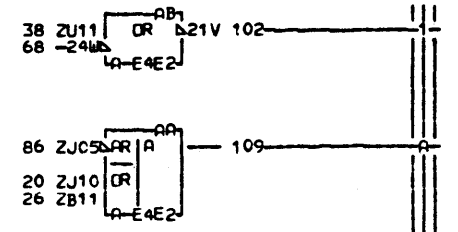
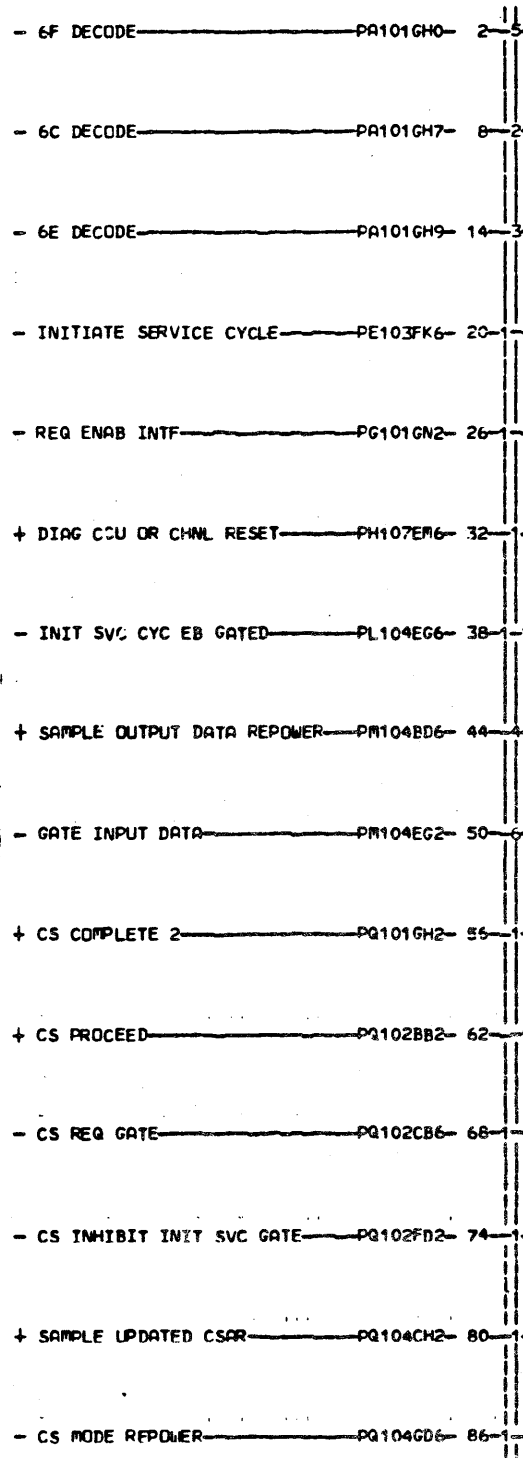


EDGE CONN.  
 352 A-E4B3D11  
 01A-E4B5D11  
 538 A-E4B3D13  
 01A-E4B5D13

LOC. TYPE  
 A-E4E2 CE25

CS BUFFER CONTROL AND ODD BYTE TRANSFER CONTROL	
E-C-HISTORY	E-MACH-27RNB
314402	FRAME 01
314424	IBM CORP.SCD
316677	PQ102
DATE LAST EC	P.N. 1755085 000
01-09-78 318552	



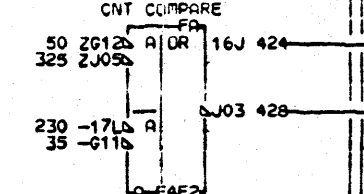
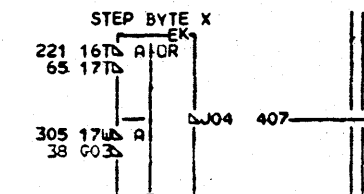
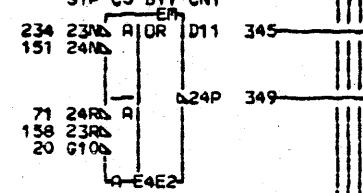
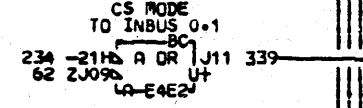
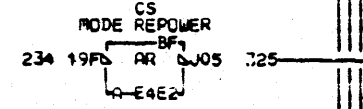
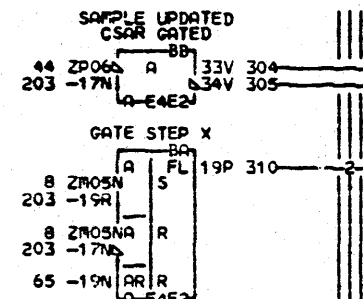
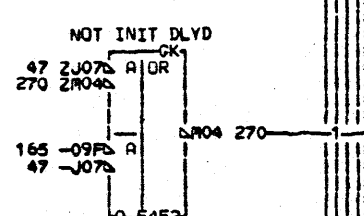
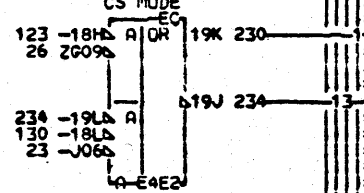
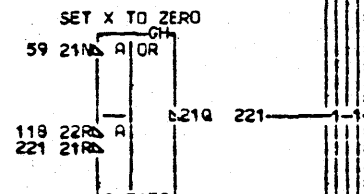
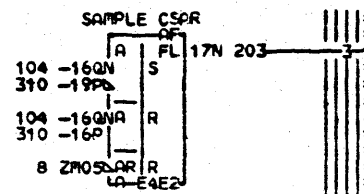
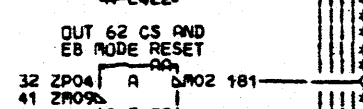
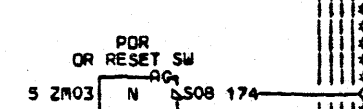
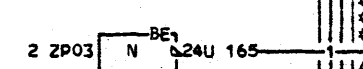
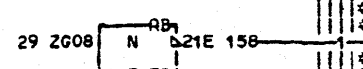
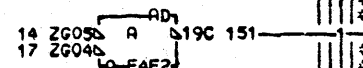
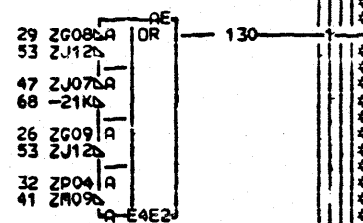
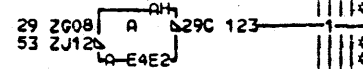
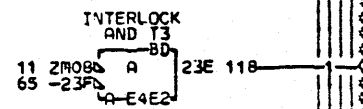
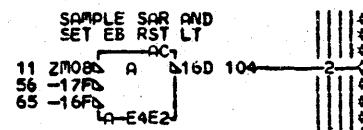


LOC. TYPE  
A-E4E2 CE25

PQ103  
000

CS DECODES AND INBUS GATING			
F.C. HISTORY	D. MACH. 27RMA	FRAME	01
314402		IBN CORP. SCD	PQ103
314424		P.N. 1755086	000
DATE	LAST EC		
11-19-76	316677		

+ RESET — PA012DE6 — 2  
 + POR OR RESET SW — PA013DH5 — 5  
 - T1 TIME — PA103DC6 — 8  
 - T3 TIME — PA103DE6 — 11  
 - CLOCK 3 — PC101DC6 — 14  
 - INCREMENT COUNTER — PC103AX6 — 17  
 - SAMPLE 67 — PF101EN6 — 20  
 + DIAG CCU OR CHNL RESET — PH107EM6 — 23  
 + OUTBUS REG 0.0 — PK101CG6 — 26  
 + OUTBUS REG 0.1 — PK101CJ6 — 29  
 + OUTBUS REG 0.7 — PK101DK6 — 32  
 - COMPARE COUNT EB — PK103CB7 — 35  
 - CCU OUTBUS EQUALS 0 — PM101EB2 — 38  
 - SAMPLE OUT 62 REPOWER — PA102AK6 — 41  
 - TO REPOWER — PM103DM6 — 44  
 - NOT INITIALIZED REPOWER — PM104BF6 — 47  
 - CS COMPARE COUNT — PP102EB4 — 50  
 - OUT 6C REPOWER — PM105BN2 — 53  
 - GO CS INTERNAL — PQ101DH0 — 56  
 - OUT 6F CS — PQ103CD6 — 59  
 - IN 6C CS — PQ103CH6 — 62  
 - INTERLOCK INTERNAL 2 — PQ106AN6 — 65  
 + INITIATE SERVICE CYCLE — PQ106FJ2 — 68  
 + REQ INTF ENAB — PQ106GJ2 — 71



000 PQ104

181 - OUT 62 CS AND EB MODE RESET — BA6  
LPL102

104 - SAMPLe SAR AND SET EB RST LT — BH6  
LPQ101 LPQ105

203 + SAMPLe UPDATED CSAR — PQ103-CH2

174 - POR OR RESET SW — PA108-DA2

304 + SAMPLe UPDATED CSAR GATED — DL7  
LPQ105

234 - CS MODE — EC6  
LPQ102 LPQ106

407 - STEP BYTE X — PP103-EK6

345 + STEP CS BYTE COUNTER — EM2  
LPQ102 LPQ105

349 - STEP CS BYTE COUNTER — EM6  
LPQ101 LPQ102 LPQ106

424 + EB OR CS CNT COMPARE — PQ101-FA2

428 - EB OR CS CNT COMPARE — FA6  
LPL105 LPQ106

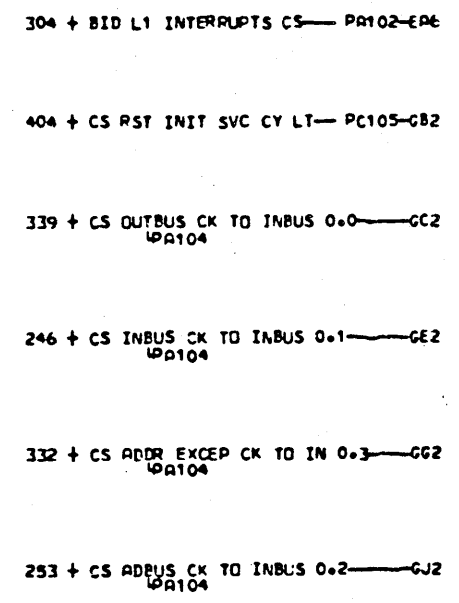
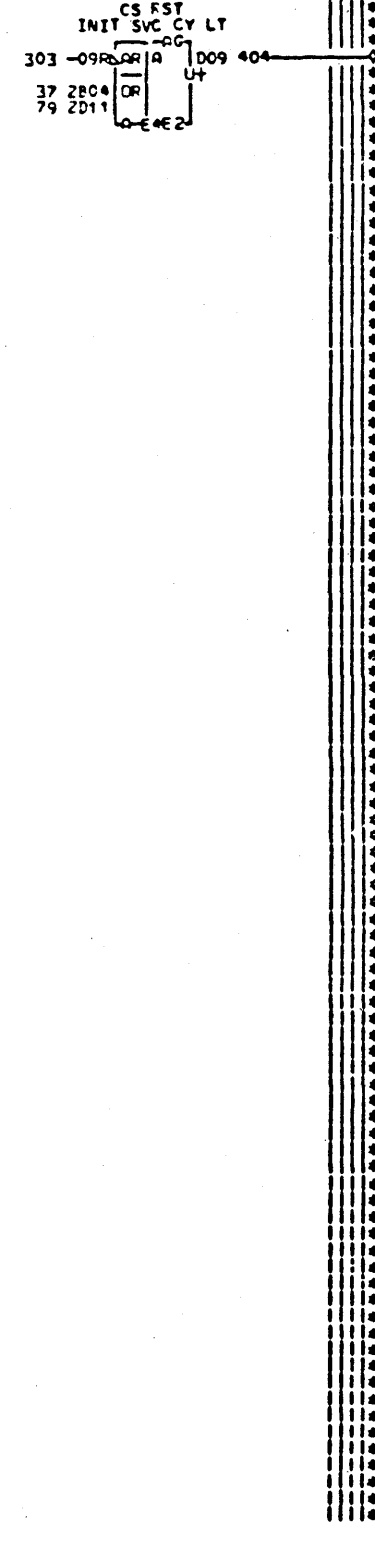
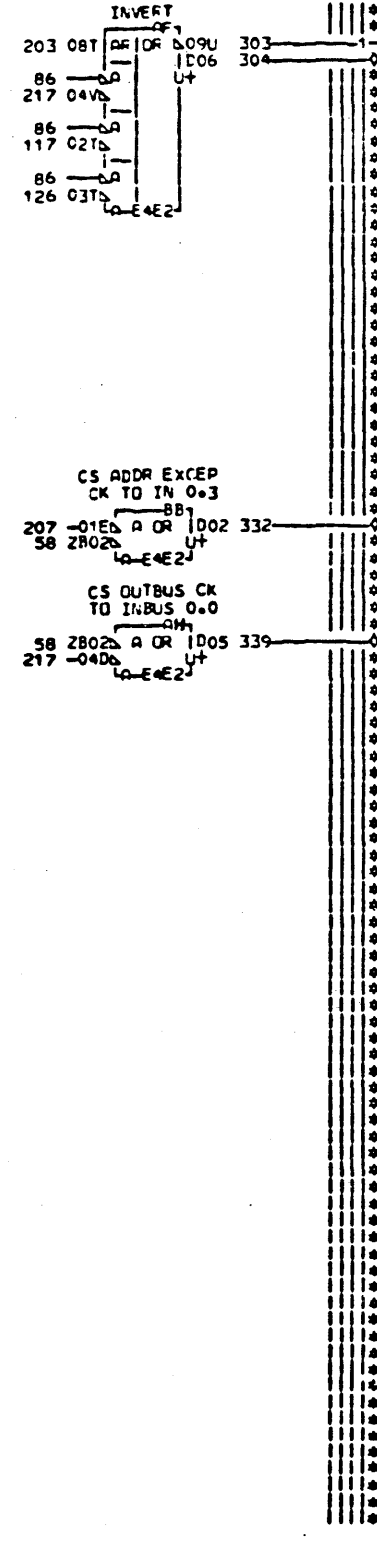
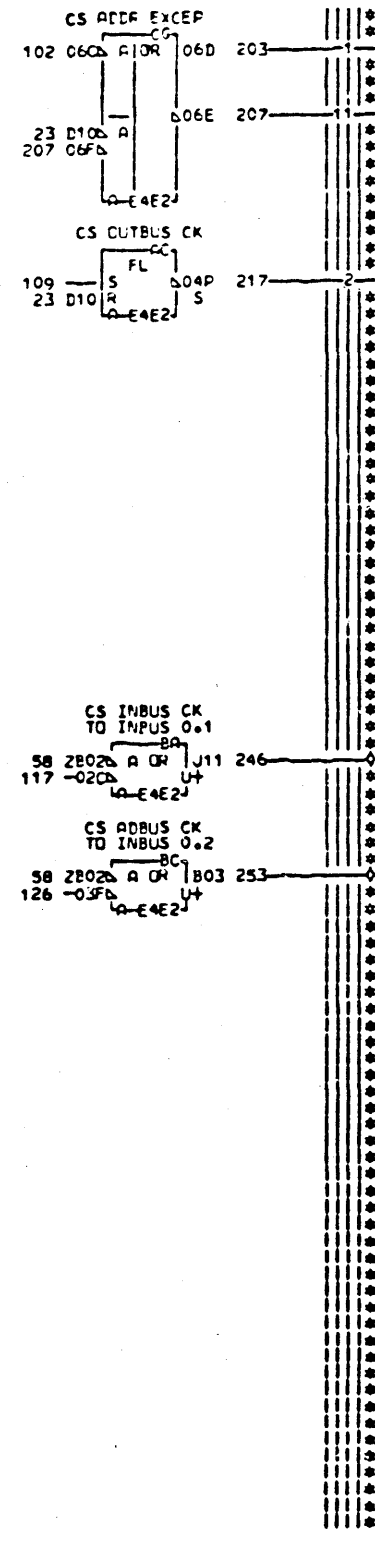
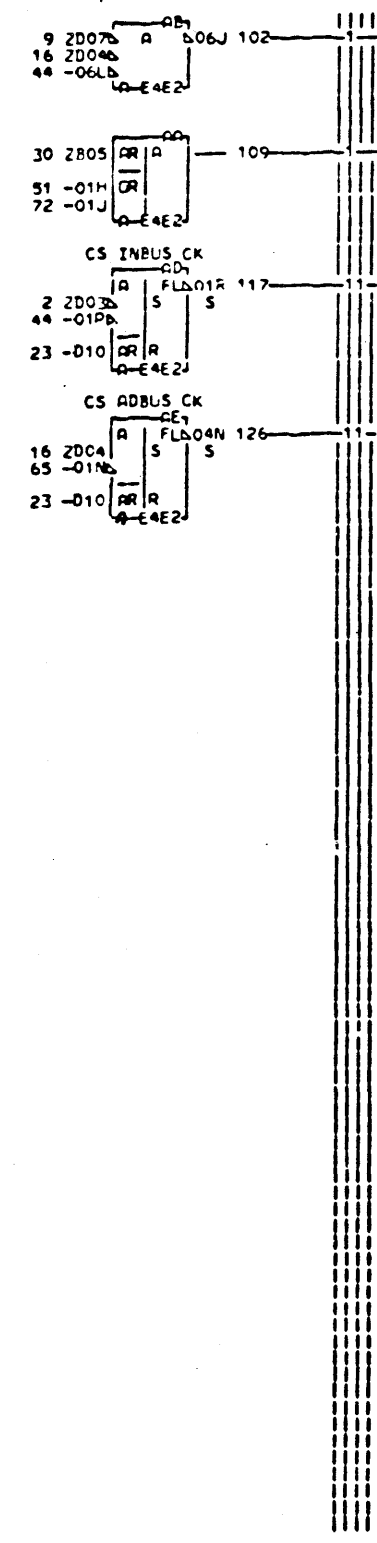
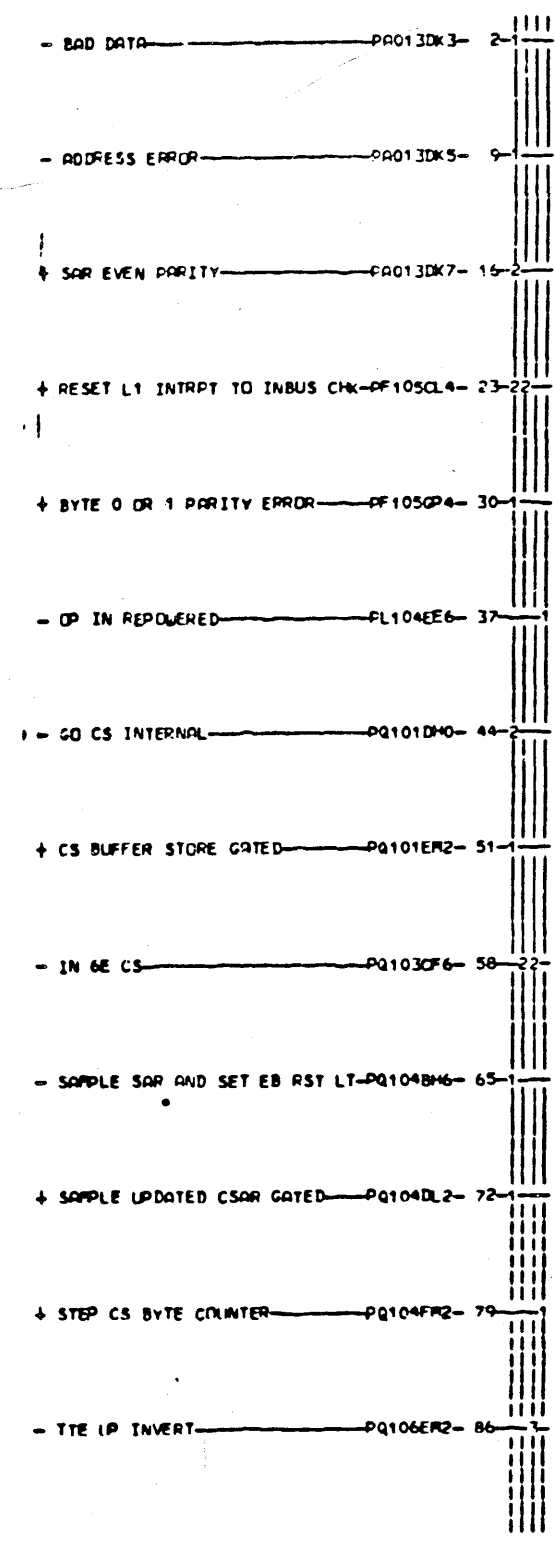
339 + CS MODE TO INBUS 0.1 — PA104-FD2

118 + INTERLOCK AND T3 — PQ106-FH2

325 - CS MODE REPOWER — GD6  
LPL102 LPQ103

270 - NOT INITIALIZED DLYD — PA108-GK6

LOC. TYPE  
A-E4E2 CE25

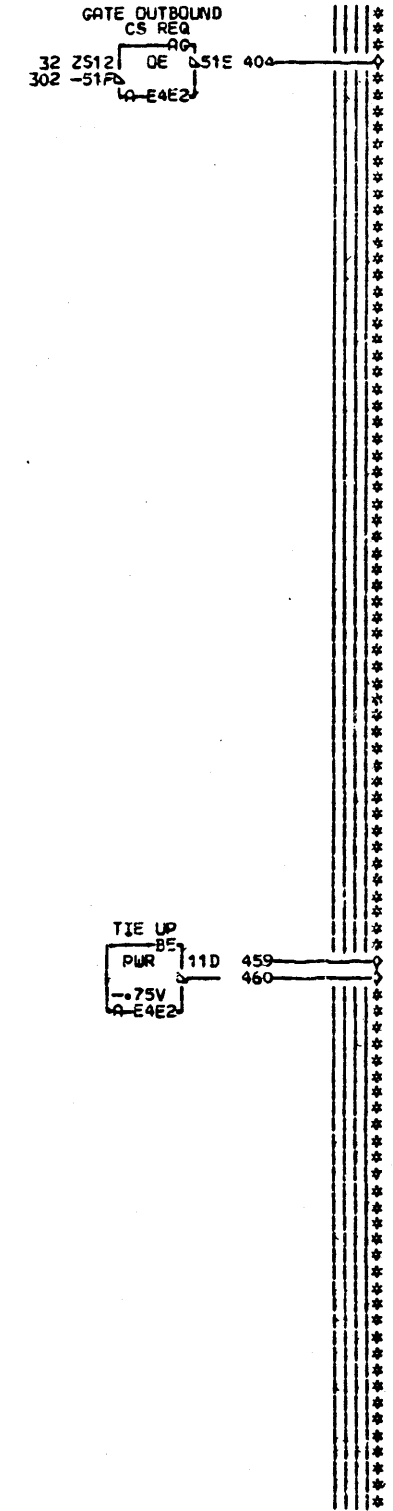
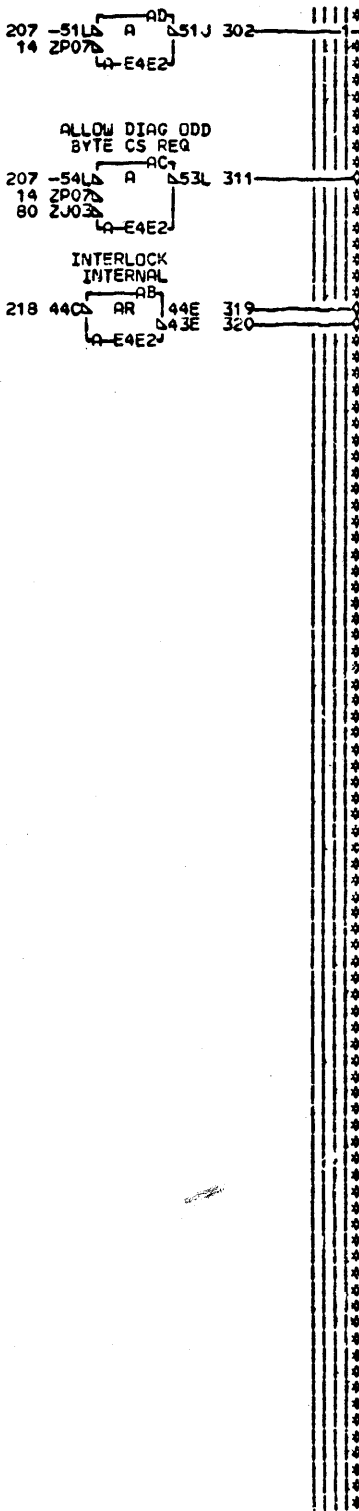
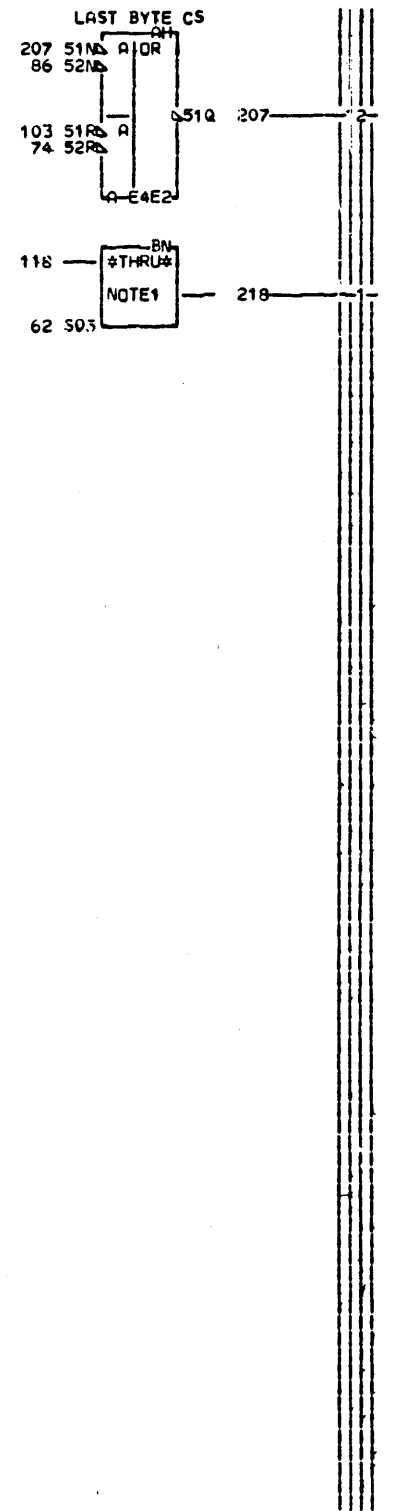
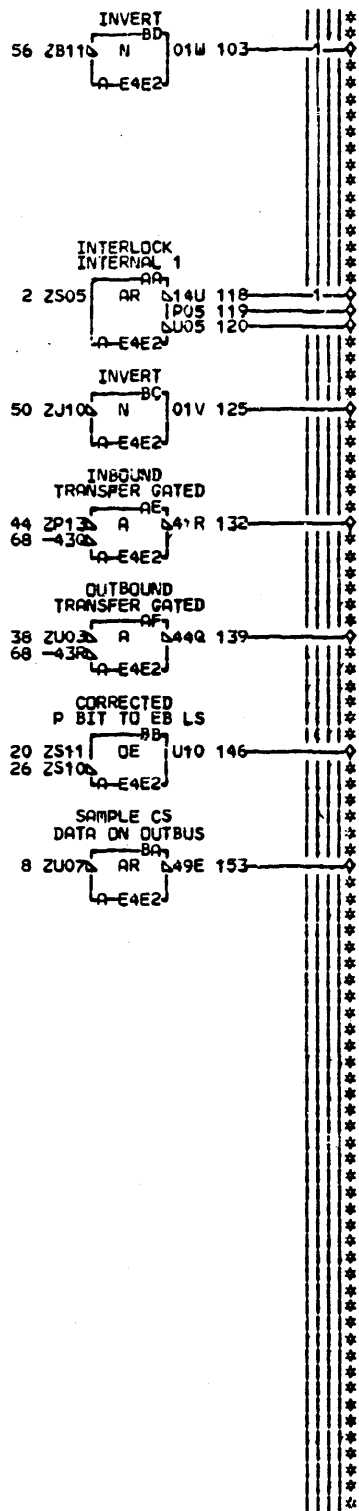
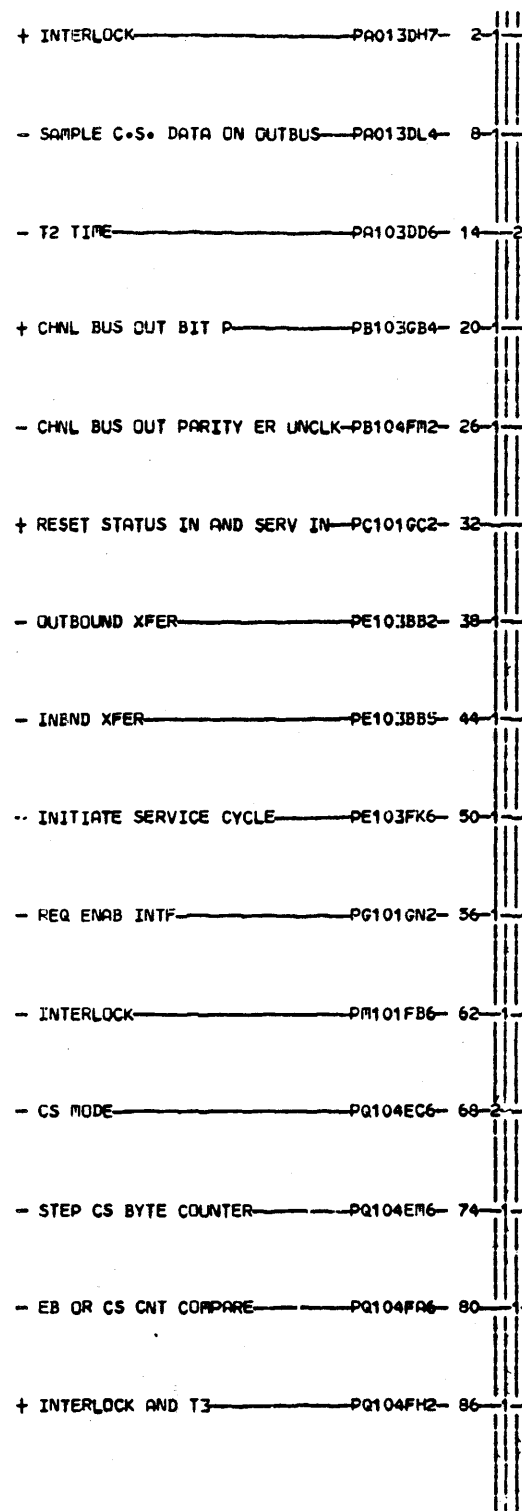


LOC. TYPE  
A-E4E2 CE25

PQ105  
000

CYCLE STEAL CHECK LATCHES			
E.C. HISTORY	D. PACH. 27RNB	FRAME	01
314402		INR CORR. SCD	PQ105
DATE	LAST FC	P.No.	1755088 000
11-19-76	316677		

1755088



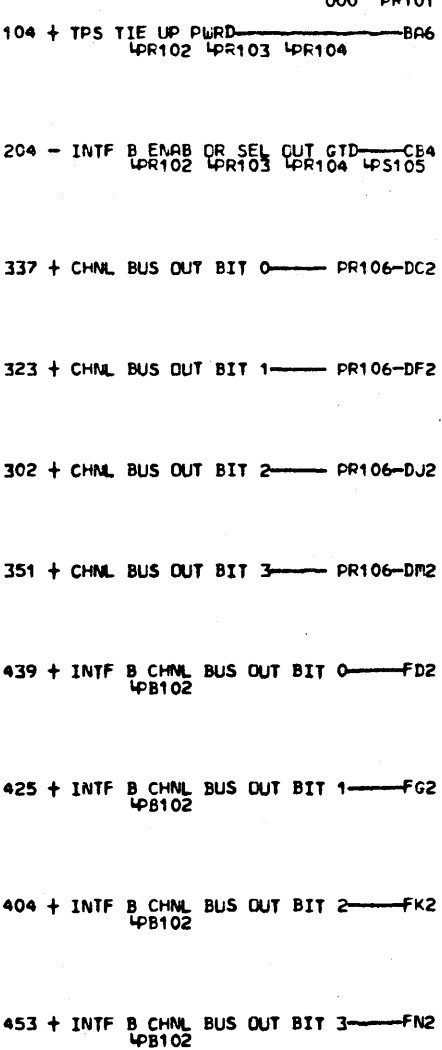
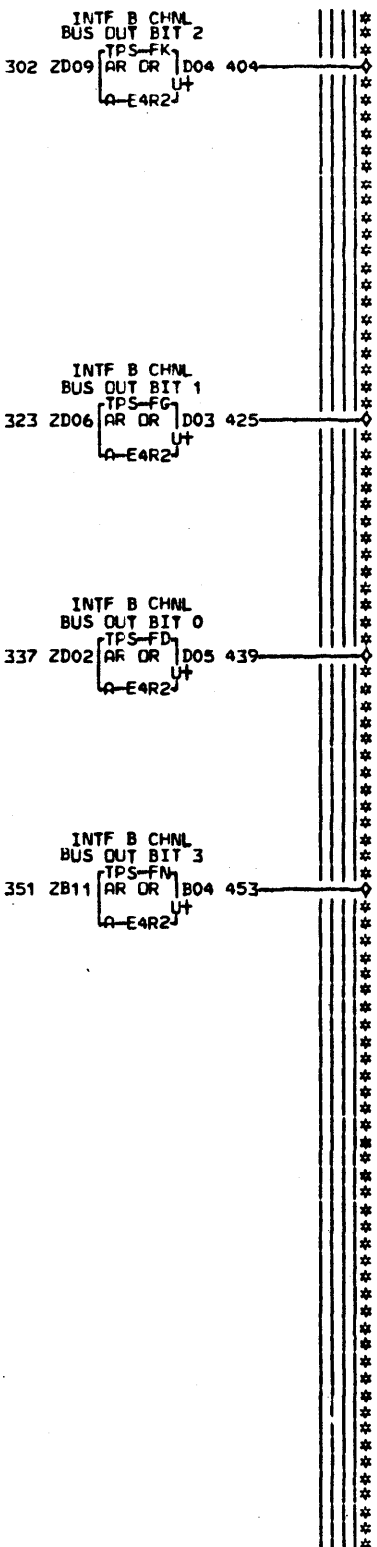
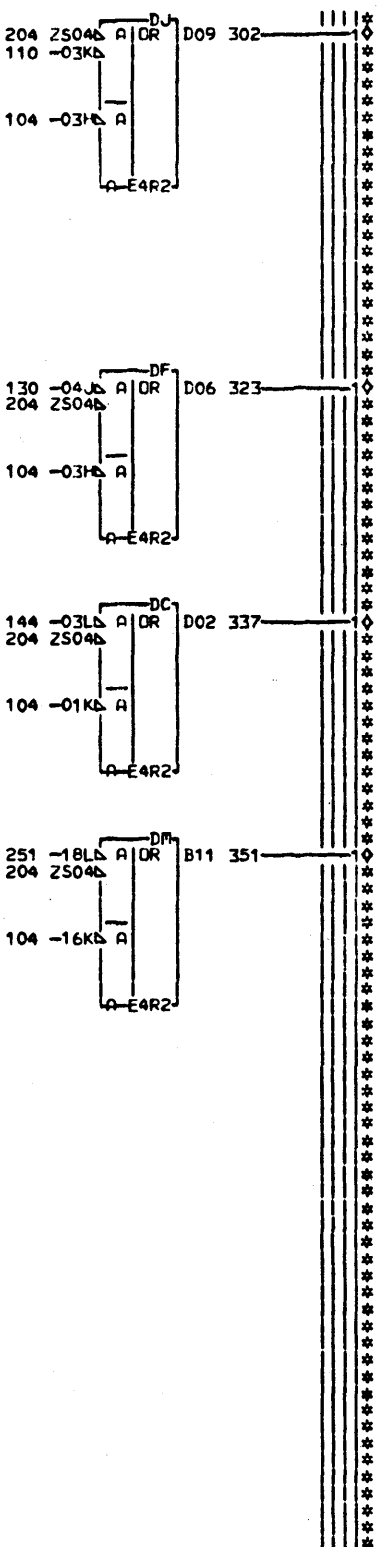
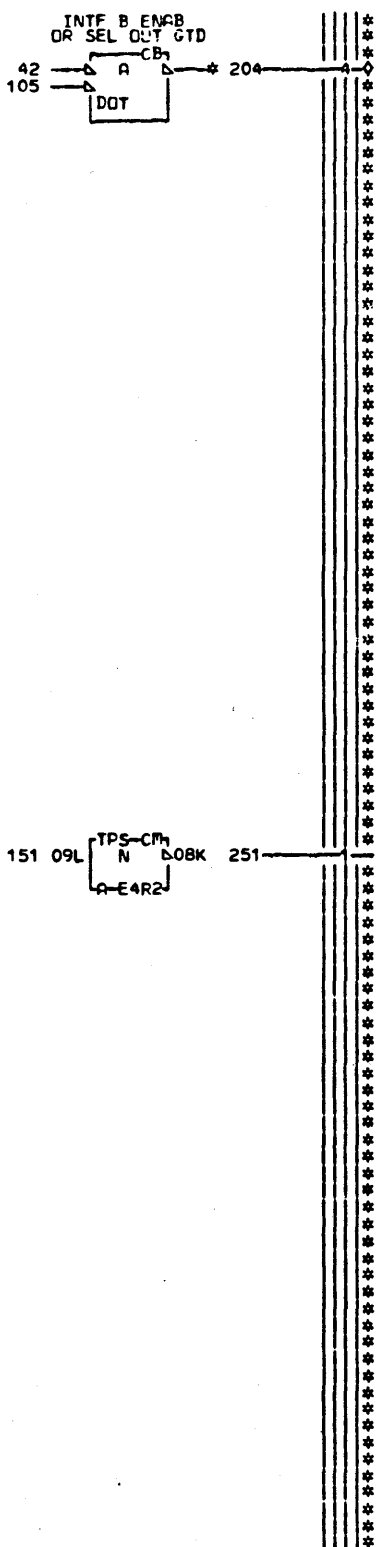
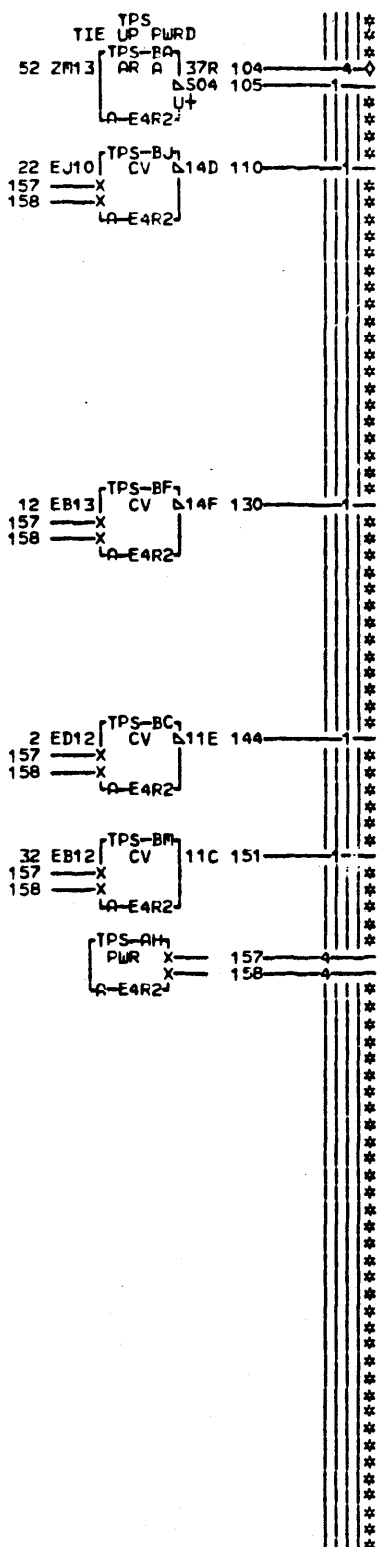
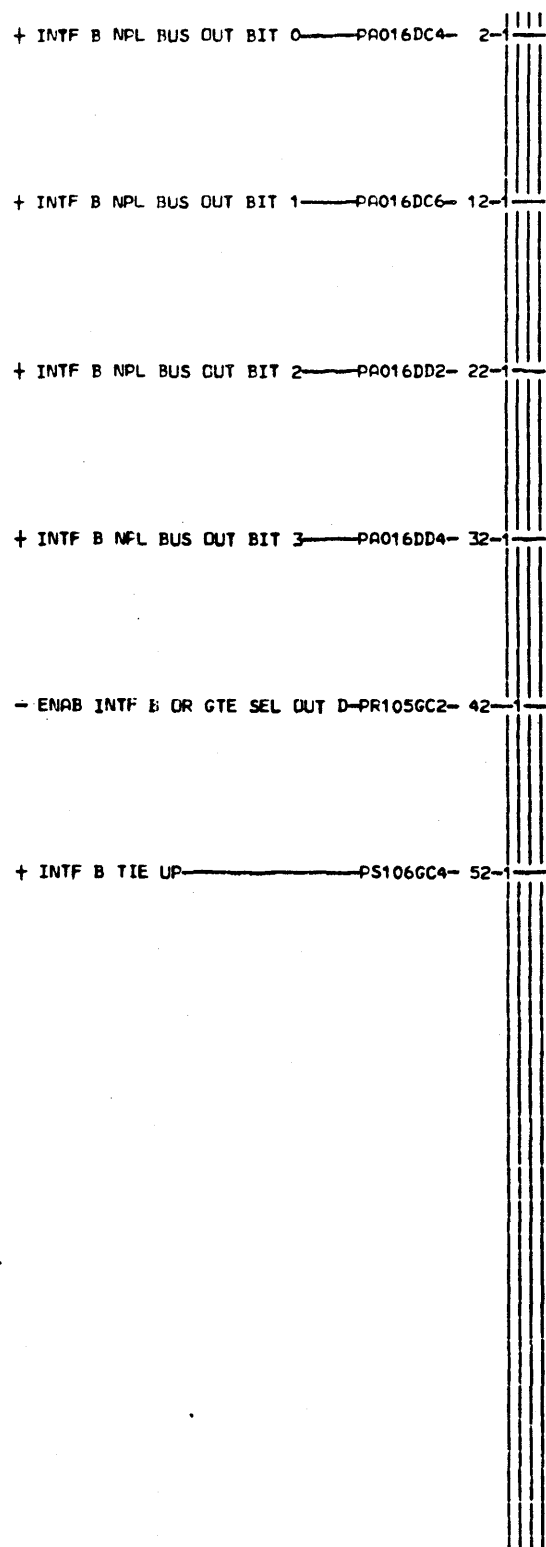
NOTE 1. THRU BLK VS CARD PN  
TOP INPUT TO OUTPUT CONN  
PN 8254562  
BOTTOM INPUT TO OUTPUT CONN  
PN 8251998

PQ106  
000

LOC. TYPE  
A-E4E2 CE25

- 000 PQ106
- 118 - INTERLOCK INTERNAL 1 - PQ101-AE2
- 319 + INTERLOCK INTERNAL - PQ101-AN2
- 320 - INTERLOCK INTERNAL 2 - AN6  
PQ101 PQ104
- 311 - ALLOW DIAG ODD BYTE CS REQ - BG6  
PQ101
- 119 + INTERLOCK REDRVEN - PR101-CE2
- 120 - INTERLOCK REDRVEN - CE6  
PE107 PL104 PP106
- 132 - INBOUND TRANSFER GATED - CL6  
PQ101 PQ102
- 139 - OUTBOUND TRANSFER GATED - CH6  
PQ101 PQ102
- 404 - GATE OUTBOUND CS REQ - PQ101-DH2
- 153 - SAMPLE CS DATA ON OUTBUS - DK6  
PQ101
- 146 + CORRECTED P BIT TO EB LS - EJ2  
PK102
- 460 - TIE UP INVERT - EP2  
PQ101 PQ105
- 125 + INITIATE SERVICE CYCLE - PQ104-FJ2
- 103 + REQ INTF ENAB - PQ104-GJ2
- 459 + TIE UP - PQ102-GR4

REPOWERING	
E-C-HISTORY	E-MACH.27RNB
314402	FRAME 01
314424	IBM CORP.SCD
316677	PQ106
DATE LAST EC	P.N. 1755089
01-09-78 318552	000



EDGE CONN.  
204 RESISTOR  
A-E4R2504

LOC. TYPE  
A-E4R2 6837

PR101  
000

INTF B BUS OUT RECEIVERS	
BITS 0-3	
E.C. HISTORY	C. MACH. 27RNB
DATE	FRAME 01
LAST EC	IBM CORP. SDD PR101
02-23-76 314402	P. No. 1755090 000

+ INTF B NPL BUS OUT BIT 4 — PA016DD6 — 2-111

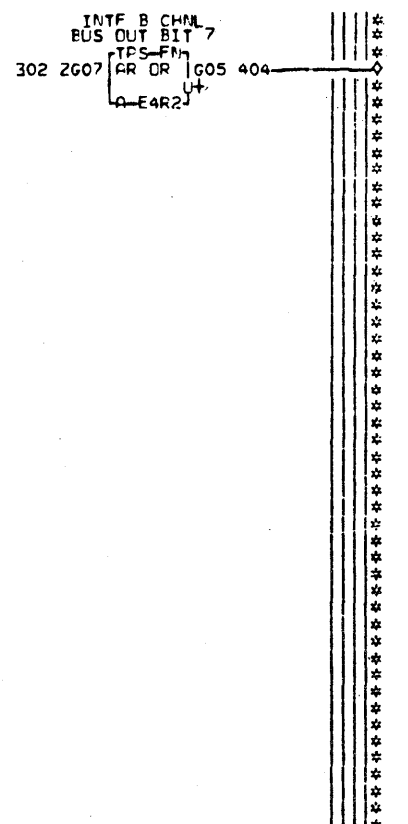
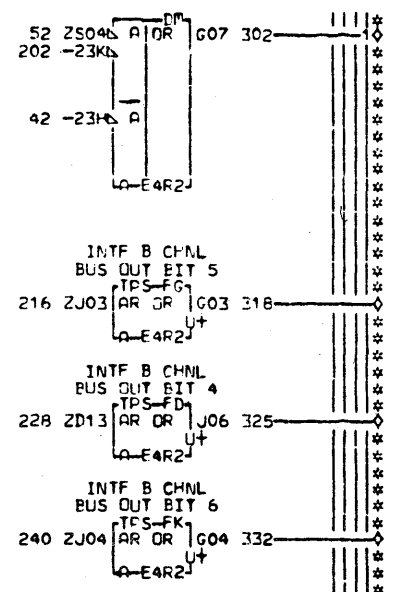
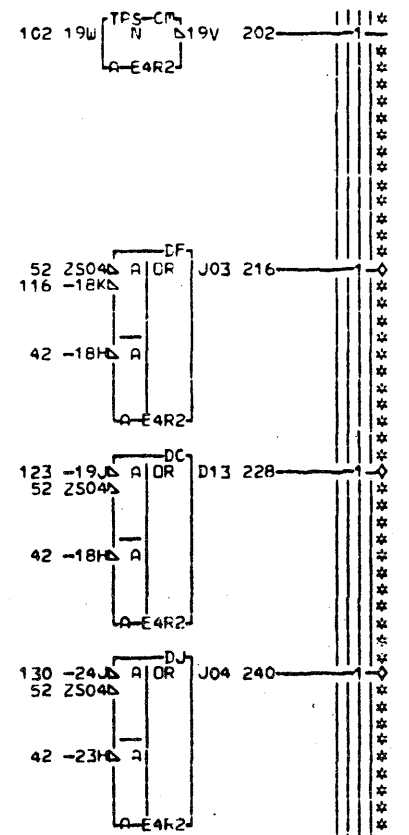
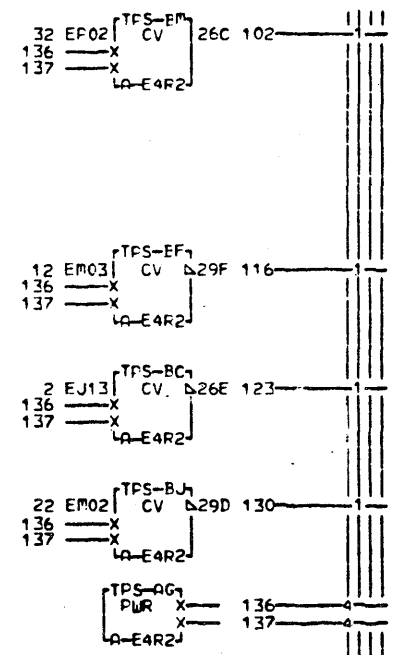
+ INTF B NPL BUS OUT BIT 5 — PA016DE2 — 12-1

+ INTF B NPL BUS OUT BIT 6 — PA016DE4 — 22-1

+ INTF B NPL BUS OUT BIT 7 — PA016DE6 — 32-1

+ TPS TIE LP PWRD — PR101BA6 — 42-31

- INTF B EIAB DR SEL OUT GTD — PR101CB4 — 52-31



000 PR102

228 + CHNL BUS OUT BIT 4 — PR106-DC2

216 + CHNL BUS OUT BIT 5 — PR106-DF2

240 + CHNL BUS OUT BIT 6 — PR106-DJ2

302 + CHNL BUS OUT BIT 7 — PR106-DM2

325 + INTF B CHNL BUS OUT BIT 4 — FD2  
WPB102

318 + INTF B CHNL BUS OUT BIT 5 — FG2  
WPB102

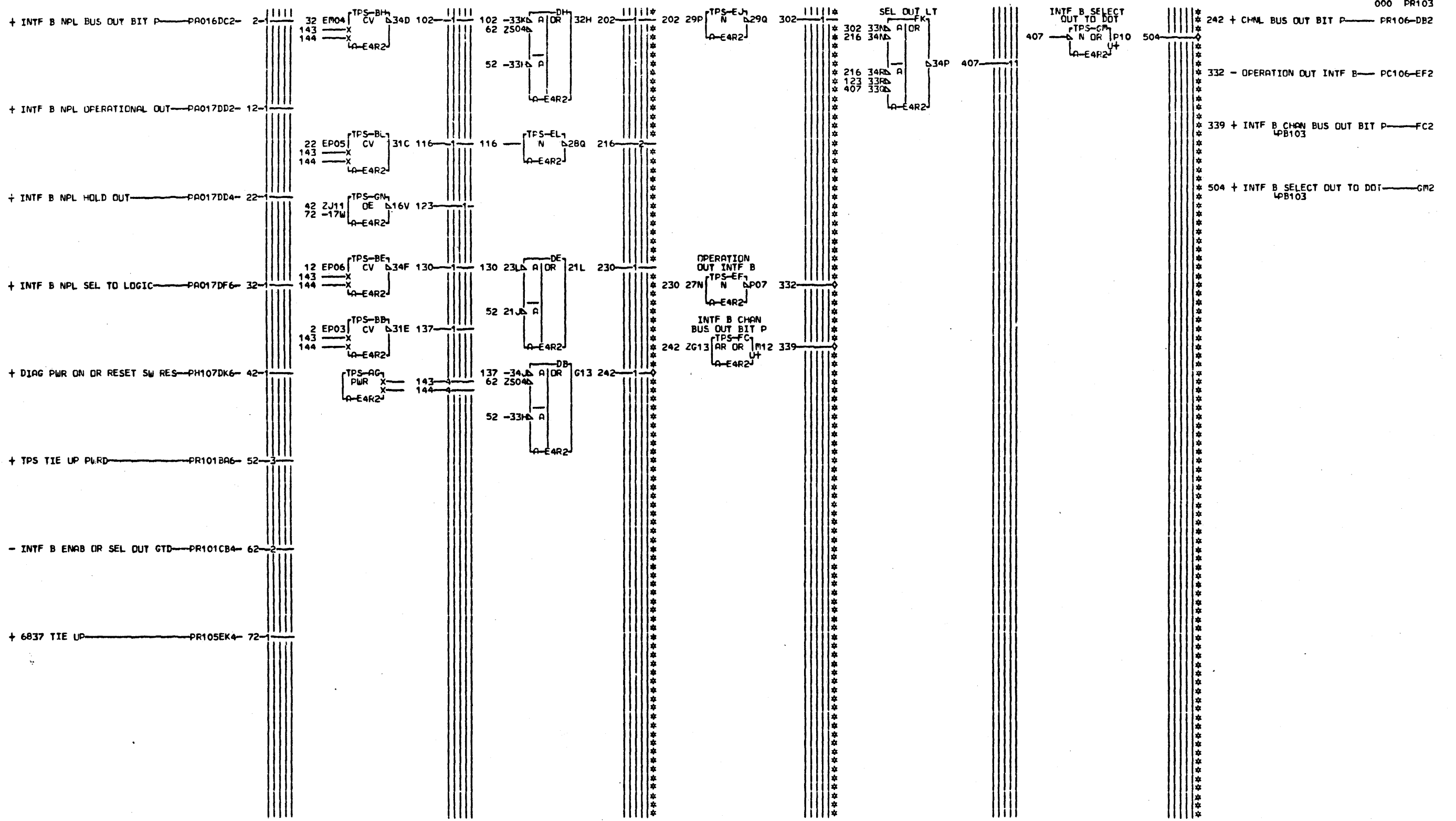
332 + INTF B CHNL BUS OUT BIT 6 — FK2  
WPB102

404 + INTF B CHNL BUS OUT BIT 7 — FN2  
WPB102

LOC. TYPE  
A-E4R2 6837

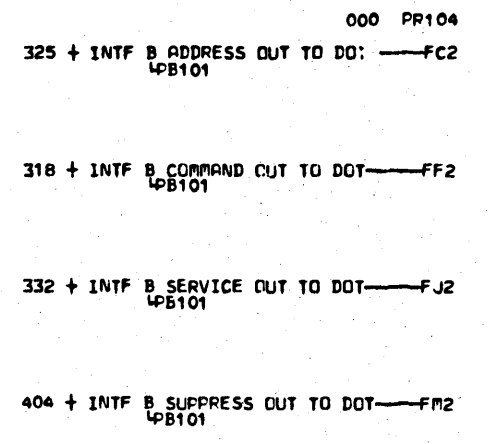
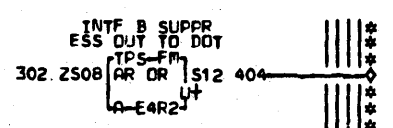
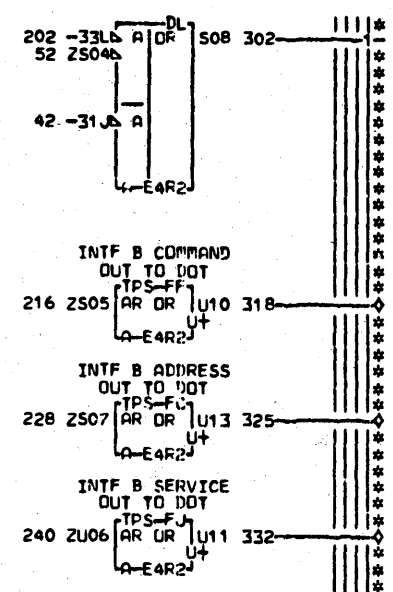
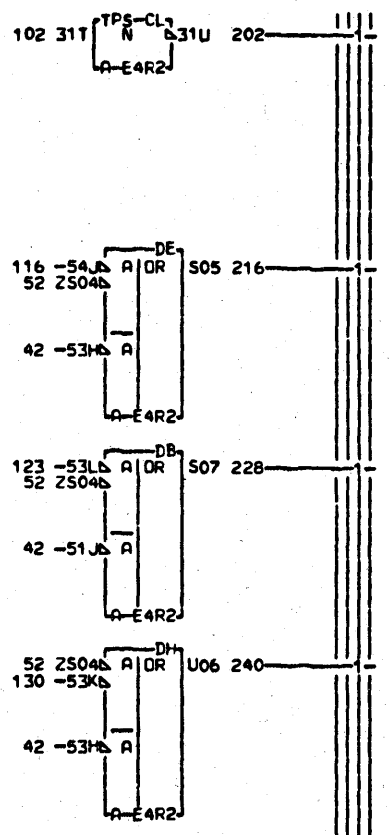
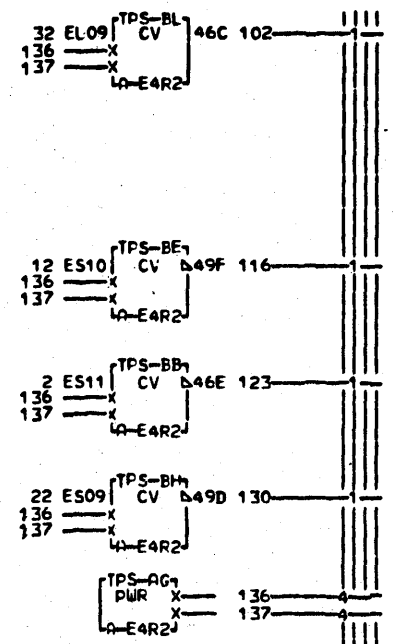
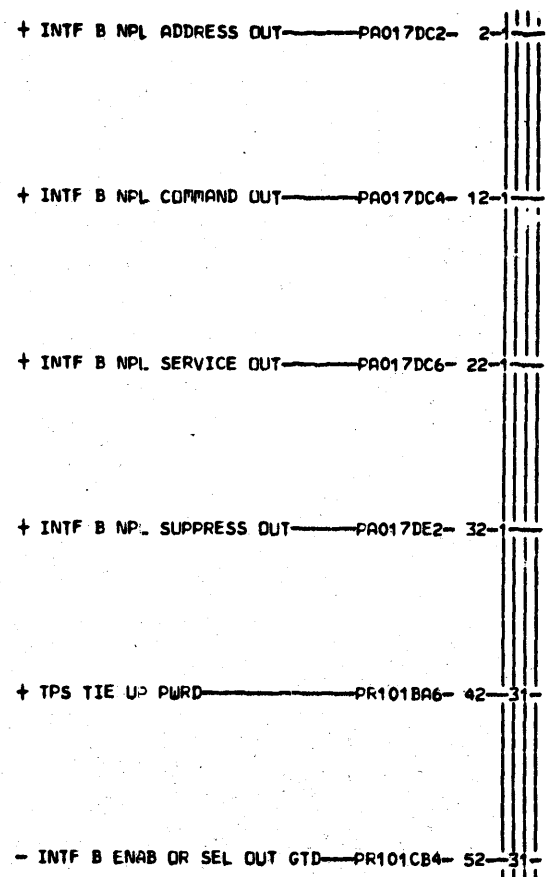
PR102  
000

+ INTF B BUS OUT RECEIVERS BITS 4-7	
- E.C. HISTORY -	C1 MACH. 27RNB
DATE	LAST EC
02-23-76	314402
FRAME	01
IBM CORP. SDD	PR102
P.No.	1755091
000	



LOC. TYPE  
A-E4R2 6837

INTF B BUS OUT RCVR BIT P AND TAGS OUT RCVR OP OUTQ SEL OUT		FRAME	01
-E.C.-HISTORY-		C-MACH	27RNB
DATE	LAST EC	IBM CORP	SDD PR103
02-23-76	314402	P.No.	1755092 000

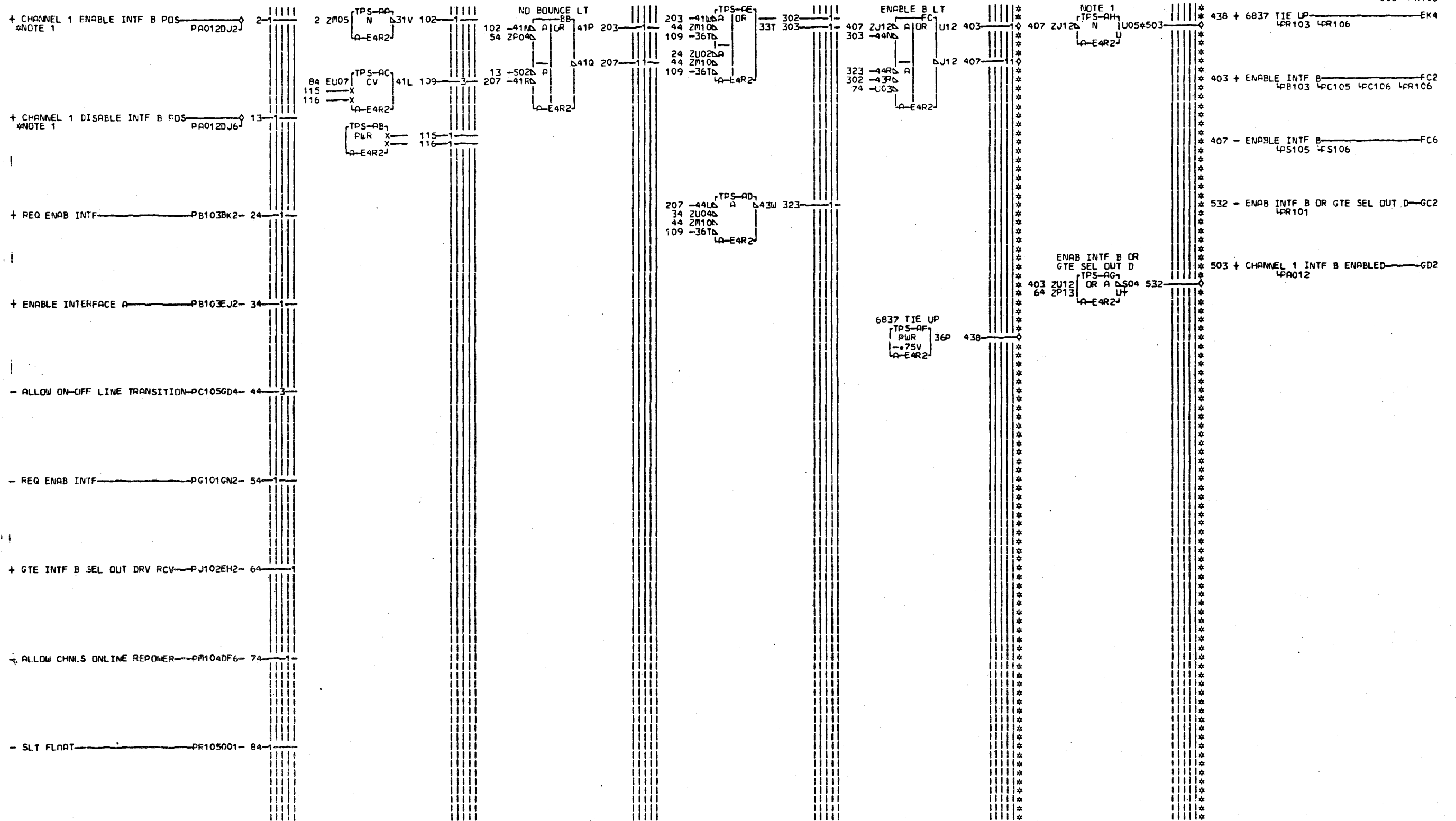


LDC TYPE  
A-E4R2 6837

PR104  
000

INTF B TAGS OUT RCVR ADR OUT		CMD OUT SVC OUT AND SUPP OUT	
E-C-HISTORY-C		MACH-27RNB	
FRAME	01	IBN CORP-SDD	PR104
DATE	LAST EC	PoN	1755093 000
02-23-76	314402		





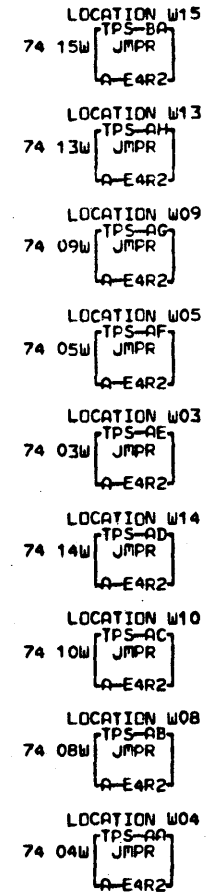
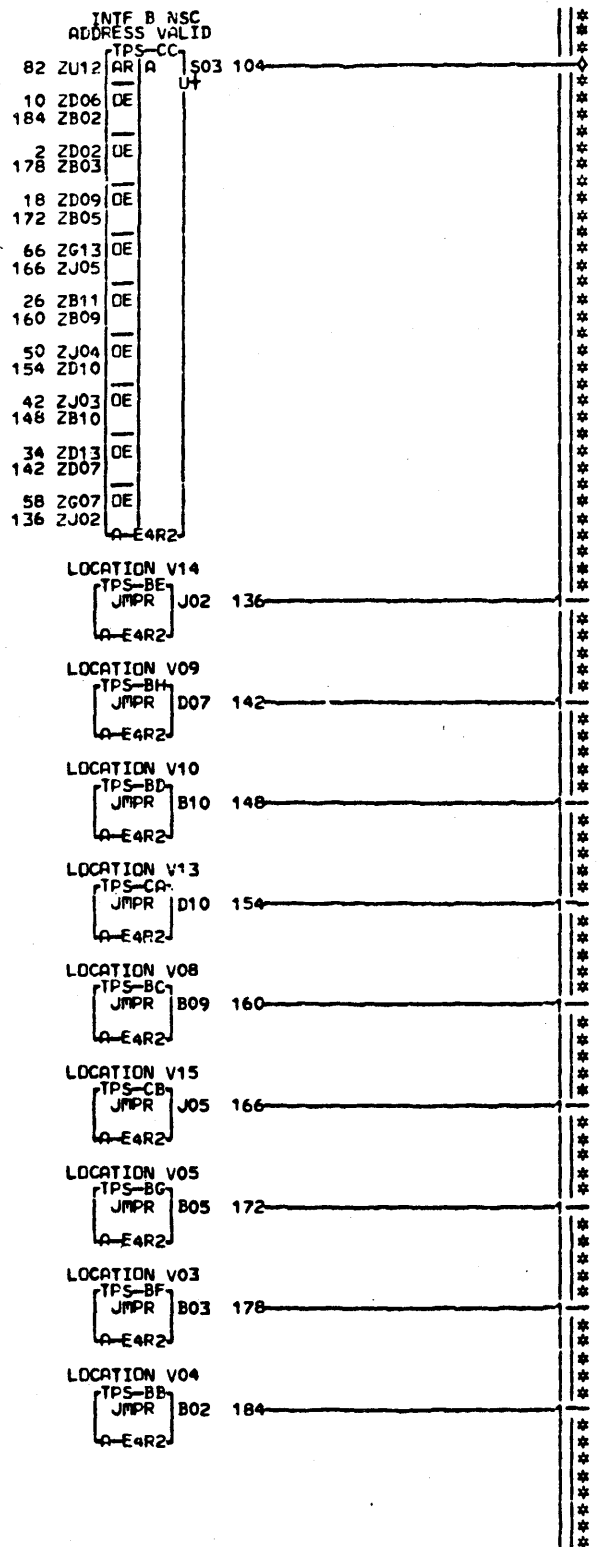
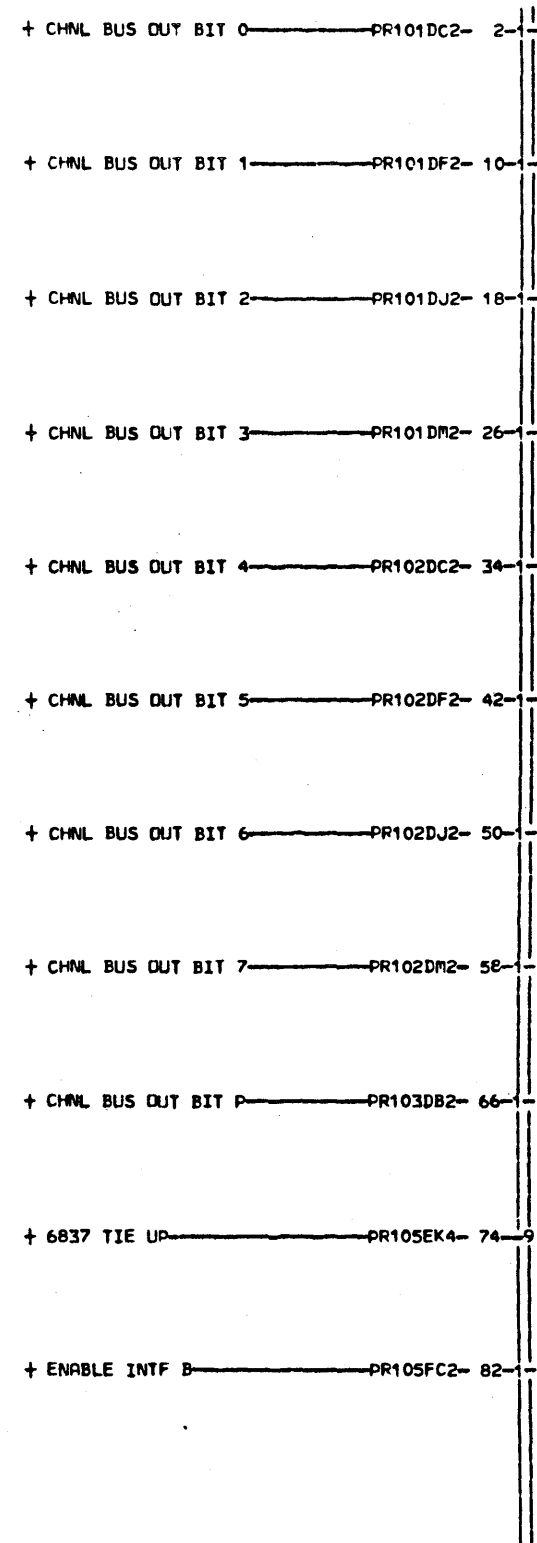
NOTE 1 SEE PA061 PGS 102  
 3 FOR ENABLE/DISABLE  
 WIRING CHANGES IF  
 MORE THAN 2 CA-35 ARE  
 INSTALLED

EDGE CONN.  
 503 A-E4C3D03  
 01A-E4C5D05

LOC. TYPE  
 A-E4R2 6837

PR105  
 000

ENABLE B LATCH			
E.C. HISTORY	MACH. 27RNB		
314402	FRAME 01		
314424	IBM CORP. SDD	PR105	
DATE	LAST EC	P.No.	1755094 000
11-19-76	316677		



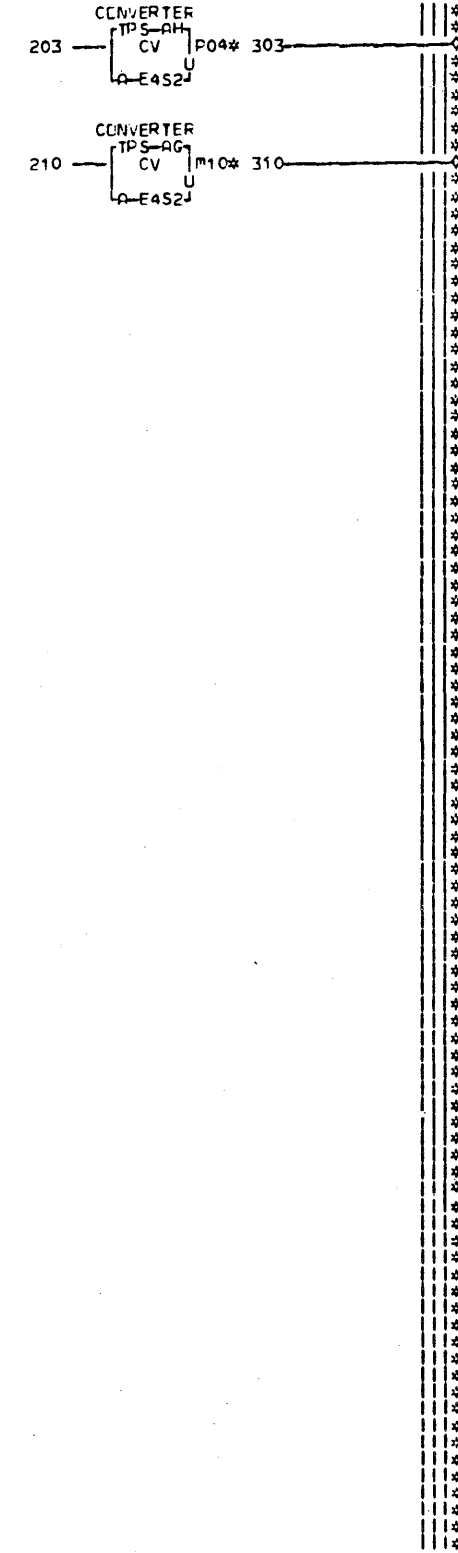
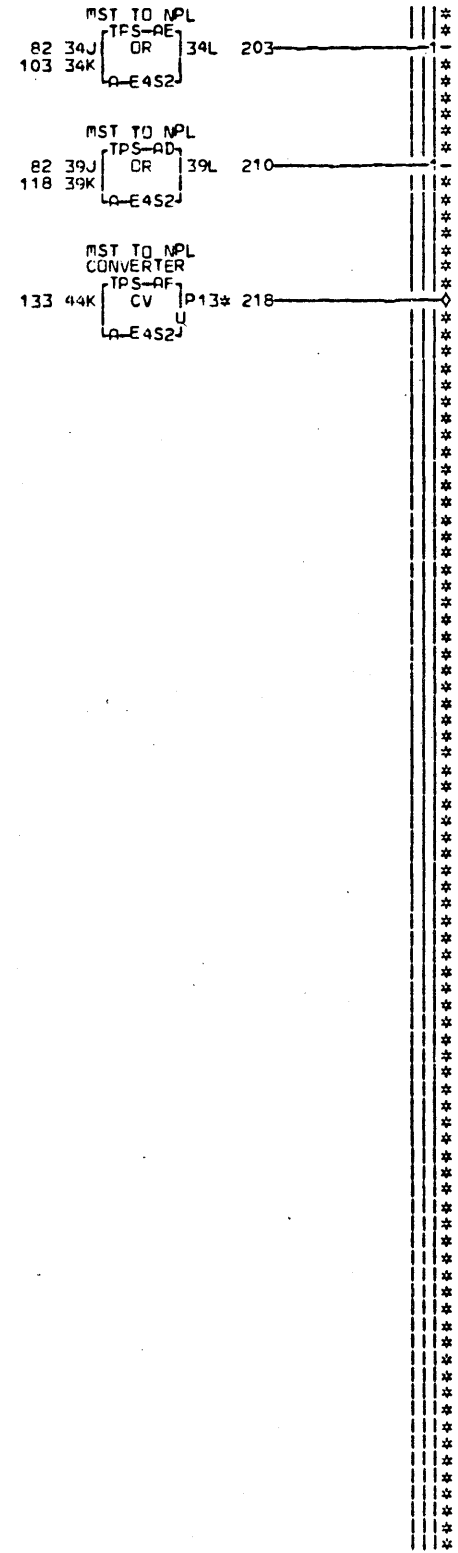
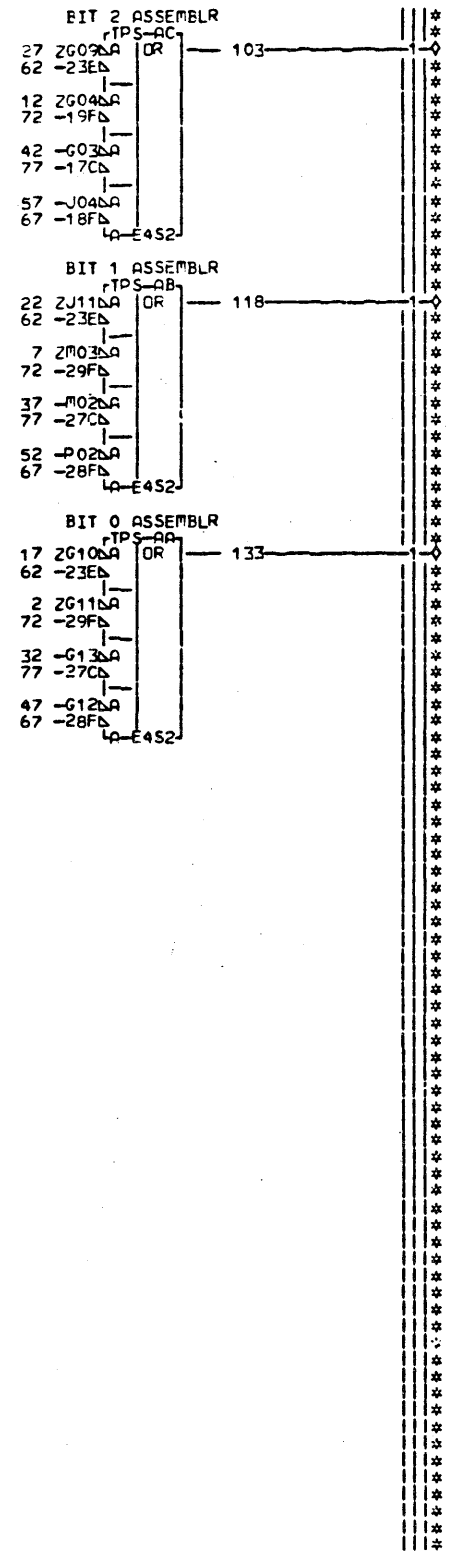
104 + INTF B NSC ADDRESS VALID — GN2  
LPB104

NOTE 1 SEE PAGE PA050 NOTES FOR INTF B NSC CHANNEL ADDRESS JUMPERING

LDC. TYPE  
A-E4R2 6837

INTF B NSC ADDRESS VALID	
E-C-HISTORY	C-MACH-27RNB
FRAME	01
DATE LAST EC	IBM CORP. SDD PR106
02-23-76 314402	P.N. 1755095 000

- LOCAL STORE BIT 0.0 TO DRVR—FB106DC4— 2—1—1
- LOCAL STORE BIT 0.1 TO DRVR—PB106DD4— 7—1—1
- LOCAL STORE BIT 0.2 TO DRVR—FB106DE4— 12—1—1
- SID ADR BIT 1.0—FD106BC6— 17—1—1
- SID ADR BIT 1.1—PD106BD6— 22—1—1
- SID ADR BIT 1.2—PD106BF6— 27—1—1
- LOCAL STORE BIT 1.0 TO DRVR—PD107DC6— 32—1—1
- LOCAL STORE BIT 1.1 TO DRVR—PD107DD6— 37—1—1
- LOCAL STORE BIT 1.2 TO DRVR—PD107DF6— 42—1—1
- ATTN STATUS TO DRIVER—PG101FC6— 47—1—1
- STAT MOD STATUS TO DRIVER—PG101FD6— 52—1—1
- CU END STATUS TO DRIVER—PG101FE6— 57—1—1
- GATE ADDRESS TO CHANNEL—PS106CF6— 62—1—1
- GATE SENSE OR STATUS TO CHAN—PS106CG6— 67—1—1
- GATE DATA BYTE 1 TO CHANNEL—PS106CH6— 72—1—1
- GATE DATA BYTE 2 TO CHANNEL—PS106CJ6— 77—1—1
- + GEN CTRL UNIT BUSY STATUS—PS106GN2— 82—1—1



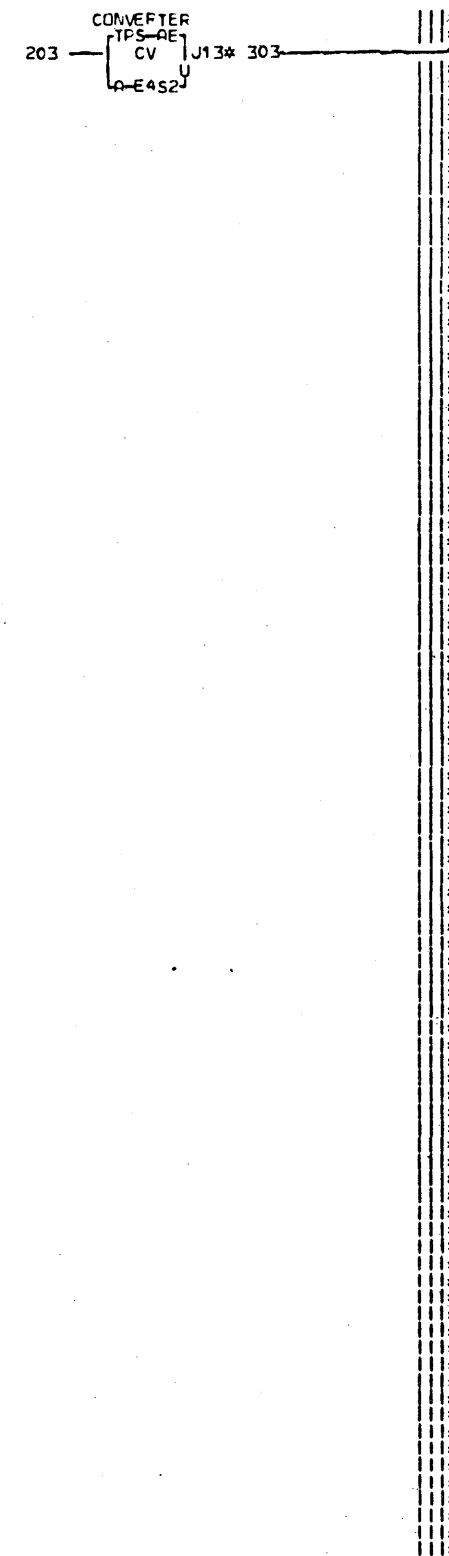
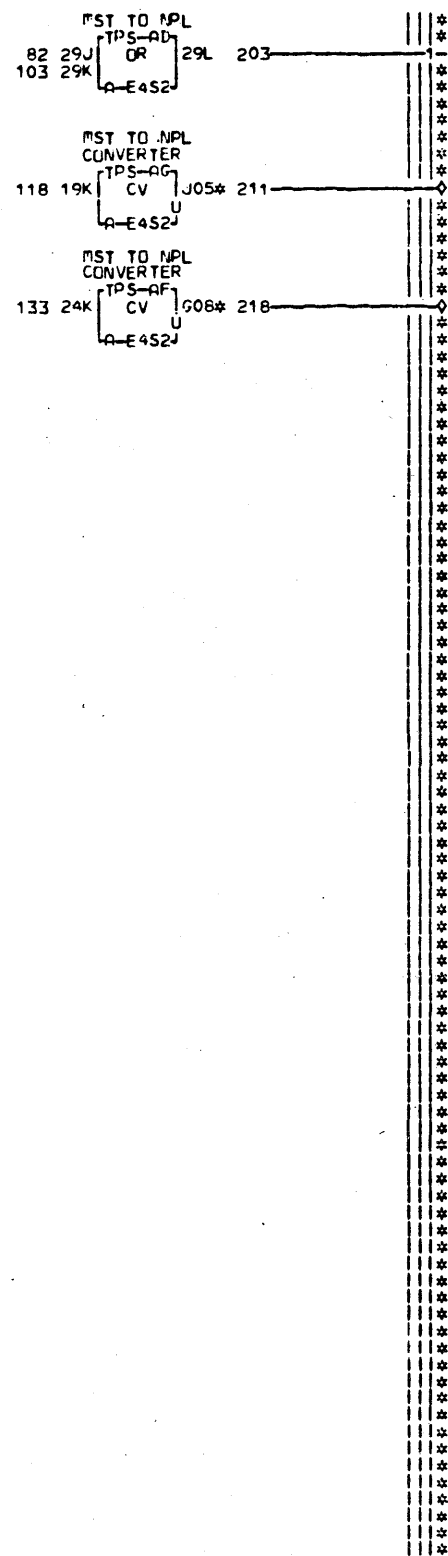
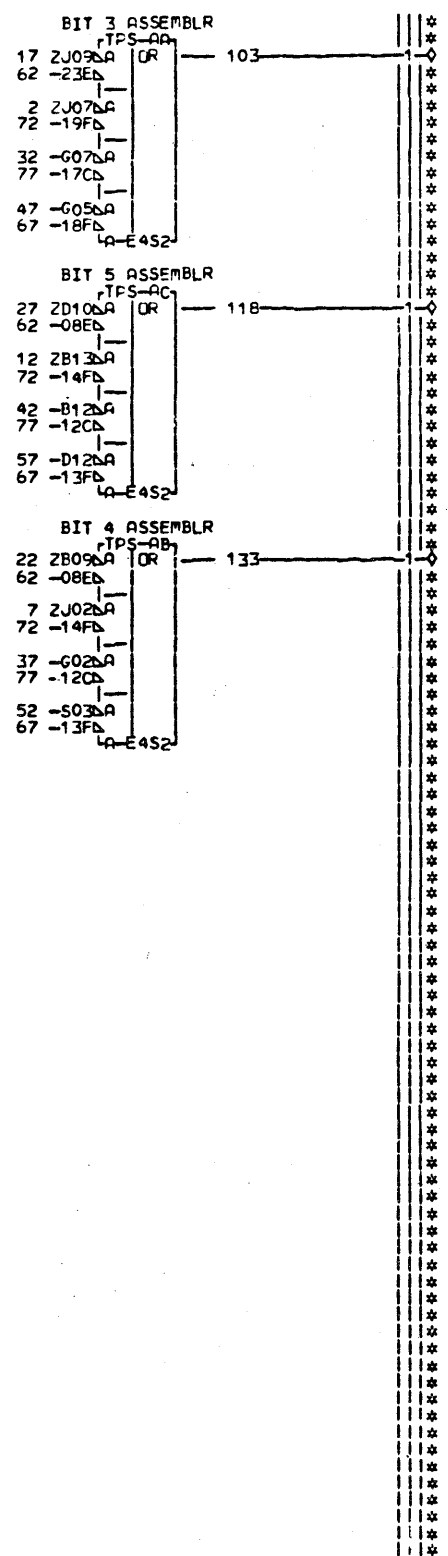
- 133 + BUS IN BIT 0 ASSEMBLED— PS103-BB4
- 118 + BUS IN BIT 1 ASSEMBLED— PS103-BF4
- 103 + BUS IN BIT 2 ASSEMBLED— PS103-BK4
- 218 + MPL BIT 0 TO INTF B— PA016-EC4
- 310 + MPL BIT 1 TO INTF B— PA016-EG4
- 303 + MPL BIT 2 TO INTF B— PA016-EL4

EDGE CONN.  
218 A-E4U3D05  
303 A-F4U3D06  
310 A-F4U3R05

LOC. TYPE  
A-E4S2 CE27

CHANNEL DRIVERS BUS IN	
BITS 0 1 2	
F.C. HISTORY	MACH. 27RNB
314402	
DATE	LAST EC
11-19-76	316677
FRAME	01
IBM CORP. SDD	PS101
P.N. 1755096	000

- LOCAL STORE BIT 0.3 TO DRV—FB106DF4— 2-1
- LOCAL STORE BIT 0.4 TO DRV—PB106DG4— 7-1
- LOCAL STORE BIT 0.5 TO DRV—PB106DH4— 12-1
- SID ADR BIT 1.3—FD106BG6— 17-1
- SID ADR BIT 1.4—PD106BH6— 22-1
- SID ADR BIT 1.5—PD106BK6— 27-1
- LOCAL STORE BIT 1.3 TO DRV—PD107DG6— 32-1
- LOCAL STORE BIT 1.4 TO DRV—PD107DH6— 37-1
- LOCAL STORE BIT 1.5 TO DRV—PD107DK6— 42-1
- BUSY STATUS TO DRIVER—PG101EF6— 47-1
- CHAN END STATUS TO DRIVER—PG101FG6— 52-1
- DEVICE END STATUS TO DRIVER—PG101FH6— 57-1
- GATE ADDRESS TO CHANNEL—PS106CF6— 62-3
- GATE SENSE OR STATUS TO CHAN—PS106CG6— 67-3
- GATE DATA BYTE 1 TO CHANNEL—PS106CH6— 72-3
- GATE DATA BYTE 2 TO CHANNEL—PS106CJ6— 77-3
- + GEN CTRL UNIT BUSY STATUS—PS106GN2— 82-1



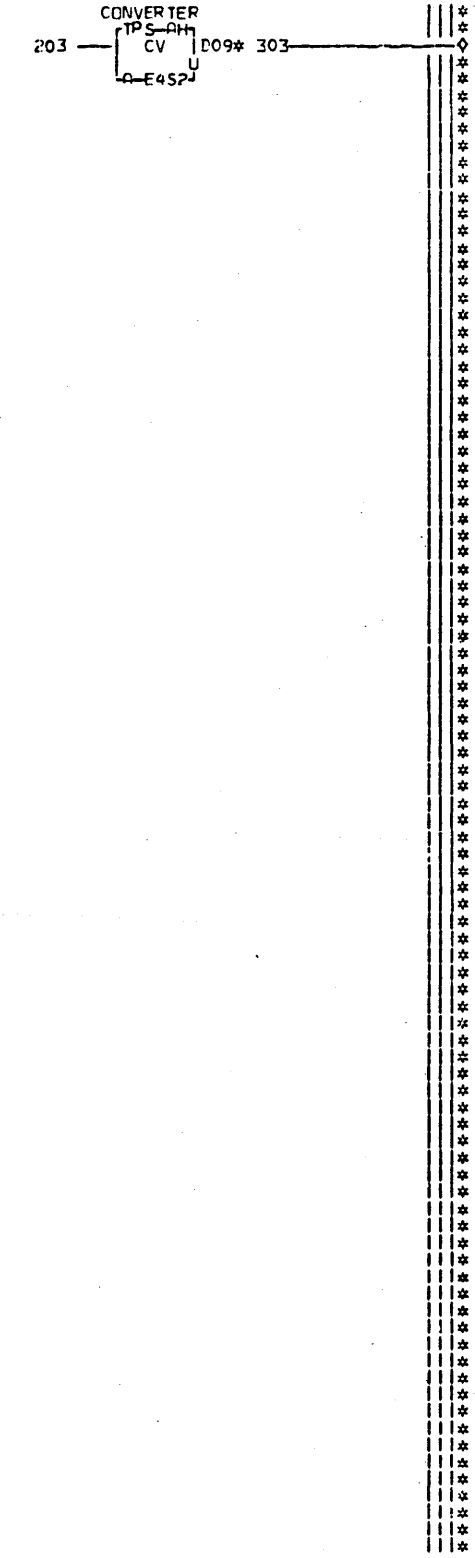
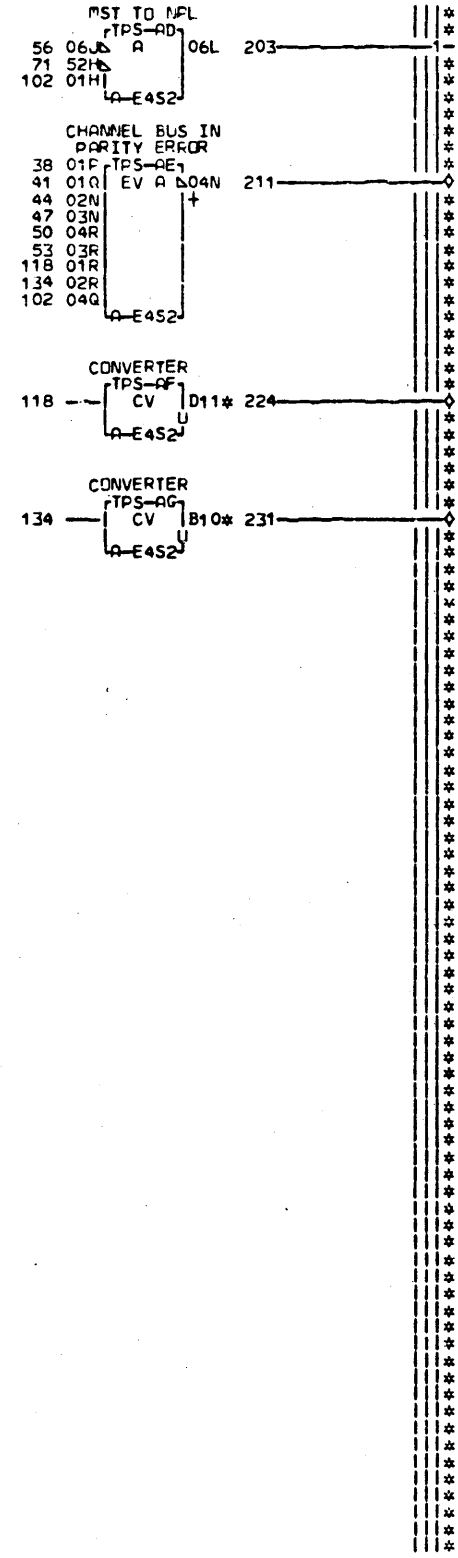
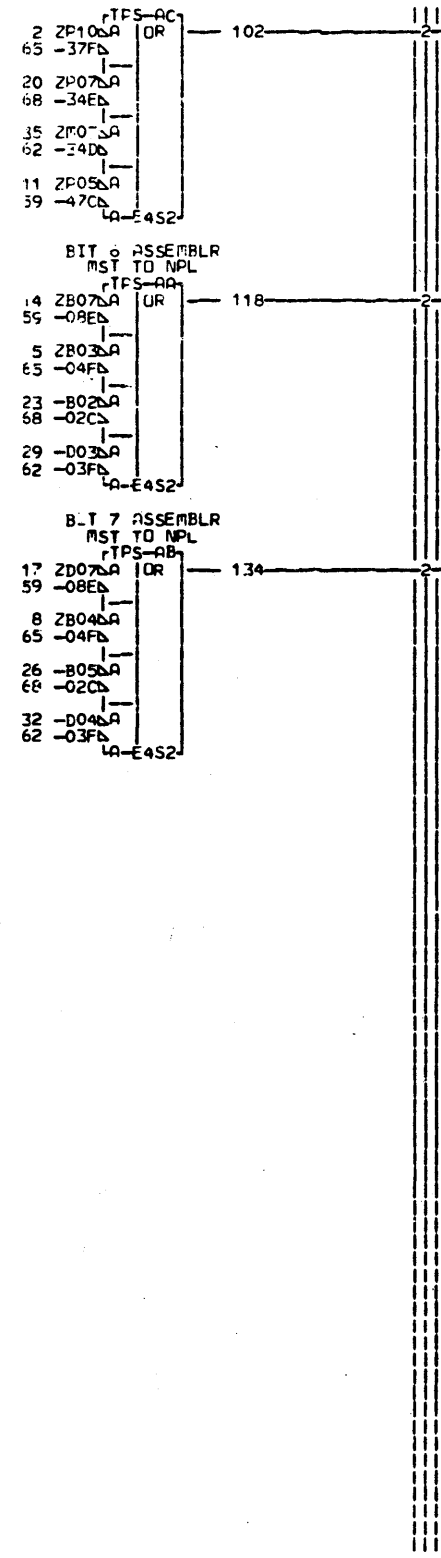
- 103 + BUS IN BIT 3 ASSEMBLER— PS103-BB4
- 133 + BUS IN BIT 4 ASSEMBLED— PS103-BF4
- 118 + BUS IN BIT 5 ASSEMBLED— PS103-BK4
- 303 + NPL BIT 3 TO INTF B— PA016-EC4
- 218 + NPL BIT 4 TO INTF B— PA016-EG4
- 211 + NPL BIT 5 TO INTF B— PA016-EL4

EDGE CONN.  
 211 A-E4U3B10  
 218 A-F4U3N09  
 01A-F4U3N10  
 303 A-F4U3B08  
 01A-E4U3B09

LOC. TYPE  
 A-E4S2 CE27

CHANNEL DRIVERS BUS IN			
BITS 3 4 5			
F.C. HISTORY—314402		MACH. 27RNB	
FRAME 01		IBM CORP. SDD PS102	
DATE LAST EC 11-19-76 316677		P.No. 1755097 000	

- LS BIT P TO DRV R INBUS - PB106LB4- 2-
- LOCAL STORE BIT 0.5 TO DRV R - PB106DJ4- 5-
- LOCAL STORE BIT 0.7 TO DRV R - PB106DK4- 8-
- SID ADR PTY 1.0-1.7 - PD105AB6- 11-
- SID ADR BIT 1.6 - PD106BL6- 14-
- SIU ADR BIT 1.7 - PD106BM6- 17-
- LOCAL STORE BIT 1.0 TO DRV R - PD107DL3- 20-
- LOCAL STORE BIT 1.6 TO DRV R - PD107DL6- 23-
- LOCAL STORE BIT 1.7 TO DRV R - PD107DM6- 26-
- UNIT CHECK STATUS TO DRIVER - PG101FJ6- 29-
- UNIT EXCEPT STATUS TO DRIVER - PG101FK6- 32-
- PARITY STATUS TO DRIVER - PG101GM4- 35-
- + BUS IN BIT 0 ASSEMBLED - PS101BB4- 38-
- + BUS IN BIT 1 ASSEMBLED - PS101BF4- 41-
- + BUS IN BIT 2 ASSEMBLED - PS101BK4- 44-
- + BUS IN BIT 3 ASSEMBLER - PS102BB4- 47-
- + BUS IN BIT 4 ASSEMBLED - PS102BF4- 50-
- + BUS IN BIT 5 ASSEMBLED - PS102BK4- 53-
- INTERFACE ENABLED AND OP IN - PS106BK6- 56-
- GATE ADDRESS TO CHANNEL - PS106CF6- 59-
- GATE SENSE OR STATUS TO CHAN - PS106CG6- 62-
- GATE DATA BYTE 1 TO CHANNEL - PS106CH6- 65-
- GATE DATA BYTE 2 TO CHANNEL - PS106CJ6- 68-
- + GEN CTRL UNIT BUSY STATUS - PS106GN2- 71-



- 211 - CHANNEL BUS IN PARITY ERROR - DH2 PS107
- 224 + MPL BIT 6 TO INTF B - PA016-EC4
- 231 + MPL BIT 7 TO INTF B - PA016-EG4
- 303 + MPL BIT P TO INTF B - PA016-EL4

EDGF CONN.  
 224 A-E4U3D11  
 231 A-F4U3R12  
 303 A-E4U3R02

LDC. TYPE  
 A-E452 CE27

CHANNEL DRIVERS BUS IN	
BITS 6 7 P	
F.C. HISTORY - 314402	D. MACH. 27RNR
FRAME 01	
IBM CORP. SUD	PS103
DATE 11-19-76	LAST FC 316677
P.N. 1755098	000

- ADDRESS IN LT — PC103AA6 — 2-1

- SERVICE IN OUT — PQ102GE6 — 12-1

- INTERFACE ENABLED AND OP IN — PS106BK6 — 22-3

- TAG DELAY GATE — PS106GM6 — 32-2

MST TO NPL  
TPS-AB  
12 ZM04 A 31L 103  
22 -16J  
32 -54L  
A-E452J

MST TO NPL  
TPS-AA  
2 ZJ03 A 16L 110  
22 -16J  
32 -54L  
A-E452J

MST TO NPL  
CONVERTER  
TPS-AC  
22 21J CV J10# 118  
A-E452J

CONVERTER  
TPS-AE  
CV P06# 203  
A-E452J

CONVERTER  
TPS-AD  
CV J06# 210  
A-E452J

118 + NPL OP IN TO INTF B — PA017-EC4

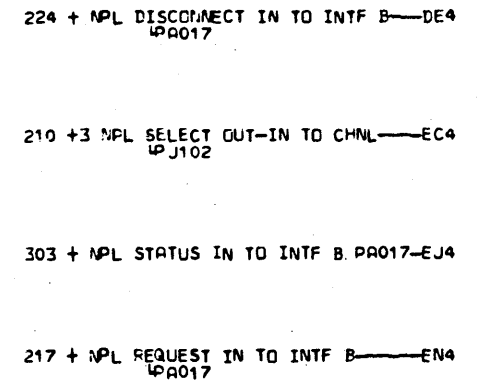
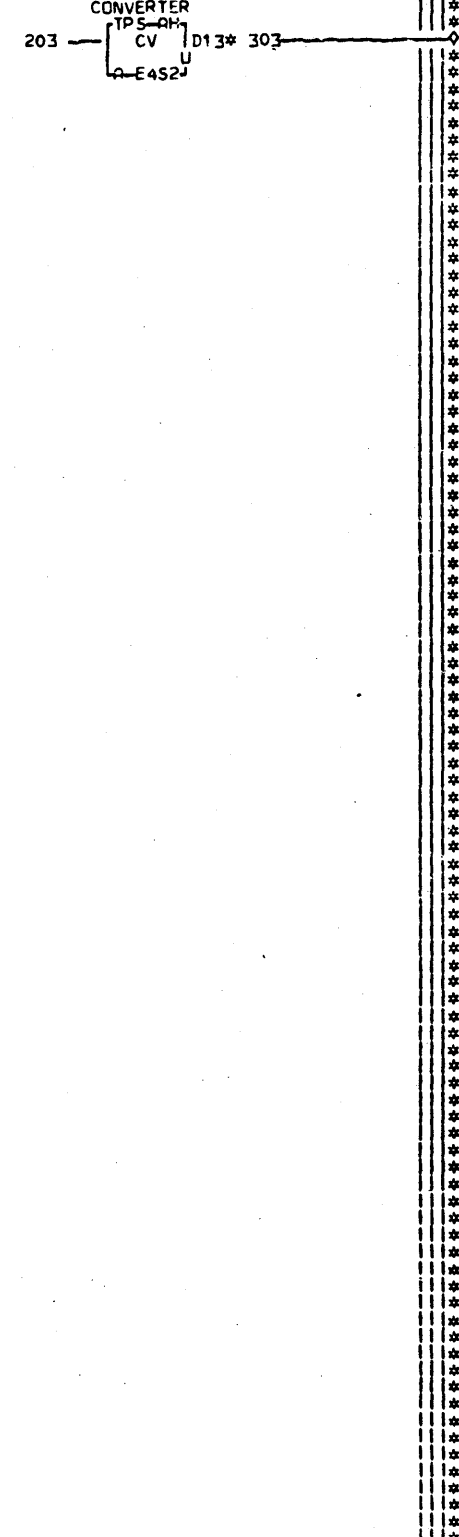
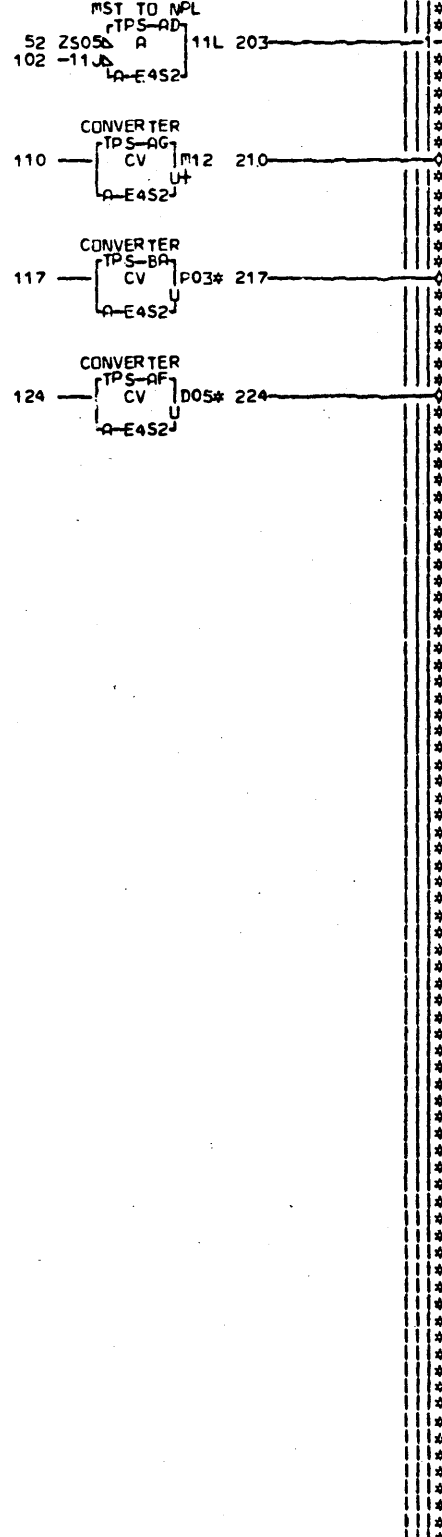
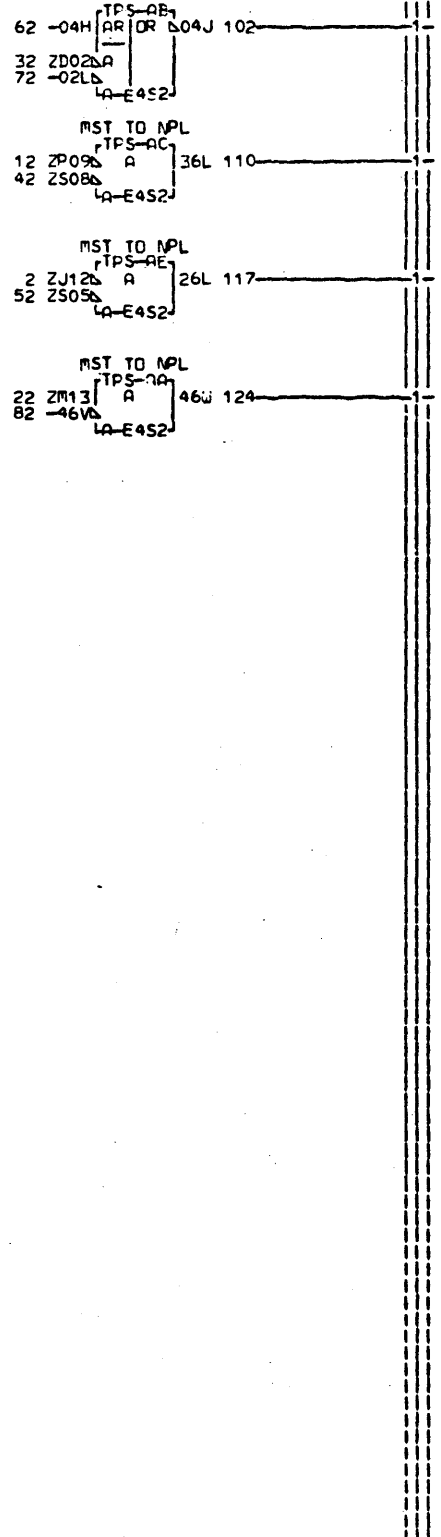
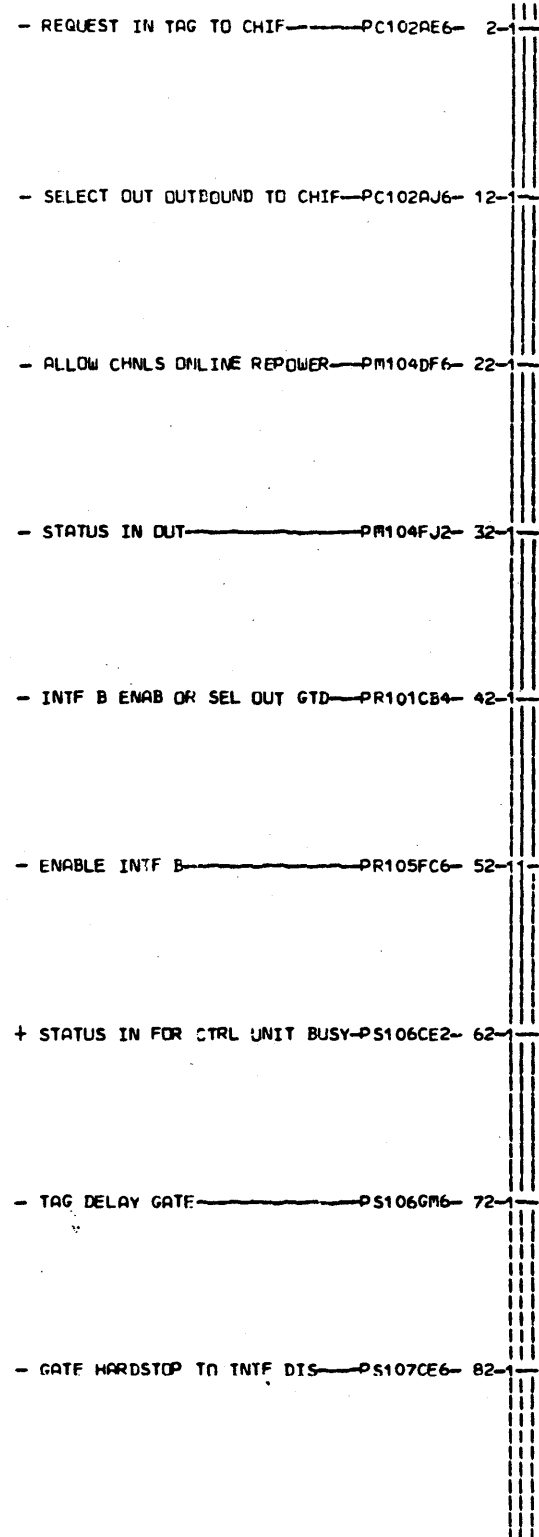
210 + NPL ADDRESS IN TO INTF B — EG4  
PA017

203 + NPL SERVICE IN TO INTF B — EL4  
PA017

EDGE CONN.  
118 A-E4U4B02  
203 A-E4U4D06  
210 A-E4U4B05

LOC. TYPE  
A-E452 CE27

CHANNEL DRIVERS	
OP IN ADDR IN AND SERVICE IN	
E.C. HISTORY	D MACH. 27RNB
314402	
DATE	LAST EC
11-19-76	316677
FRAME	01
IBM CORP. SDD	PS104
P.N. 1755099	000

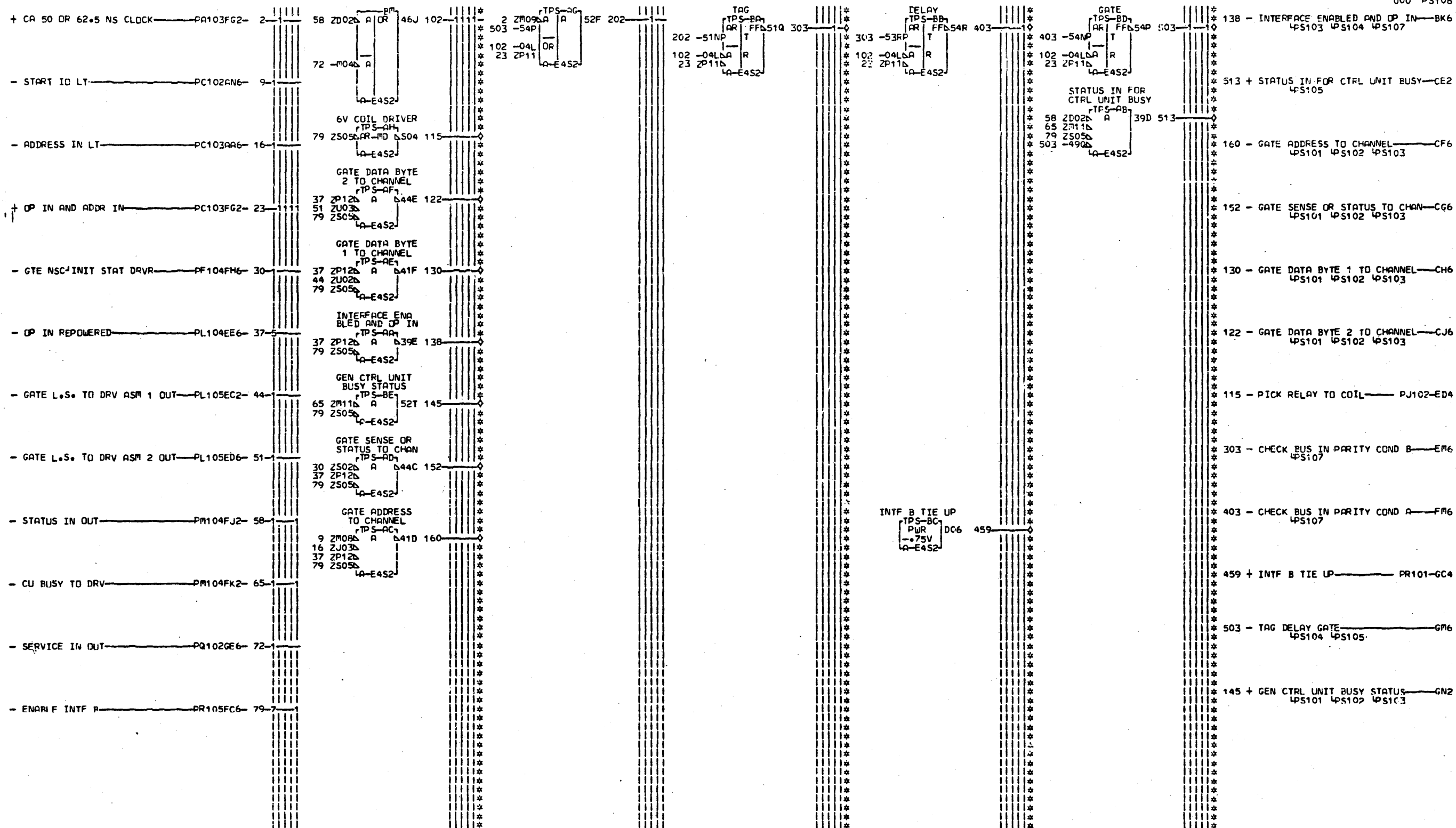


EDGE CMM.  
 217 A-E4U5D06  
 224 A-F4U5N11  
 303 A-F4U4N05

IDC. TYPE  
 A-E4S2 CE27

PS105  
 000

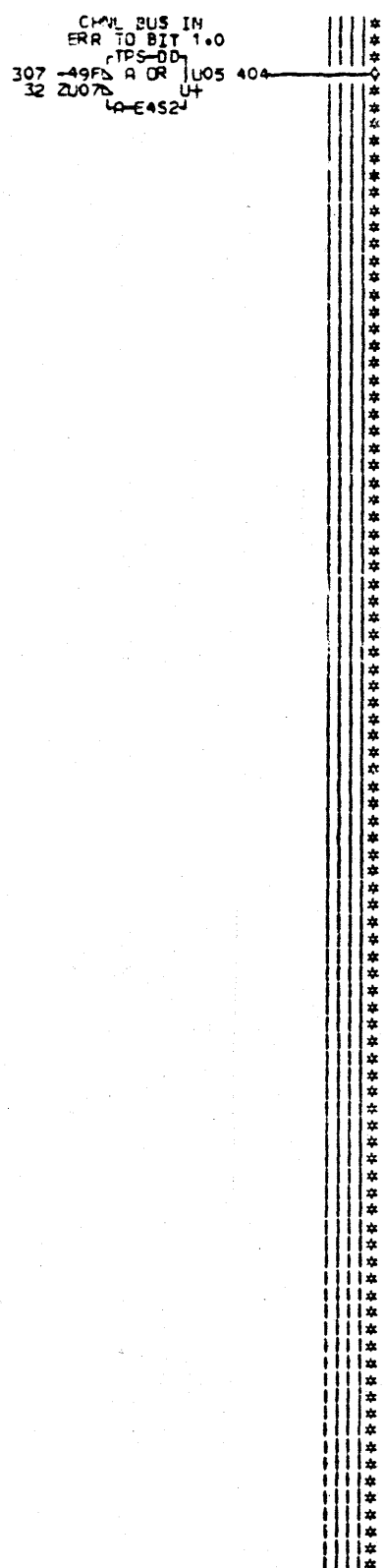
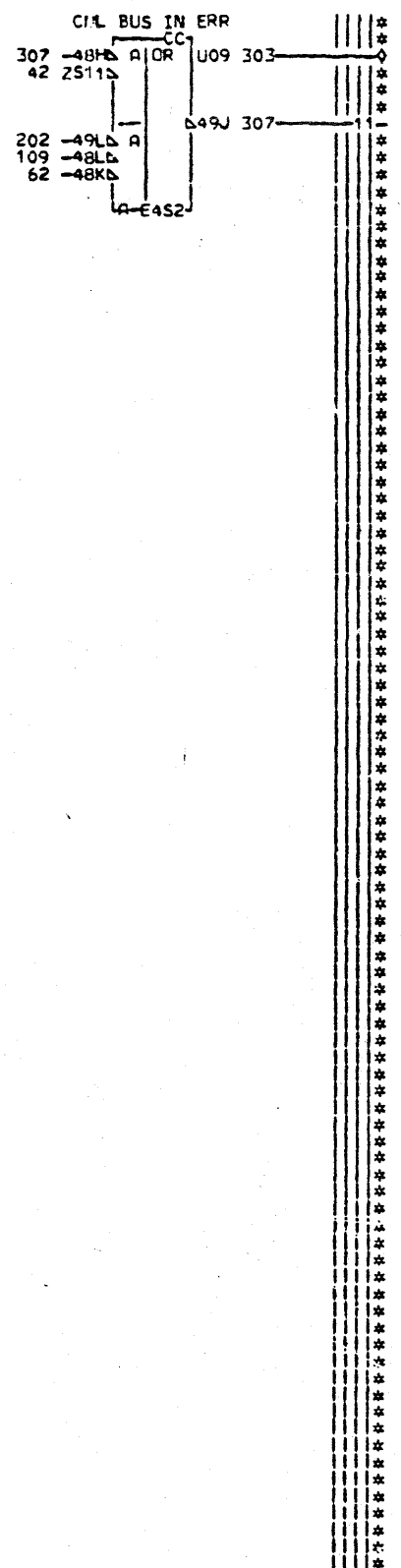
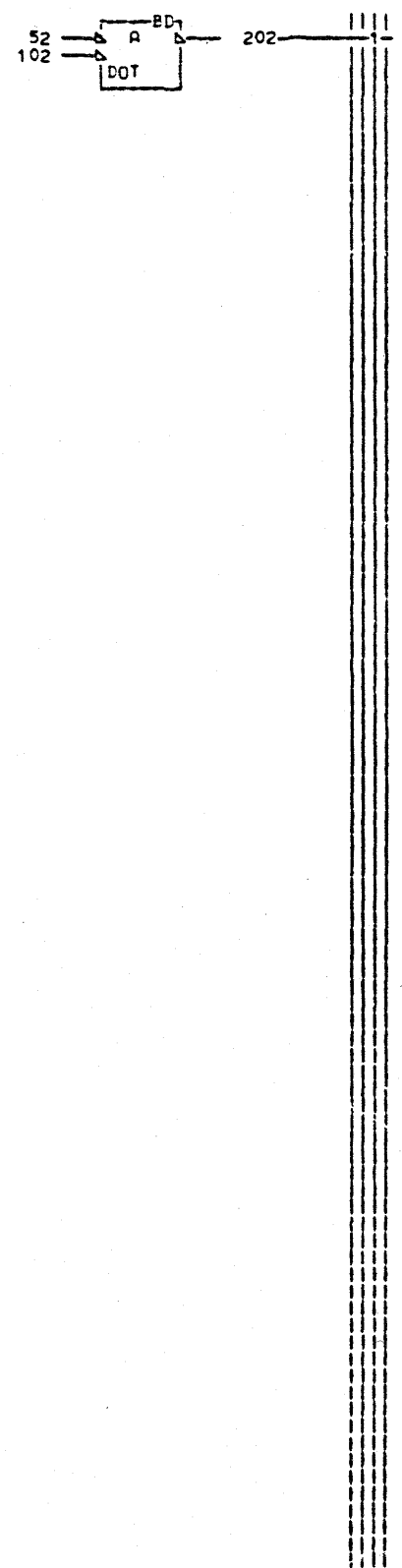
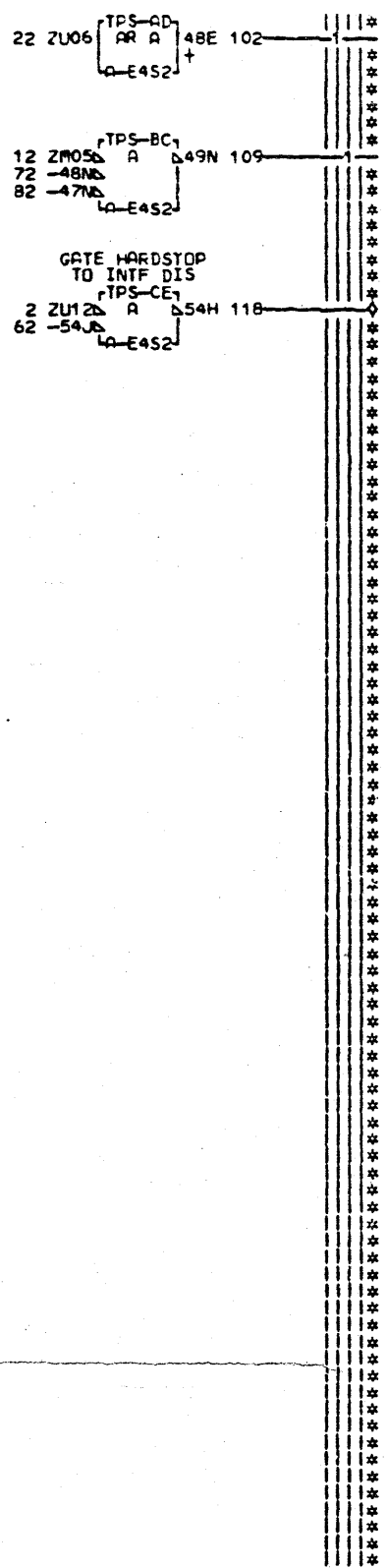
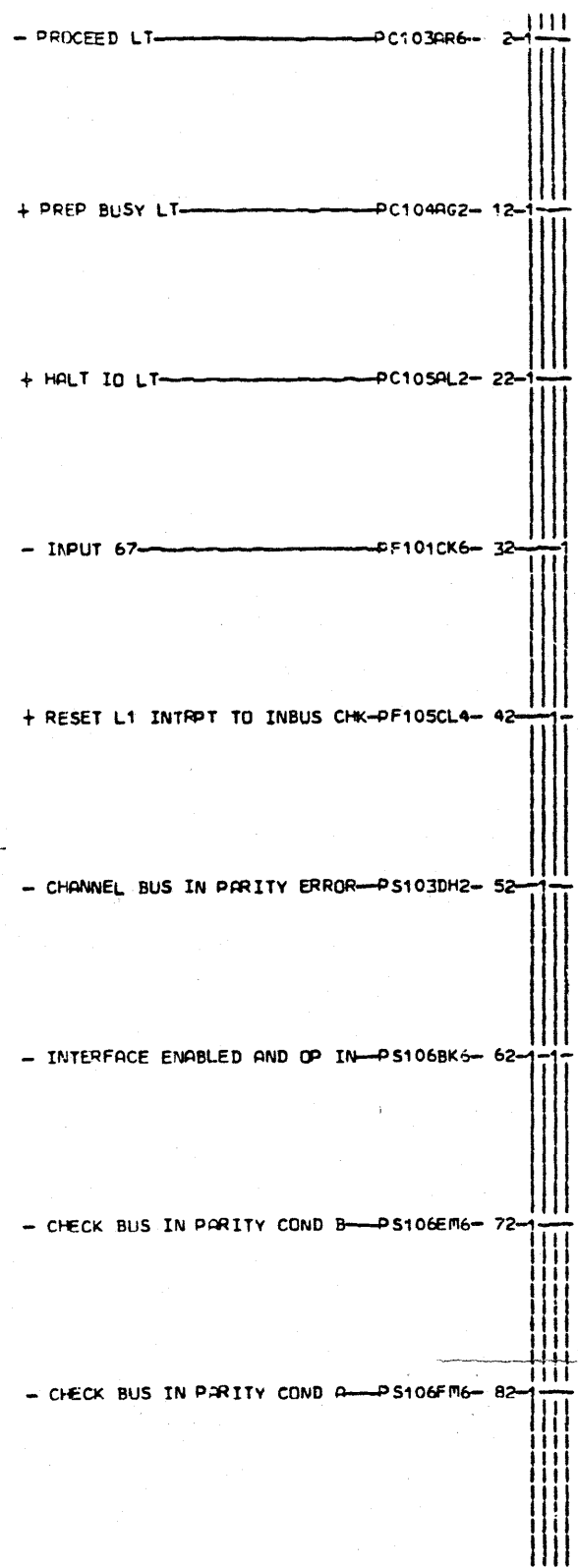
CHANNEL DRIVERS	
SELECT IN REQ IN AND STATUS IN	
F.C.—HISTORY—D.MACH.—27RNB	
314402	FRAME 01
DATE LAST EC	IRM CORP.—SDD PS105
11-19-76 316677	P.N. 1755100 000



IOC TYPE  
A-E4S2 CE27

CHANNEL SELECT OUT RELAY DRIVER AND CONTROL GATING			
E.C. HISTORY MACH. 27RNR			
314402			
DATE		LAST FC	
11-19-76		316677	
FRAME		01	
IBM CORP. SDD		PS106	
P.N. 1755101		000	





LOC. TYPE  
A-E4S2 CE27

BUS IN ERROR LATCH AND RESET GENERATION	
-E.C.-HISTORY-314402	-D-MACH-27RNB
FRAME 01	PS107
DATE LAST EC 11-19-76 316677	P.N. 1755102 000